

## Supplementary Materials

### High open-circuit voltage in single-crystalline n-type SnS/MoO<sub>3</sub> photovoltaics

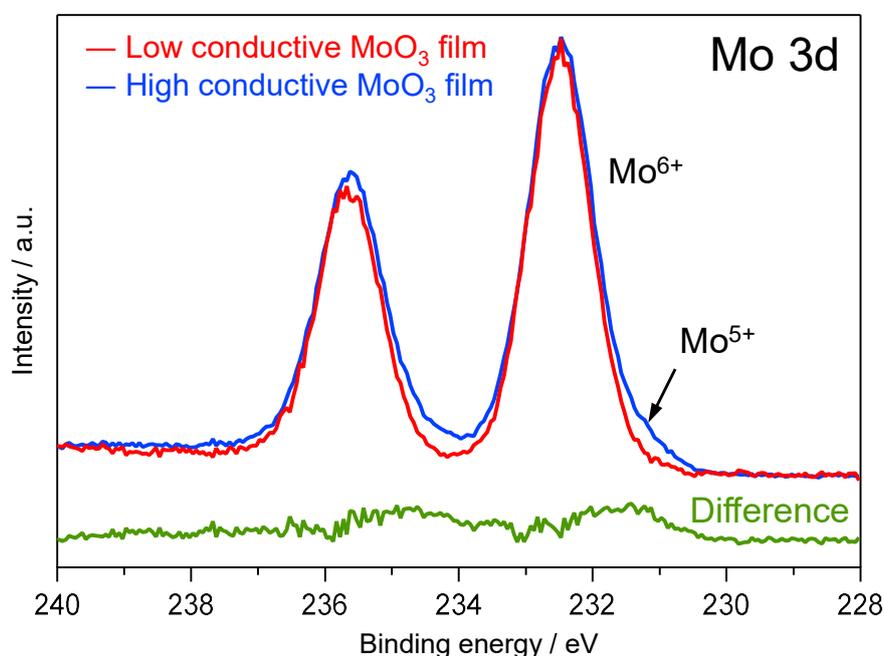
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## Supplementary Note.1

### Valence state of Mo in MoO<sub>3</sub> thin films

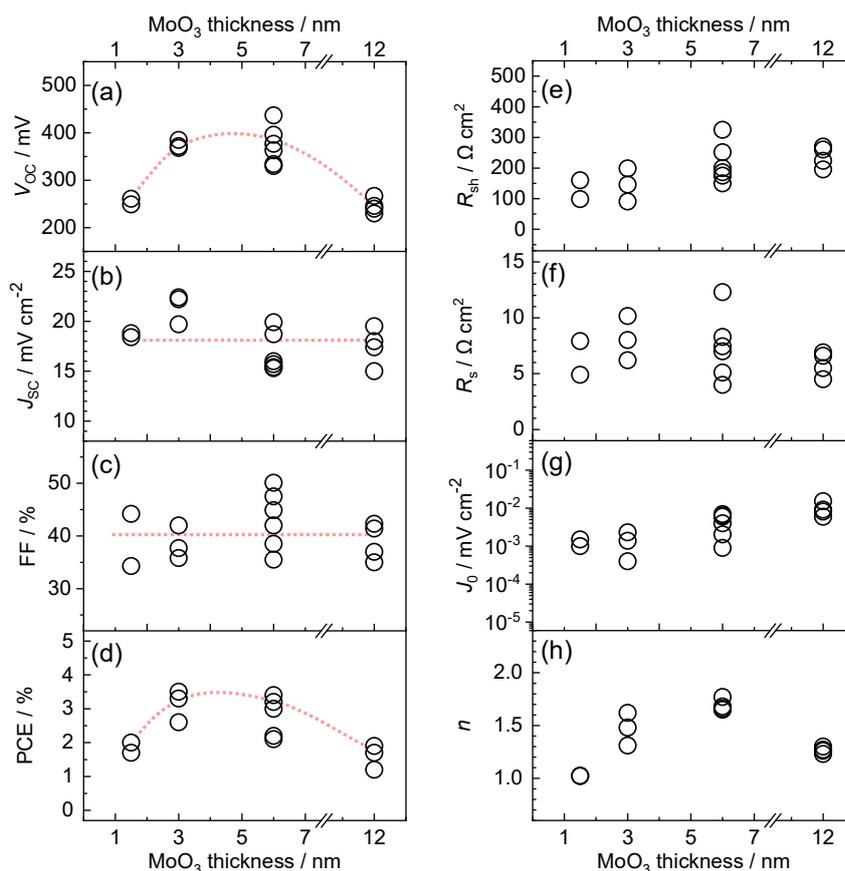


**FIG. S1.** XPS spectra of the surfaces of low- and high-conductivity MoO<sub>3</sub> thin films (red and blue lines, respectively), which were deposited onto glass substrates. The green line shows the difference. The spectra were aligned with the main peaks at 232.5 eV. Only the spectrum of the high-conductivity MoO<sub>3</sub> layer exhibited a shoulder band at a lower binding energy. This may be owing to the introduction of oxygen vacancies resulting in the partial reduction of Mo<sup>6+</sup> to Mo<sup>5+</sup>. The chemical shift of Mo<sup>6+</sup> and Mo<sup>5+</sup> is reported to be ~1 eV<sup>[S1]</sup>, which is consistent with the observed energy difference between the main and shoulder peaks (green line). Notably, the thin films were exposed to air prior to the XPS measurement; therefore, the spectra may be partially affected by surface contamination and/or possible reaction with air.

## Supplementary Note.2

### MoO<sub>3</sub> thickness dependence of the photovoltaic properties

The effects of MoO<sub>3</sub> thickness on the photovoltaic properties and device parameters are shown in Fig. S2, and those of the device with the highest *PCE* are summarized in Table S1. High *V*<sub>OC</sub> values were obtained when the MoO<sub>3</sub> layer thickness was 3 and 6 nm, and *V*<sub>OC</sub> decreased when the MoO<sub>3</sub> layer thickness was 1.5 and 12 nm (Fig. S2(a)). It is assumed that the 1.5-nm-thick MoO<sub>3</sub> layer is too thin to form an inadequate interface, and thus the *V*<sub>OC</sub> becomes lower, considering that the band bending at the interface of n-type SnS with MoO<sub>3</sub> becomes complete when the MoO<sub>3</sub> layer is more than ~3 nm<sup>[S1]</sup>. The devices with a 12-nm-thick MoO<sub>3</sub> layer exhibited a reverse saturation current (*J*<sub>0</sub>), which was larger than those of devices fabricated with other MoO<sub>3</sub> layer thicknesses (Fig. S2(g)). This indicates a high recombination rate at the interface, suggesting that a thicker MoO<sub>3</sub> layer does not allow efficient carrier separation, resulting in a lower *V*<sub>OC</sub>. Moreover, *J*<sub>sc</sub> and *FF* exhibited no clear relationship with MoO<sub>3</sub> thickness (Fig. S2(b,c)). Thus, the MoO<sub>3</sub> layer thickness dependence of *PCE* reflects that of *V*<sub>OC</sub>. Thus, the 3- and 6-nm-thick MoO<sub>3</sub> layers are favorable for both *V*<sub>OC</sub> and *PCE* (Fig. S2(a,d)).



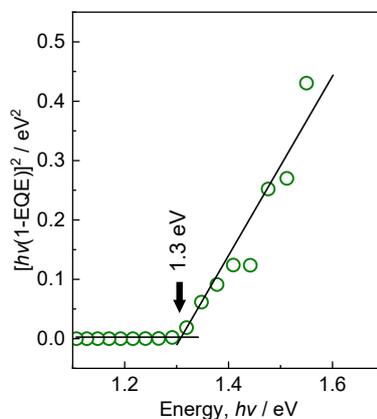
**FIG. S2.** Photovoltaic performance of n-type SnS single crystal/MoO<sub>3</sub> junction solar cells as a function of MoO<sub>3</sub> thickness. The MoO<sub>3</sub> layer with a low conductivity ( $\sigma_{\text{MoO}_3} = 1.7 \times 10^{-8} \text{ Scm}^{-1}$ ) was used. (a) *V*<sub>OC</sub>, (b) *J*<sub>sc</sub>, (c) *FF*, (d) *PCE*, (e) *R*<sub>sh</sub>, (f) *R*<sub>s</sub>, (g) *J*<sub>0</sub>, and (h) *n*. Dotted lines are only for eye guides.

**Table S1.** Photovoltaic characteristics of representative solar cells with n-type SnS single crystal/MoO<sub>3</sub> junctions with different MoO<sub>3</sub> layer thicknesses and conductivities.

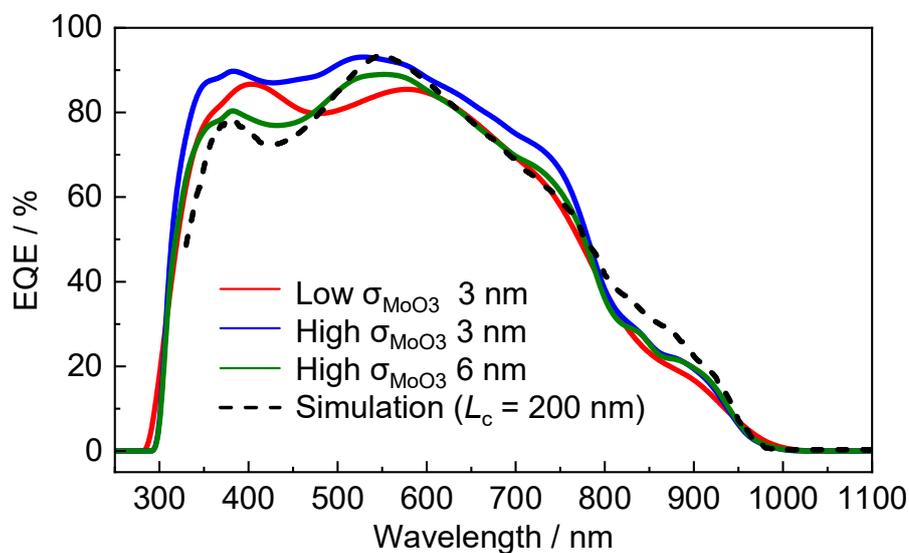
MoO <sub>3</sub> layer conductivity	MoO <sub>3</sub> layer thickness	<i>PCE</i> / %	<i>V</i> <sub>OC</sub> / mV	<i>J</i> <sub>SC</sub> / mA cm <sup>-2</sup>	FF / %	<i>R</i> <sub>sh</sub> / Ω cm <sup>2</sup>	<i>R</i> <sub>s</sub> / Ω cm <sup>2</sup>	<i>J</i> <sub>0</sub> / mA cm <sup>-2</sup>	<i>n</i>
Low $\sigma_{\text{MoO}_3}$ ( $1.7 \times 10^{-8}$ S cm <sup>-1</sup> )	1.5 nm	2.0	249	18.4	44.2	160	4.9	$1.5 \times 10^{-3}$	1.02
	3 nm	3.3	385	22.4	37.7	146	8.0	$2.3 \times 10^{-3}$	1.48
	6 nm	3.4	437	15.7	50.1	200	7.5	$9.0 \times 10^{-4}$	1.77
	12 nm	1.9	246	19.1	42.3	270	4.5	$9.2 \times 10^{-3}$	1.26

### Supplementary Note.3

#### EQE spectra



**FIG. S3.** Bandgap of the absorber layer determined by the plot of  $[hv(1-EQE)]^2$  against  $hv$ . The device is fabricated using 6-nm-thick  $\text{MoO}_3$  thin films with a high  $\sigma_{\text{MoO}_3}$ .



**FIG. S4.** EQE spectra of the solar cells with n-type SnS single crystal/ $\text{MoO}_3$  film junctions with different  $\text{MoO}_3$  layer thicknesses and conductivities. The simulated spectrum is obtained by EQE using the e-ARC code.

#### REFERENCES

[S1] I. Suzuki, B. Huang, S. Kawanishi, T. Omata, A. Klein, Avoiding Fermi Level Pinning at the SnS Interface for High Open-Circuit Voltage, *J. Phys. Chem. C*. 126 (2022) 20570–20576.