

# Nanocarbon Devices and Sensors

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Von

M.Sc. Sandeep Kumar aus Bokaro in India

Referrant: Prof. Dr. Ralph Krupke

Koreferrant: Prof. Dr. Jan Philipp Hofmann

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This thesis is dedicated to my beloved family

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# **Abstract**

Nanocarbon materials have the potential to substitute the silicon in devices that are needed to further improve in terms of scalability, speed of operation, and performance. Therefore, carbon nanotubes and graphene - allotropes with outstanding properties have been intensively explored over the years. This thesis contributes to the synthesis of nanocarbon materials and its use as sensing and transistor material by focusing on the topics: hysteresis in carbon nanotube field-effect transistors, selective molecule sensing with graphene field-effect transistors, and sensing applications in nanocrystalline graphene. Hysteresis in carbon nanotube transistors has limited its utility in large-scale device implementation. The issue of hysteresis in such device structures is addressed. A hysteresis-free device operation is achieved by packaging the carbon nanotube field-effect transistors between hexagonal boron nitride and a hydrophobic polymer Teflon. The findings indicate that hysteresis is eradicated only if the metal-carbon nanotube contacts along with the tubes are completely encapsulated with Teflon. The time dependence of reducing hysteresis for encapsulated devices indicates out-diffusion of water molecules adsorbed at the metal-nanotube contacts. Graphene field-effect transistors suffer the issue of selectivity applications. A novel sensor based on graphene field-effect transistor and surface-mounted metal-organic frameworks is demonstrated. The sensor shows sensitivity and selectivity to ethanol molecules by the shift in Dirac voltage of graphene and is insensitive to other alcohols like methanol and isopropanol, and molecules like CO<sub>2</sub>, H<sub>2</sub>O, H<sub>2</sub>. The device performance shows a detection limit of 100 ppm levels. This class of sensors is tailorable and opens up a completely new range of sensors. Nanocrystalline graphene is an interesting material for several sensing applications like strain sensing, moisture sensing, gas sensing, etc. In order to be investigated as strain sensor, thin films should be either grown or transferred on flexible substrates. This motivated the study of the lowtemperature synthesis (600°C) process using a metal capping layer over a carbon source. Raman spectroscopy is used to characterize the grown films. The results indicate this technique promising for a low-temperature NCG synthesis. Next, thin-film transfer technique on flexible substrate is studied. The quality of transferred films on different substrates is confirmed by atomic force microscopy. Next, influence of temperature on conductivity of thin films of NCG is investigated. Piezoresistive property of nanocrystalline graphene is explored based on changes in sheet resistance of the film and Raman spectroscopy. Finally, a potential application is demonstrated where a top (ionic liquid) gate field effect configuration of NCG works as a moisture sensor.

Declaration	
I, hereby declare that, this thesis is an outcome of a collaborations are acknowledged. I certify that this thesis elsewhere. Additionally, care has been taken to ensure the material included in the thesis previously published is cited of	has not been submitted at the work is original and
 Date	Signature

# List of conference and publications

The following research articles and conference contributions includes research performed by author during his course of study.

#### **Research Articles:**

- Sandeep Kumar, Daghan Dagli, Simone Dehm, Chittaranjan Das, Li Wei, Yuan Chen, Frank Hennrich and Ralph Krupke. Vanishing hysteresis in carbon nanotube transistors embedded in boron nitride/polytetrafluoroethylene heterolayers. *Phys. status solidi – Rapid Res. Lett.* 2020, 14, 2000193.
- Sandeep Kumar, Yohanes Pramudya, Kai Müller, Abhinav Chandresh, Simone Dehm, Shahriar Heidrich, Artem Fediai, Devang Parmar, Delwin Perera, Manuel Rommel, Lars Heinke, Wolfgang Wenzel, Christof Wöll and Ralph Krupke. Sensing molecules with metal-organic framework functionalised graphene transistors. Adv. Mater. 2021, 2103316.

#### **Conferences Contributions:**

- Sandeep Kumar, Adnan Riaz, Simone Dehm and Ralph Krupke. Conversion of nanocrystalline graphene to hexagonal boron nitride. *Annual meeting of GDR-I,* Sete, France, October 15-19, 2018, Poster.
- 2. Sandeep Kumar, Kai Müller, Simone Dehm, Artem Fediai, Devang Parmar, Manuel Rommel, Lars Heinke, Wolfgang Wenzel, Christof Wöll and Ralph Krupke. Ethanol sensing with surface mounted metal-organic framework functionalised graphene transistors. *International Conference on the Science and Application of Nanotubes and Low-Dimensional Materials*, July 21 -26, 2019, Wuerzburg, Germany, Poster.
- 3. Sandeep Kumar, Kai Müller, Simone Dehm, Artem Fediai, Devang Parmar, Manuel Rommel, Lars Heinke, Wolfgang Wenzel, Christof Wöll and Ralph Krupke. Ethanol sensing with surface mounted metal-organic framework functionalised

- graphene transistors. *Annual meeting of GDR-I,* **October 27-30 2019**, Bad Herrenalb, Germany, Poster.
- 4. Sandeep Kumar, Simone Dehm, Li Wei, Yuan Chen, Frank Hennrich and Ralph Krupke. Hysteresis free carbon nanotube transistors embedded in boron nitride and polytetrafluorethylene heterlolayers. *International Winterschool on Electronic Properties of Novel Materials*, March 7-14 2020, Kirchberg in Tirol, Austria, Poster.

# **Curriculum Vitae**

## **Personal Information**

Name Sandeep Kumar

Date of Birth 04 September 1992

# **Educational Background**

09/2017 – Current Doctoral student in *Technical University* 

Department of Materials Darmstadt, Germany

and geosciences

06/2011 - 06/2016 M.Tech - B.Tech Dual Indian Institute of Technology

degree, Materials science Kanpur, India

and engineering.

# **Research Background**

05/2015 – 08/2015 Internship Applied Materials

Mumbai, India

07/2016 – 06/2017 Research Assistant National centre for flexible

electronics

IIT Kanpur, India

# 1 Introduction

## 1.1 Scope of thesis

In 1965, Gordon Moore predicted that the number of transistors on a computer chip doubles approximately every two years. However, around the year 2000, the silicon semiconductor industry started suffering due to physical limits. Hence, radical approaches were required to utilize and apply nanotechnology. This motivated the research on high mobility materials such as carbon nanotubes (CNTs) and graphene. These carbon nanomaterials have the potential to improve the semiconductor device performance in terms of operation speed, and size. Therefore, in the past two decades, interest has increased towards this class of material as can be seen by a plot of the number of publications vs year in figure 1.1.

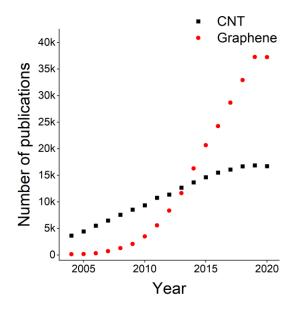


Figure 1.1: Number of publications per year in the field of CNTs and graphene. (Source: web of science)

Carbon is a chemical element with atomic number 6. With a valency of four, it can form four covalent bonds. It can also bond with itself to create different allotropes like diamond, graphite, and fullerenes. Each of the allotropes inherits distinguished properties depending on the chemical bonds formed with the neighboring carbon

atoms. For example, in diamond,  $sp^3$  hybridised carbon atoms form four covalent  $\sigma$  bonds in a tetrahedral configuration. Due to the strong  $\sigma$  bonds, it is one of the hardest materials. On the other hand, in graphite,  $sp^2$  carbon forms three  $\sigma$  bonds, and one electron is delocalized in  $\pi$  bond. Carbon arranges in flat layers of hexagonal structure, between which the extra electrons prevail, and are held together by Van der Waals forces. A monolayer of graphite is known as graphene with electron cloud residing on its surface and a continuous energy-momentum (E-K) space. Rolling up the 2D graphene film gives CNTs, a 1D system, and quantized energy-momentum (E-K) space<sup>[1]</sup>. Nanocrystalline graphene (NCG) is another form of graphene possessing large defect density in form of grain boundaries (GBs) and triple junctions (TJs) <sup>[2]</sup> and crystallite size in nanometers.

The motivation of this thesis was to improve the performance and control the functionality of nanocarbon devices by engineering the interfaces between the material and the environment, and the GBs. This gave rise to three research topics utilizing nanocarbon materials: hysteresis in CNT field-effect transistors (FETs), molecule sensing with graphene FETs, and NCG for sensing applications. These three topics have been covered in the thesis.

Semiconducting single-walled CNTs (SWCNTs) are touted as an attractive material for the semiconductor industry. With a high on/off ratio, excellent charge carrier mobility, and heat conductivity, SWCNTs are ideally suited as the channel material for FETs. The wide-ranging applications involving CNT-FETs include hydrogen sensing, light emission, and light detection<sup>[3][4][5]</sup>. Nevertheless, hysteresis in CNT-FETs is an underlying problem and is often undesirable. It hampers the FET device operation by making it difficult to achieve a steady-state current at a fixed gate voltage [6][7]. Several studies were reported in the literature to understand and limit the origin of hysteresis in CNT-FETs<sup>[8][9]</sup>. Hexagonal boron nitride (h-BN), on the other hand, has proven to be an excellent dielectric for graphene FET<sup>[10]</sup> to counter hysteresis. In this thesis, an attempt to eliminate hysteresis in CNT-FETs is explored by using a h-BN/SiO<sub>2</sub>/Si substrate. A direct growth method of h-BN on the dielectric substrate (SiO<sub>2</sub>/Si), contrary to the wellestablished technique of synthesis on the metal substrate is followed, thus avoiding the transfer step<sup>[11,12]</sup>. CNT-FETs are then fabricated on the grown h-BN on top of SiO<sub>2</sub>/Si substrate. Next, the role and significance of a top hydrophobic encapsulating layer are studied. Here, Teflon is used as the hydrophobic film to seal the CNTs from the adsorption of water molecules. Effectively, the carbon nanotubes are sandwiched between the top Teflon layer and the bottom h-BN layer. Time-dependent reduction of hysteresis after encapsulation with Teflon indicates an out-diffusion of water molecules

at the metal-CNT contacts. Finally, a hysteresis-free operation and stable device performance are demonstrated with the encapsulated CNT devices.

Electrically, graphene is a semimetal. The conduction and valence bands meet at K points where the charge carriers behave as massless fermions resulting in a ballistic transport<sup>[13]</sup>. It has  $\pi$  electrons delocalised on the surface creating a 2D electron gas (2DEG). These charge carriers can have high mobility up to 200,000 cm<sup>2</sup>V<sup>-1</sup>s<sup>-1[14]</sup> based on the device and environmental conditions<sup>[15]</sup>. High thermal conductivity in graphene up to 5,300 WmK<sup>-1</sup> is attributed to the weak interaction between electron and acoustic phonon<sup>[16]</sup>. The absence of slip planes and high C-C bond strength leads to a high breaking strength of 42 Nm<sup>-1</sup> and Young's modulus of 1 TPa [17]. Furthermore, the large surface area to volume ratio and high mobility of charge carriers<sup>[13]</sup> makes it a suitable candidate for sensing molecules. A molecular gas sensor based on a graphene fieldeffect (GFET) transistor was first demonstrated by Schedin et al.<sup>[18]</sup> Over the years several reports depicting the sensitivity of graphene gas sensors for sensing gases such as NH<sub>3</sub>, CO, O<sub>2</sub>, SO<sub>2</sub>, H<sub>2</sub>S, etc.<sup>[19–21]</sup> were reported. Alcohol detection was studied<sup>[22]</sup> as well. However, the graphene sensors lack selectivity in detecting specific molecular species. Alternatively, surface-mounted metal-organic frameworks (SURMOFs), when integrated with GFET, lead to the detection of specific molecules. In this thesis, a novel concept of sensor based on the combination of GFET and SURMOF, tailored for the detection of ethanol is exhibited. The fabrication steps of GFETs are discussed. Next, the sensitivity and selectivity to ethanol are demonstrated from the shifts in Dirac voltage of the transconductance measurement. Subsequently, a resetting scheme for fast regeneration was explored via current annealing. A detection limit of 100 ppm was achieved for the GFET/SURMOF sensor which is comparable to the commercially available sensors. The combination of SURMOF and GFET to achieve selectivity and sensitivity, respectively, can open a new class of tailorable molecule sensors.

Only a few reports (~150 in 2020) on NCG, compared to graphene and CNTs, suggest the need for further exploration of this nanocarbon material. Due to a high defect density, NCG suffers from reduced carrier mobility and mechanical strength<sup>[2]</sup>. However, despite the limitations, NCG has found applications in gas sensing<sup>[23]</sup>, strain sensing<sup>[24]</sup>, and photodetectors<sup>[25]</sup>. The primary advantage of NCG lies in its synthesis simplicity, as NCG can be grown directly on a dielectric substrate with a predefined thickness. However, the high synthesis temperature (1000°C) limits its use to only a few substrates. For strain sensing applications, thin films should be either grown directly or transferred on flexible substrates. Therefore, in this thesis, a low temperature (600°C) synthesis route is explored by utilizing the catalytic activity of the metal capping layer on the carbon source. Next, the thin-film transfer technique is discussed, and a route for achieving a

wrinkle-free film is studied. Devices are fabricated using the NCG films and are then investigated under temperature and strain by sheet resistance measurements and Raman spectroscopy. Finally, an application of NCG-FET is demonstrated where the top-gated (using ionic liquid) channel displays sensitivity towards moisture and can act as a potential moisture sensor.

### 1.2 Outline of thesis

This thesis describes the performance and applications of devices based on carbon materials. The materials studied are graphene, carbon nanotubes, and NCG. In Chapter 2, a general overview of material studied in the thesis and their properties are discussed.

In Chapter 3, experimental methods used in the thesis are explained. Gas sensing and piezoresistance setup built in-house is explained in detail.

In Chapter 4, experiments and results on achieving hysteresis-free CNT-FETs are discussed. In the first section, the h-BN synthesis technique and characterization are described. The second section includes the device fabrication and electrical characterization. Lastly, the mechanism of achieving hysteresis-free operation is explained and the performance of the fabricated device is demonstrated.

In Chapter 5, a detailed study is described on SURMOF/GFET. The fabrication process is explained in detail. Next, different characterization techniques used are described. In the last part of the chapter, the results of SURMOF/GFET device sensing with selectivity to ethanol molecules are described. Raman measurements are shown to visualize the selectivity. Device performance is then studied confirming sensitivity down to 100 ppm levels.

In Chapter 6, NCG as a material is studied. First, the advantages of using a metal encapsulant layer for a low temperature (600°C) are demonstrated. Next, the transfer technique for thin-film transfer is discussed. Finally, the characterization of NCG film is done based on temperature and strain. As an example, a prospective application of NCG-FET as a moisture sensor is demonstrated.

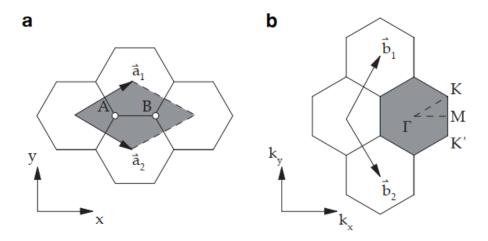
In Chapter 7, the work in the thesis is summarized and prospects for further improvements are included.

# 2 Materials and Properties

### 2.1 Nanocarbon materials

### 2.1.1 Graphene

Graphene is an atomic monolayer of graphite. It was experimentally isolated from graphite in 2004 by Novoselov and Geim using an adhesive tape. The research article published in Science showed an application of graphene as a FET<sup>[26]</sup>. Since then, graphene has sparked a fascination in the research community which led to several studies and applications of this material<sup>[18,27]</sup>.

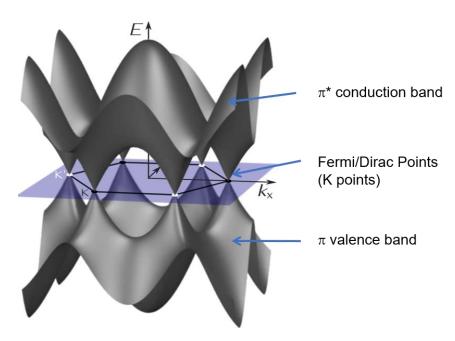


**Figure 2.1:** a) Unit cell of graphene in real space with  $a_1$  and  $a_2$  unit vectors and two carbon atoms A and B b) Reciprocal space with lattice vectors  $b_1$  and  $b_2$  and the Brillouin zone with K and K' points<sup>[28]</sup>.

Graphene has a hexagonal honeycomb structure with  $sp^2$  hybridized carbon atoms. Each carbon is bound to 3 neighboring in-plane carbon atoms with a bond angle of 120°. The C-C bonds in graphene are covalent bonded with 3  $\sigma$  bonds and 1  $\pi$  bond. The  $\sigma$  bonds give immense mechanical strength as these bonds are stable and difficult to break. However, the electrons in  $\pi$  bonds are mobile and impart enhanced conductivity.

Graphene unit cell has unit vectors  $a_1$  and  $a_2$  (figure 2.1a) spanning an angle of 60°. It encloses two carbon atoms sitting at  $1/3(a_1 + a_2)$  and  $2/3(a_1 + a_2)$ . The unit vectors  $a_1$ 

and  $a_2$  are the base to derive reciprocal lattice by Fourier transform and Brillouin zone (figure 2.1b)<sup>[28]</sup>.



**Figure 2.2:** Conduction and valence band of graphene meeting each other at six symmetrical K points at the Fermi level<sup>[29]</sup>.

Using the wave function from the molecular orbital theory for  $\pi$  electrons in graphene and tight-binding calculations the energy dispersion relation can be derived<sup>[30]</sup>.

$$E(k) = \frac{\epsilon_{2p} \pm tw(k)}{1 \pm sw(k)}$$
 (2.1)

$$w(k) = \sqrt{1 + 4\cos\frac{\sqrt{3}k_{x}a}{2}\cos\frac{k_{y}}{a} + 4\left(\cos\frac{k_{y}a}{2}\right)^{2}}$$
 (2.2)

Here,  $\varepsilon_{2p}$  is the energy of an electron in  $2p_z$  orbital, s, and t are overlap integrals and have identical values for all the nearest neighbor carbon atoms. w(k) accounts for the phase change in the electron wave function from carbon atom A to B. There have been many theoretical approaches to calculate the values of the three unknowns. To setup zero energy point at the Fermi level  $\varepsilon_{2p} = 0$  is used. t = -3.033 eV and s = 0.129 have been calculated theoretically before the discovery of graphene<sup>[31]</sup>. Hence, using equation 2.1 the energy dispersion relation can be plotted as shown in figure 2.2. In the energy dispersion relation, the conduction band and valence band meet at a point named K point. This makes graphene a zero band-gap semiconductor.

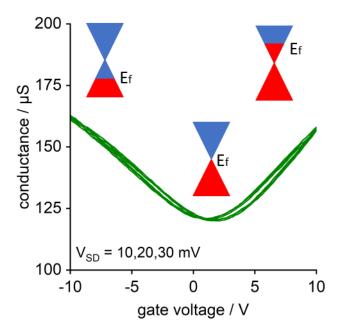
At the K point, w(k) and s have small values. Therefore, using binomial expansion of the equation 2.1, energy dispersion relation at K point can be written with good approximation as [30]

$$E(k) = \pm tw(k) \tag{2.3}$$

The positive and negative signs correspond to conduction (anti-bonding orbital) and valence bands (bonding orbital) respectively. The bonding orbital is fully occupied by two electrons with opposite spins contributed by each carbon atom. The anti-bonding orbital is empty. However, the bands meet at the K point and electrons in valence bands have adequate energy to move to the conduction band at non-zero temperature. For the electrons held in strong  $\sigma$  bonds, energy >10 eV is required to excite electrons to conduction bands<sup>[32]</sup>. Therefore, mostly electrons in  $\pi$  bonds contribute to the conductivity of graphene closer to the K points.

At the K point, the energy dispersion is linear. Due to this linear relation, electrons can be regarded as massless ( $E=\hbar k v_F$ ) and have constant velocity (Fermi velocity)  $v_F=10^6~{\rm ms}^{-1[31,32]}$ .

#### 2.1.1.1 Graphene field-effect transistors



**Figure 2.3:** Transconductance curve of a GFET showing Fermi levels at different gate voltages. Red and blue color corresponds to electrons and holes.

The transconductance in an FET is an important parameter for device applications. It gives information about the carrier mobility in the channel, doping levels, and intrinsic charge carrier concentration. A transconductance curve is shown in figure 2.3 for a GFET. The back gate modulates the position of Fermi energy in the Dirac cone. Depending on the potential applied to the back gate, the majority of charge carriers can be electrons (red) or holes (blue) as shown in figure 2.3. There exists a minimum in the conductance. The voltage at which the minimum of conductance occurs is called the Dirac voltage and indicates the location of the Fermi energy at the *K*-point where the valence band and conduction band touch each other. For p-doped (n-doped) graphene, the Dirac voltage can shift to positive (negative) voltage.

#### 2.1.1.2 Charge carrier density in graphene

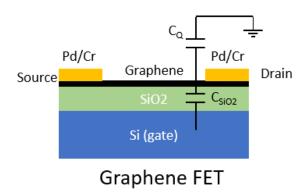


Figure 2.4: Schematic of GFET with equivalent capacitance circuit (adapted from ref [33]).

Kim et al.<sup>[34]</sup> had studied mono and bi-layer graphene charge carrier density. Here, few excerpts are adapted from Kim's work. In a transistor configuration, the back gate  $(V_{BG})$  can be used to modulate the charge carrier density (n) in the graphene. Considering a parallel capacitance model, shown in figure 2.4, the charge carrier density can be given as

$$V_{BG} = \frac{en}{C_{BG}} \tag{2.4}$$

Important to note here is that the work function of graphene is equal to Si back gate and the charge neutrality point of graphene is at  $V_{BG} = 0$  V<sup>[35]</sup>. Due to a limited density of states and Pauli's exclusion principle, for a given electron density, the Fermi energy has to be increased to occupy all the electrons in higher energy states. Hence, the Fermi energy term should be included in equation 2.4.

$$V_{BG} - \frac{E_F}{e} = \frac{en}{C_{BG}} \tag{2.5}$$

 $E_F/e$  term is the surface potential of graphene due to the electric field not being screened by graphene. This phenomenon is termed quantum capacitance and is used to determine the surface potential at a fixed charge carrier density<sup>[36]</sup>. The equation for the quantum capacitance and density of states is given as

$$C_O = e^2 D(E) (2.6)$$

$$D(E) = \frac{2}{\pi (\hbar \nu_F)^2} E = \frac{2}{\hbar \nu_F \sqrt{\pi}} \sqrt{n}$$
 (2.7)

$$C_Q = \frac{2e^2}{\hbar v_E \sqrt{\pi}} \sqrt{n} \tag{2.8}$$

Using the series configuration of parallel plate capacitances yields

$$V_{BG} = \left( \left( \frac{1}{C_Q} \right) + \left( \frac{1}{C_{BG}} \right) \right) en \tag{2.9}$$

The integration gives the equivalent quantum capacitance  $C_{Q,Eq} = (1/2)C_Q$ 

$$C_{Q,Eq} = \frac{e^2}{\hbar \nu_F \sqrt{\pi}} \sqrt{n} \tag{2.10}$$

The quantum capacitance  $\mathcal{C}_Q$  contribution to overall capacitance is insignificant when  $\mathcal{C}_Q$  is larger than the geometrical capacitance  $\mathcal{C}_{BG}$ . At the charge neutrality point, n is minimum and therefore  $\mathcal{C}_Q$  is minimum. Hence, the contribution of quantum capacitance is maximum at the Dirac voltage and reduces with higher charge density.

The charge density can be now calculated based on the back gate voltage  $V_{BG}^{[33]}$ . Using equations 2.9 and  $E_F=\hbar v_F \sqrt{\pi n}$ 

$$n(V_{BG}) = \left[ \left( \sqrt{\frac{\hbar v_F \sqrt{\pi} C_{BG}}{2e^2}} \right)^2 + \frac{C_{BG}}{e} V_{BG} - \frac{\hbar v_F \sqrt{\pi} C_{BG}}{2e^2} \right) \right]^2$$
 (2.11)

Using the following equation 2.12

$$\rho = \frac{1}{\sqrt{n^2 + n_o^2} e\mu} \tag{2.12}$$

the resistivity of graphene can be then calculated. Here,  $n_o$  is the residual carrier density and  $\mu$  is the mobility.

Now the total resistance of the device can be written as

$$R_{total} = R_{contact} + \frac{L}{W\sqrt{n^2 + n_o^2}e\mu}$$
 (2.13)

Here L and W are the length and width of the channel. The experimental transconductance curve can be fitted to equation 2.13 and the parameters  $R_{contact}$ , mobility, and residual charge carrier density can be extracted<sup>[37]</sup>.

#### 2.1.1.3 Literature

Since the discovery of graphene, this 2-D material has been extensively studied for different applications based on its high mobility, conductivity, and large surface area to volume ratio<sup>[38]</sup>. One of the many applications is its use as a sensing material. It can detect slight changes surrounding its environment and give corresponding output, electrically or optically<sup>[39]</sup>. Graphene has also found its way into biological applications for monitoring human health and water quality<sup>[40]</sup>. Some reports on these sensors are discussed below.

- Gas/Vapor sensors: Graphene-based sensors have increased exponentially in the last decade. There are different device structures used. These can be chemiresistor, FET, capacitance sensor, surface work function change transistor, surface acoustic wave (SAW) change transistor<sup>[41]</sup>. Chemiresistor is simple in fabrication and convenient in application and is the most commonly used sensor structure. A bias is applied across a channel/sensing material and a change in the surrounding is then detected by a change in the current values. A few of the reports are briefly described below:
  - 1. <u>Ammonia Sensor:</u> Using gold nanoparticles on chemical vapor deposited (CVD) graphene the ammonia sensing and regeneration time have been enhanced<sup>[42]</sup>. Yavari et al. displayed room temperature sensing ammonia at ppb levels<sup>[43]</sup>. Inkjet-printed graphene-PEDOT: PSS composite film sensed ammonia concentration low to 25 ppm. This device structure is capable of mass production and can be realized in flexible electronics<sup>[44]</sup>.
  - 2. <u>Nitrogen dioxide sensor:</u> Choi et al. fabricated gas sensor on a flexible substrate with multi-layered CVD graphene showing a detection limit at low ppm levels<sup>[45]</sup>. Monolayer graphene FET has been also used for detection limit to 2.5 ppm but had a long response time<sup>[46]</sup>.

- 3. <u>Carbon dioxide sensor:</u> Nemade et al. used electrochemically exfoliated few-layer graphene to fabricate CO<sub>2</sub> sensor with excellent performance<sup>[47]</sup>. Yoon et al. used mechanically cleaved graphene to fabricate a chemiresistor for sensing carbon dioxide to 10 ppm levels<sup>[48]</sup>.
- **pH sensor:** Fu et al. used the shift in Dirac point of graphene transconductance curve to detect the change in pH. They show that the GFET itself is not highly sensitive and a few nm of aluminum oxide is needed for increasing the sensitivity<sup>[49]</sup>. Vivaldi et al. fabricated a pH sensor based on reduced graphene oxide on paper<sup>[40]</sup>.
- Strain sensor: Graphene inherently does not possess piezoresistivity with a low gauge factor (GF) of 1.9 reported by Huang et al.<sup>[50]</sup> Hence, the engineering of graphene is required to achieve a higher GF. Introducing grain boundaries, defects can improve GFs as high as 151 in graphene<sup>[51]</sup>. Kim et al. demonstrated interconnected micro-sheets of graphene as a channel material for strain sensing<sup>[52]</sup>. It can detect strain by changing the resistance when stretched or compressed due to the change in the overlap of neighboring sheets.
- Optical sensors: Surface-enhanced Raman scattering (SERS) sensors have also been implemented in various fields based on graphene<sup>[53][54]</sup>. SERS is an attractive tool due to its enhanced detection of sensitivity. Graphene quenches the fluorescence background and with certain nanometals can be a potential substrate for SERS<sup>[55]</sup>. Ling et al. showed graphene enhances the Raman signal for molecules adsorbed onto its surface<sup>[56]</sup>. Ye et al. displayed polarization-dependent optical absorption in graphene<sup>[57]</sup>. This property was then used by Xing et al. to develop high sensitivity detection of cancer cells<sup>[58]</sup>.

#### 2.1.2 Carbon nanotubes

CNTs are a rolled-up structure of a graphene sheet. Hence, an additional boundary condition is imposed. The  $\overrightarrow{k_{||}}$  vector parallel to the CNT axis is continuous but  $\overrightarrow{k_{\perp}}$  is quantized due to the rolled-up structure<sup>[1]</sup>. The rolling up of the graphene can be done in numerous ways by superposing multiples of the primitive lattice vectors  $a_1$  and  $a_2$  forming a chiral vector, shown in figure 2.5.

$$C_h = na_1 + ma_2, m \le n \tag{2.14}$$

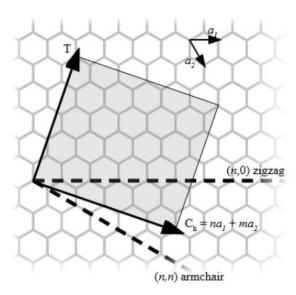
Due to the hexagonal symmetry,  $m \le n$  should be only considered. Here n, m belongs to the natural number and are called chiral indices and unique for a CNT. The translation

vector T is defined as the vector normal to the chiral vector and parallel to the nanotube axis. Using the indices, the diameter d of the CNTs can be given as

$$d = \frac{|C_h|}{\pi} = \frac{a}{\pi} \sqrt{n^2 + nm + m^2}, a = \sqrt{3}a_c = 0.246 nm$$
 (2.15)

The angle between  $na_1$  and  $ma_2$  is the chiral angle and given by the equation 2.16 [59]

$$\theta = \arctan\left(\frac{\sqrt{3}m}{2n+m}\right) \tag{2.16}$$

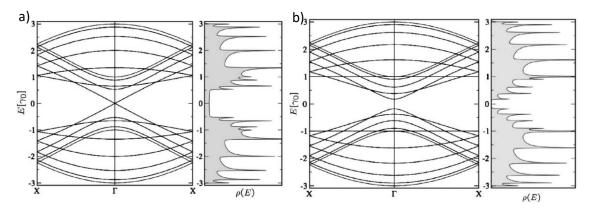


**Figure 2.5:** Graphene sheet rolling to a carbon nanotube. T is the translation vector along the axis and C<sub>h</sub> is the chiral vector<sup>[60]</sup>.

Due to hexagonal symmetry,  $0 \le \theta \le 30^\circ$ . CNTs with (n,0) indices and chiral angle of  $0^\circ$  are called zig-zag CNTs and (n,n) with the chiral angle of  $30^\circ$ , armchair CNTs. They are metallic or semiconducting depending on the chiral indices (n,m). The condition 3i = (n-m), where i is an integer, denotes all the tubes that have metallic behavior  $^{[61]}$ . All the others have semiconducting properties. The band structure of a CNT is calculated by a zone-folding approximation based on the tight-binding calculation of graphene. Due to multiple 1D parabolic bands, the density of states is marked by van Hove singularities and  $\frac{1}{\sqrt{E}}$  energy dependence is given by  $^{[59]}$ ,

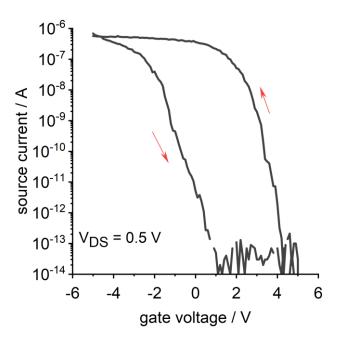
$$\rho(E) = \frac{2a}{\pi t |C_h|} \sum_{i=1}^{2n} \sum_{s=\pm} \frac{|E_i^s(k)|}{\sqrt{E_i^s(k)^2 - \epsilon_{is}^2}}$$
(2.17)

Here *i* iterates over the different bands and *s* is the spin state. The band gap in semiconducting CNTs is calculated by the energy gap between the first two van Hove singularities. Figure 2.6 shows the band diagram of a semiconducting and a metallic CNT.



**Figure 2.6:** a) (9,0) metallic zig-zag carbon nanotube b) (10,0) semiconducting zigzag carbon nanotube (adapted from ref<sup>[59]</sup>).

#### 2.1.2.1 Carbon nanotubes field-effect transistors

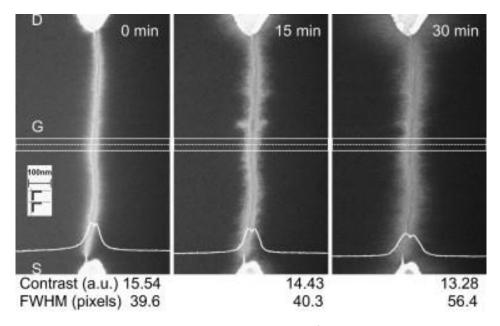


**Figure 2.7:** Transconductance curve of a CNT-FET fabricated on h-BN/SiO<sub>2</sub>/Si. Red arrows indicate the sweep direction.

Carbon nanotubes can be utilized as a channel material to fabricate FETs. The transconductance curve of p-type CNTs on h-BN and  $SiO_2$  as gate dielectrics is shown in figure 2.7. The channel conducts only holes (at negative gate voltage) and not electrons. The red arrows indicate the sweep direction. The source current is not equal in forward and reverse sweeps. This can happen if hysteresis exists in the system. Hysteresis in these types of field-effect transistors is always present and is discussed in the next section. Important parameters to understand the performance of such transistors are field-effect mobility, on-off ratio, and sub threshold slope which are also discussed in next section.

### 2.1.2.2 Hysteresis

### 2.1.2.2.1 Causes of hysteresis

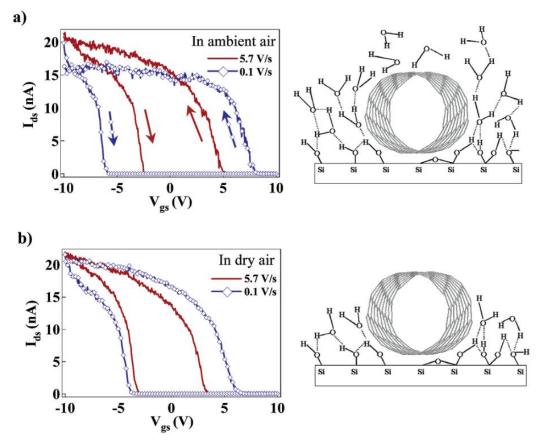


**Figure 2.8**: Time-dependent voltage contrast SEM images for a metallic nanotube at 20 V gate bias. At 0 min charges are injected in the nanotube and it appears brighter than the substrate. At 15 mins of charging, dielectric around the nanotube is populated with charges visible by high contrast area around the tube. At 30 mins the nanotube is itself darker but the substrate around is brighter (adapted from ref <sup>[62]</sup>).

a) Charge traps in dielectric: Vijayraghavan et al.<sup>[62]</sup> used voltage-contrast scanning electron micrography (VC-SEM) to study and image the charging and discharging of charge traps present in the dielectric layer. The concept is based on the fact that charges from nanotube would be injected into the dielectric (SiO<sub>2</sub>) which contributes to surface potential and consequently to secondary electrons yield

which then can be used to image the charge injection and redistribution. For gate bias of +20 V, images were recorded at three timestamps (figure 2.8). At t=0, the nanotube appears brighter and the substrate darker because of the charging of the nanotube. With time, electrons start getting injected from the nanotube into the substrate which is visible at t=15 min as a brighter area growing around the tube. At time t=30 min, the nanotube itself remains dark and the substrate becomes brighter because of zero potential to nanotube but the negative potential to the substrate because of charge injection.

**b)** <u>Water molecules:</u> Kim et al. <sup>[63]</sup> have shown how water molecules in the environment affect device performance and hysteresis. They infer that the main contributors are water molecules, especially SiO<sub>2</sub> surface-bound, which cannot be removed even in an inert atmosphere.



**Figure 2.9:** a) In ambient air, water molecules and water adsorbed on silicon oxide can interact with nanotubes and give rise to larger hysteresis compared to b) dry air, where only water molecules on the substrate contribute towards hysteresis (adapted from ref <sup>[63]</sup>).

Water on CNTs can also act as slow charge traps, and while gate sweeping, charging, and discharging do not occur on the same time scale. This in turn can lead to hysteresis (figure 2.9).

c) <u>Surface hydroxyl groups:</u> Jin et al. <sup>[64]</sup> studied the interface of CNTs and the substrate SiO<sub>2</sub>. Surface-hydroxyl groups present on SiO<sub>2</sub> surface due to adsorption of water molecules are one of the prime contributors to hysteresis<sup>[65,66]</sup>. Charging and discharging of hydroxyl (-OH) group can electrostatically modulate and thereby change the conductivity of the SWCNTs. With a positive bias on the gate, -OH groups can trap electrons. For a p-type SWCNT-FET, this causes the threshold voltage to shift in the positive direction to counter-balance electron charges trapped. The density of physisorbed water molecules at the interface affects the magnitude of this effect<sup>[67,68]</sup>.

#### 2.1.2.2.2 Mechanism of hysteresis

To understand the mechanism of hysteresis, charge redistribution at the dielectricnanotube interface during gate bias needs to be realized. Figure 2.10e illustrates a transconductance curve measured for (9,8) p-type SWCNT transistor on SiO<sub>2</sub>/Si substrate under ambient conditions. The complete gate voltage sweep (forward and reverse) is explained by showing charge redistribution in the oxide layer using figures 2.10a-d. The sweep range is -5 V to 5 V with a scan rate of 0.1 Vs<sup>-1</sup>. Initially,  $V_g = -5 \text{ V}$  is applied depicted in figure 2.10a. Subsequently, positive charges appear on the dielectric surface (SiO₂) which p dopes CNT channel and source-drain current (~nA) is measured. The channel is in the on-state (regime a). Due to charge traps present in the dielectric, even after removal of bias (V<sub>g</sub> = 0 V), the dielectric surface remains positively charged and starts to n dope channel as shown in figure 2.10b. Electrons do not contribute as charge carriers because the Schottky barrier is very large for electrons and low for holes. Therefore, the channel is in off-state (regime b). At positive gate bias (V<sub>g</sub> > 0 V), mirror charges (negative charges for positive gate bias) appear on oxide surface and n dopes the channel shown in figure 2.10c. Source-drain current in the order of fA flows through the channel and the channel is still in off-state (regime c). During the reverse sweep (at V<sub>g</sub> = 0 V), charges trapped in the dielectric are negative which p dopes the CNT channel (figure 2.10d). A finite value (~ 1 nA) of current flows from source to drain and the channel is in on-state (regime d). To summarize, in forward bias at  $V_g = 0 \text{ V}$ , the sourcedrain current is zero, but in the reverse sweep at V<sub>g</sub> = 0 V, the source-drain current has a non-zero value. Therefore, the history and present conditions of gate voltage play a role if the channel would behave as off or on in a transistor. Although the reasons are discussed in the previous section, it is important to note that charge traps in the

dielectric can induce a condition of hysteresis in CNT transistors, and different time scales of charging and discharging of these charge traps lead to this phenomenon. Charge traps can also be formed at metal-CNT contacts due to the adsorption of water molecules on the metal <sup>[70]</sup> for devices with CNT on top of metal contacts. For devices with metal on top of CNT contacts, water adsorbed charge traps on metal do not play any role in the hysteresis. After understanding the mechanism, one can ponder over possible solutions.

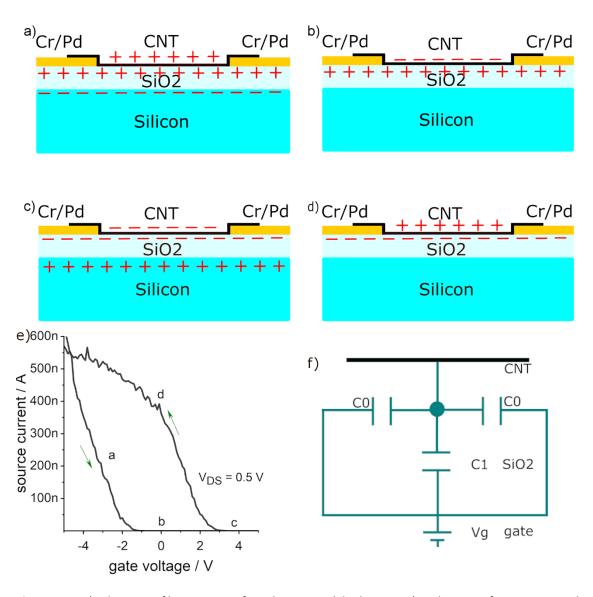


Figure 2.10: a) Schematic of hysteresis surface charging and discharging. a) At the start of gate sweep with negative gate voltage ( $V_g < 0$ ). b) At  $V_g = 0$  V. c) At positive gate bias ( $V_g > 0$ ). d) At  $V_g = 0$  V in a reverse sweep. e) Transconductance curve of (9,8) SWCNTs on SiO<sub>2</sub> measured under ambient conditions, green arrows indicate sweep direction. f) Equivalent circuit of hysteresis model. C1 is silicon oxide capacitor and C0 is the surface leaky capacitors where charge redistribution takes place (modified from ref [69]).

#### 2.1.2.2.3 Solutions to hysteresis

The key to hysteresis-free SWCNT transistors is to form an environment for the nanotubes free from charge traps and water molecules. Some of the reported studies are briefly described below.

#### a) Passivation of substrate:

- a. Functionalization of SiO<sub>2</sub>/Si using silanes<sup>[71]</sup>.
- b. Making the substrate hydrophobic by polymers like parylene<sup>[72]</sup> or PTFE<sup>[73]</sup>.
- c. Self-assembled monolayer on metal gate oxide which is used as gate dielectric<sup>[74]</sup>
- **b)** Using top gate structure: Many oxides like  $TiO_2^{[75]}$ , self-grown Aluminum dioxide<sup>[76]</sup> have been used as top-gate dielectric which can remove the effects of  $SiO_2$  charge traps and can also act as an encapsulant.
- **c)** Encapsulation of the nanotubes: To inhibit any interaction of water molecules from the environment, SWCNTs should also be encapsulated.
  - a. Oxide coating, for example, aluminum dioxide<sup>[77]</sup>
  - b. Polymer coating like methyl siloxanes or polymethylmethacrylate (PMMA) [63][64][78][79][80][81]

### d) Creating inert atmosphere:

- a. Suspending carbon nanotubes and exposing them to high vacuum<sup>[69]</sup>
- b. Cooling down to 4 K<sup>[5]</sup>.

#### 2.1.2.3 Charge traps

Carbon nanotube transistors on h-BN and  $SiO_2$  can be considered in the capacitance model shown in figure 2.11. The capacitance of the system was calculated using CNT as a cylinder on top of the parallel plate Si shown in figure 2.11. The capacitance of such a system is<sup>[69]</sup>

$$c_g = C/L = \frac{2\pi\varepsilon}{\ln\left(\frac{4h}{d}\right)} \tag{2.18}$$

Here L is the length of the CNT channel, d is nanotube diameter and h is the distance between CNT and silicon.

 $\varepsilon = \varepsilon_o \ \varepsilon_{eq}$ , ( $\varepsilon_o$  is the permittivity in free space).

 $h = 300 \text{ nm (SiO}_2) + 80 \text{ nm (h-BN)} = 380 \text{ nm}$ 

$$\varepsilon_{h-BN}$$
= 3.29,  $\varepsilon_{SiO2}$  = 3.9

For CNT transistors on top of the SiO<sub>2</sub>

$$c_{g,SiO2}$$
 = 0.302 pF cm<sup>-1</sup>

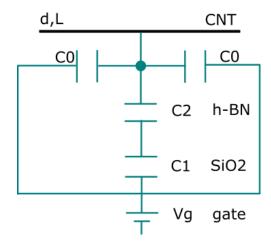


Figure 2.11: Capacitance system of nanotubes on h-BN and SiO<sub>2</sub>/Si.

For 80 nm of h-BN on top of 300 nm of SiO<sub>2</sub>, two dielectrics are in series, and therefore the equivalent dielectric constant of the system has to be calculated using two capacitors in series. The equivalent dielectric constant of the system is then

$$\varepsilon_{eq} = \frac{(d_{h-BN} + d_{SiO2})\varepsilon_{SiO2} \varepsilon_{h-BN}}{d_{h-BN}\varepsilon_{SiO2} + d_{SiO2}\varepsilon_{h-BN}}$$
(2.19)

which is calculated  $\varepsilon_{eq}$  = 3.75

Calculating  $c_q$  gives the value

$$C = 0.290 \text{ pF cm}^{-1}$$

For calculating the number of charge traps for devices  $^{[63]}$ , an absolute value of threshold voltage for forward and reverse sweeps are required (hysteresis). Utilizing the capacitance value, charge traps can be calculated by using Q = C V where V is the value of hysteresis in volts.

#### 2.1.2.4 Field-effect mobility

Mobility for a nanotube device is given by:

$$\mu = \sigma/q \tag{2.20}$$

Charge density per unit length q is given by

$$q = c_q(V_t - V_q) (2.21)$$

For channel length greater than the thickness of the dielectric, mobility is given by

$$\mu = \frac{L}{c_a} \frac{G}{(V_t - V_a)} \tag{2.22}$$

Due to ambiguity in determining the threshold voltage  $V_t$  for field-effect transistors, mobility is expressed in terms of field-effect mobility<sup>[82]</sup>:

$$\mu_{FE} = \frac{L}{c_g} \frac{\delta G}{\delta V_g} \tag{2.23}$$

Field-effect mobility is device-specific and not material-specific. It depends on contact resistance, surface effects, etc.<sup>[82]</sup>.

The device performance can also be measured in terms of field-effect mobility values. Higher values are required for better performance of transistors.

#### 2.1.2.5 Sub threshold slope

The subthreshold slope is one of the parameters on which a transistor performance is characterized. In the subthreshold region of the device's transport, current in the channel decays exponentially controlled by the gate, similar to a forward-biased diode. The subthreshold slope is the reciprocal value of subthreshold swing and is given by the following equation<sup>[82]</sup>:

$$S = (d(\ln G)/dV_g)^{-1}$$
 (2.24)

The theoretical limit of subthreshold slope known as the thermionic limit is  $60 \text{ mV dec}^{-1}$  at room temperature, and experimentally ~ 70 mV dec<sup>-1</sup>, slightly degraded due to parasitic capacitances<sup>[83]</sup>. Smaller values imply a faster switching rate and therefore better performance of a transistor.

#### 2.1.2.6 Literature

CNTs have been employed in several device applications and shown promising performance in diodes<sup>[84]</sup>, quantum light sources<sup>[85]</sup>, and sensors<sup>[3]</sup>. Few reports are discussed below:

- Memory devices: CNTs based memories have advantages due to high stability, high thermal conductivity, and small sizes<sup>[86]</sup>. Fuhrer et al. reported the first application of carbon nanotube transistors as a non-volatile memory<sup>[87]</sup>. Cai et al. reported excellent charge storage capability at room temperature<sup>[88]</sup>. Derycke et al. fabricated a voltage inverter by using n-type and p-type carbon nanotubes<sup>[89]</sup>. Yang et al. showed non-volatile memory based on semiconducting CNTs with large retention capacity<sup>[90]</sup>.
- Gas Sensors: The ability to detect changes in its surroundings, doping, and large on/off ratio makes CNTs an ideal candidate for sensing application. When interacted with a foreign molecule, the electrical conductivity of the CNTs changes due to charge transfer. Kong et al. first displayed this principle in 2000 where the conductivity increased or decreased when exposed to NO<sub>2</sub> or NH<sub>3</sub> molecules<sup>[91]</sup>. Huang et al. fabricated a three-terminal device N<sub>2</sub> sensor based on vertically aligned CNTs<sup>[92]</sup>. Ganzhorn et al. demonstrated a hydrogen sensor based on carbon nanotubes, where the change in the work function of Pd was the sensing principle<sup>[3]</sup>.
- **Photo sensors:** A photon with energy higher than the band gap of CNTs generates electron-hole pair, which is then separated by built-in electric potential and measured as photocurrent. This photocurrent can be utilized for photo sensing. The potential is created by either a p-n junction<sup>[93]</sup> or a Schottky barrier<sup>[94]</sup>.

## 2.1.3 Nanocrystalline graphene

#### 2.1.3.1 Synthesis

Nanocrystalline graphene (NCG), is another form of graphene with crystal size in nanometers. Several synthesis routes are reported in the literature. Zhang et al. graphitized the spin-coated photoresist under high vacuum at higher temperatures (~1000°C) with a controlled flow of H<sub>2</sub>/Ar<sup>[95]</sup>. Turchanin et al. used aromatic self-assembled monolayer (SAM) to grow NCG. The SAM was first cross-linked using a high dose of electron irradiation and subsequently vacuum annealed to form graphene<sup>[96]</sup>. Zhao et al. reported an ultrafast technique of NCG synthesis. A platinum foil was heated at high temperature (~900°C) and thereafter quenched in ethanol at room temperature, resulting in the synthesis of NCG in few seconds<sup>[97]</sup>. Yekani et al.<sup>[98]</sup> synthesized NCG on germanium substrate using a similar process reported by Riaz et al<sup>[99]</sup>.

The CVD technique can also be utilized to synthesize NCG. Noll et al. reported the fabrication of NCG for ammonia detection by using an additional layer of PMMA as a carbon source other than the CH<sub>4</sub>/H<sub>2</sub> precursor<sup>[100]</sup>. Plasma-enhanced CVD allows the synthesis at lower temperatures (750-900°C) as the precursors can be dissociated using plasma<sup>[101]</sup>. Oliveira et al. reported NCG fabrication using molecular beam epitaxy (MBE) on sapphire substrates<sup>[102]</sup> where HOPG filament as a carbon source is heated at extremely high temperatures (2200-2400°C) in ultra-high vacuum.

#### 2.1.3.2 Electrical properties

In graphene, resistance can be given as [103][104]

$$R_{total} = R_{grains} + R_{GB} (2.25)$$

The GBs have a diminished conductivity for electron and hole-type charge carriers and may act as an electrical potential barrier for charge transport and enhance intervalley scattering<sup>[105]</sup>. Scanning tunneling microscope (STM) studies have shown an inclination of GBs towards n-type behavior when compared with the grains in bulk graphene<sup>[106]</sup>. It is independent of temperature and can range from few to hundreds of  $k\Omega$ . The charge transport in graphene is controlled by scattering at GBs and scattering in grains. In a polycrystalline graphene sample, average GB resistivity can be calculated using the scaling law

$$R_S = R_S^0 + \frac{\rho_{GB}}{l_G} {(2.26)}$$

Here,  $R_s$  is the sheet resistance of polycrystalline graphene,  $R_s^0$  is the sheet resistance within grains and  $I_G$  is the average size of the grain<sup>[107]</sup>. For larger grain size (>10 um), the first term is dominated and the sheet resistance is equivalent to the resistance of the individual grains and the contribution from GBs does not play an important role<sup>[108]</sup>. However, for nanocrystalline graphene (grain size 2-5 nm), resistance is dominated by scattering at GBs. Therefore, the conductivity in NCG can be described as a framework where the electron conduction occurs within grains and tunnels through the grain boundaries. It is based on tunneling conduction theory. Simmons gave a generalized formula by studying conduction between two electrodes separated by a thin insulating film <sup>[109]</sup>. Considering two grains separated by GB, based on the Simmons formula, charge transfer from one grain to another occurs through tunneling, and the conductance is given by <sup>[110]</sup>

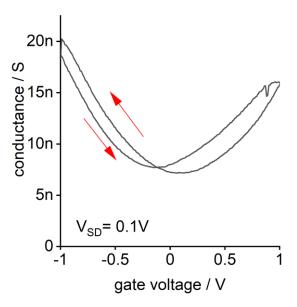
$$G_{ij} = G_f \exp(-2\chi S_{ij} - \frac{E_{ij}}{kT})$$
 (2.27)

$$E_{ij} = \frac{1}{2}(|E_i - E_j| + |E_j - E_i|)$$
(2.28)

Here  $\chi$  is the tunneling parameter,  $E_i$  and  $E_j$  are the electron energies at the grains i and j.  $S_{ij}$  is the tunneling distance between the center of the grains.

Few studies reported on the values of sheet resistance of NCG thin films. The sheet resistance primarily depends on the thickness and crystallite size. Riaz et al. studied the sheet resistance based on thickness which varied from 20 k $\Omega$  sq<sup>-1</sup> (6nm) to 80k $\Omega$  sq<sup>-1</sup> (1nm)<sup>[99]</sup>. Zhang et al. reported 30 k $\Omega$  sq<sup>-1</sup> for 1 nm thick NCG films<sup>[111]</sup>. Turchanin et al. were able to tune the crystal size with temperature and had values of sheet resistance from 10 to 100 k $\Omega$  sq<sup>-1</sup> for crystal sizes 25 to 8 nm<sup>[96]</sup>. Zhao et al. achieved 33.7 k $\Omega$  sq<sup>-1</sup> (10.3 nm) and 180 k $\Omega$  sq<sup>-1</sup> (3.6 nm) sheet resistance values<sup>[97]</sup>.

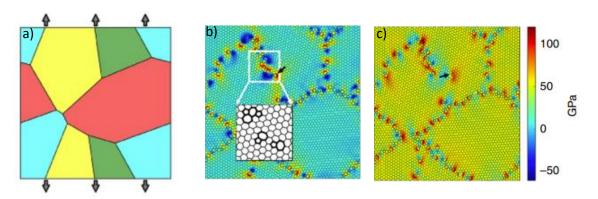
#### 2.1.3.3 NCG field-effect transistor



**Figure 2.12:** Transconductance curve of an ionic liquid gated NCG field-effect transistor. The red arrows indicate the sweep direction.

Figure 2.12 shows a transconductance curve of an NCG-FET with a top ionic liquid gate. The curve is similar to graphene field-effect transistors and shows hole (negative gate voltage) and electron (positive gate voltage) conduction regimes. The conductance minima occur close to zero gate voltage indicating small doping levels.

## 2.1.3.4 Mechanical properties



**Figure 2.13:** a) Schematic of randomly oriented grains in a polycrystalline graphene under tensile stress b) Residual stress showing the grain boundaries have higher concentration of strains (black arrow) compared to grains c) Stress in the grains and grain boundaries just before fracture of the film (adapted from ref [15]).

Lee et al. were the first to report on the mechanical properties of graphene specifying excellent mechanical strength with young's modulus of 340 N m<sup>-1[17]</sup>. NCG, however, has a large density of defects present in the form of GBs and TJs which comprises heptagon and pentagon defects in an otherwise hexagonal structure. This affects the mechanical and electrical properties in comparison to graphene<sup>[112,113]</sup>. Shekhawat et al. showed that these defects can act as stress concentrators<sup>[2]</sup>. In figure 2.13a, simulation is shown where a polycrystalline graphene sample is subjected to tensile stress. Figure 2.13b-c show two instances of zero external stress and the peak stress at failure. In figure 2.13c it is depicted that the origin of fracture occurs at one of the GBs and then propagates through the crystals. The GBs are however already under higher stress compared to the crystals even before the application of any external stress. Zhao et al. showed that as the size of the crystals moves down to 3.8 nm, the fracture strength of graphene reduces by 40%<sup>[97]</sup>. GBs are a low elastic modulus region in NCG because of the long bond length in GBs compared to inside grains<sup>[114]</sup>. According to the Hall-Petch effect, nanocrystalline materials are primely harder compared to their microcrystalline counterparts<sup>[115]</sup>. However, NCG GBs have stable covalent  $sp^2$  bonds and hence no slip occurs between two grains. Therefore, under externally applied stress, brittle breakage of these covalent bonds occurs and the origin of fracture is always at a defect site (GBs or TJs). Fracture propagation could be intergranular or intragranular<sup>[2]</sup>.

#### 2.1.3.5 Piezoresistance principle in NCG

Piezoresistance exists in many metals like iron and semiconductors like silicon. It was first studied by William Thomson in iron and copper back in 1856<sup>[116]</sup>. He observed a

change in resistance by elongating the metals using a Wheatstone bridge. In 1935 the term "Piezoresistance" was coined by Cookson<sup>[117]</sup>. The first experimental proof of piezoresistance on semiconductors was shown in 1954<sup>[118]</sup>. Since then, silicon has been used in various strain gauges to measure mechanical quantities like force, torque, etc.<sup>[119,120]</sup>. Consequently, this led to the emergence of the MEMS technology in 1982<sup>[121]</sup>.

The resistance (R) of material with a current (direction indicated by orange arrow) passing through an area A and length I (figure 2.14) is given by



Figure 2.14: Resistance of a block of material with length I and cross-section area A.

$$R = \frac{\rho l}{A} \tag{2.29}$$

Here,  $\rho$  is the resistivity of a material.

Resistance can change due to the application of external strain. The change occurs either due to a change in resistivity of material or change in geometry or both. GF is the ratio of fractional change in resistance and applied strain  $\varepsilon$ . It is expressed as

$$GF = \frac{\Delta R}{R \, \varepsilon} \tag{2.30}$$

Taking into consideration the geometrical factor (1+2 $\nu$ ) and the resistivity change<sup>[122]</sup>

$$\frac{\Delta R}{R} = (1 + 2v)\varepsilon + \frac{\Delta \rho}{\rho} \tag{2.31}$$

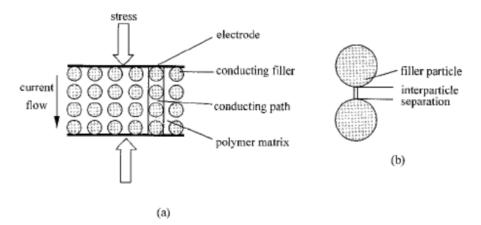
Here  $\nu$  is the Poisson's ratio. The value for most metals' ranges from 0.2 to 0.35. Silicon, as an anisotropic material, the Poisson's ratio depends on the direction of the crystal structure and the value ranges from 0.06 to 0.36<sup>[123]</sup>. The limits of v for an isotropic material are -1 to 0.5<sup>[124]</sup>.

Metals have a very small GF due to the small change in resistivities, whereas, silicon and germanium experience large changes in resistivity in a certain direction of crystal structure and strain due to anisotropic behavior. Semiconductors have a directional dependence of piezoresistivity in terms of directions of load applied (stress) and fields

such as current or potentials. In semiconductors, the explanation of piezoresistance is in how electrons and holes move in the crystals under strain. Mobilities and effective mass of charge carriers change under applied strain<sup>[125]</sup>. The two types, n- and p-type semiconductors behave inversely. The reason for the change in mobility is attributed to band bending and non-uniformity in the density of states<sup>[118]</sup>.

There have been few studies that have reported on NCG as a piezoresistance material. Riaz et al. demonstrated a proof of concept of NCG strain sensor with a GF of  $23^{[99]}$ . Zhao et al. achieved GF of ~300 based on nanographene films. They used the charge tunneling model to explain their findings<sup>[126]</sup> until small strain values of 0.3%. In another report by the same group, a tunable piezoresistive strain sensor was fabricated with GF up to 500. The tunability of resistance and GF was gained by controlling the domain size of NCG films. The piezoresistance behavior beyond 0.3% strain follows the tunneling and destruction model<sup>[24]</sup>.

#### 2.1.3.5.1 Piezoresistance model



**Figure 2.15:** Schematic of a) the stress applied on a conductor-filled polymer composite and b) two conductive particles with an interparticle separation (adapted from ref<sup>[127]</sup>).

NCG film displays the inherent property of piezoresistance. To explain this, researchers take into account the charge tunneling model given by Simmons<sup>[109]</sup> where the tunneling of charged carriers occurs between adjacent electrodes separated by a distance. Zhang et al.<sup>[127]</sup> studied a conductor-filled polymer composite as a piezoresistance system which can be mimicked for piezoresistance in NCG. The same model was also applied by Zhao et al<sup>[24]</sup>. Grains of NCG can be considered as conductive fillers in a composite of polymer separated by a tunneling distance similar to GBs. In this section, the derivation of the model is discussed briefly.

In the polymer conductor composite, resistance is contributed by resistance through each conductor particle and the polymer matrix. Figure 2.15a-b shows a polymer conductor composite where the conductor particles are separated by a distance. Resistance can be given by [128]

$$R = \frac{L(R_m + R_c)}{N} \tag{2.32}$$

Here L is the number of particles forming a conducting path,  $R_m$  and  $R_c$  are the resistances of matrix and conductor and N is the number of conducting paths. An assumption of constant resistance of the matrix is made, and therefore, several conducting paths perpendicular to the current flow is ignored. At small distances between the conductor, tunneling current can flow<sup>[129]</sup> which can be given by Simmon's function at low bias as  $^{[109]}$ 

$$J = \frac{3\sqrt{2m\varphi}}{2s} \left(\frac{e}{h}\right)^2 V \exp\left(-\frac{4\pi s}{h}\sqrt{2m\varphi}\right)$$
 (2.33)

Here, e, m is the electron charge and mass, h is Plank's constant, V is the bias applied and s is the thickness of the insulating film and  $\Phi$  is the difference in work functions of conductor and polymer. Assuming all the electrons tunnel within a small surface area  $a^2$  and at the least distance s between particles <sup>[130]</sup>. Resistance of the matrix can now be expressed as

$$R_m = \frac{V}{a^2 J} = \frac{8\pi hs}{3a^2 \gamma e^2} \exp(\gamma s)$$
 (2.34)

Here  $\gamma = \sqrt{2m\varphi} \frac{4\pi}{h}$ 

Using equating 2.34 in equation 2.32, the total resistance can be expressed as

$$R = \frac{L}{N} \left( \frac{8\pi hs}{3a^2 v e^2} exp(\gamma s) \right)$$
 (2.35)

After establishing the relation between the resistance and the tunneling distance, the rate of change of resistance with original tunneling distance  $s_0$  can be derived (for strain values < 0.3%) using equation 2.35 as

$$ln\frac{R}{R_0} = ln(1+\varepsilon) + \gamma s_0 \varepsilon \tag{2.36}$$

$$GF = \frac{\Delta R}{R_0} / \varepsilon = \frac{1}{\varepsilon + \varepsilon^2} exp(\gamma \Delta s) - \frac{1}{\varepsilon}$$
 (2.37)

From equation 2.37, for the same strain values, higher sensitivity can be achieved if the change in tunneling distance,  $\Delta s$ , is larger. As strain is equivalent to  $\Delta s/s_0$ , Zhao et al. reported that films with similar sheet resistance have different GFs because of different initial tunneling distances <sup>[24]</sup>. For higher tunneling distance, higher GFs were achieved. Therefore, the initial tunneling distance should be the prime focus to achieve a higher GF piezoresistor. From the slope of  $\ln(R/R_0)$  vs  $\epsilon$  curve, the initial tunneling distance  $s_0$  can be calculated, considering  $\Upsilon$  = 10 nm<sup>-1[131]</sup>.

For strain > 0.35%, tunneling and destruction model is applied, where the number of conductive path N is reduced exponentially with increasing strain, and given by<sup>[132]</sup>

$$N = N_0 exp[-(\alpha \varepsilon + \beta \varepsilon^2 + \gamma \varepsilon^3 + \lambda \varepsilon^4)]$$
 (2.38)

and<sup>[24]</sup>

$$ln\frac{R}{R_0} = ln(1+\varepsilon) + \alpha\varepsilon + \beta\varepsilon^2 + \gamma\varepsilon^3 + \lambda\varepsilon^4$$
 (2.39)

Here  $\alpha = \gamma s_0$ .

#### 2.1.3.6 Literature

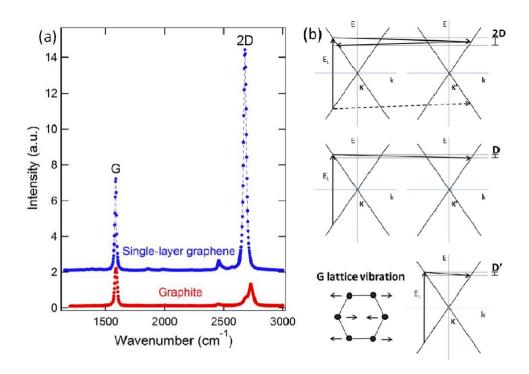
NCG has been employed in several applications. A few of the reports are mentioned below:

- **Sensors:** NCG with conductive domains, and a high density of GBs makes it a suitable candidate for sensing. The conductivity of the material changes if gas molecules get adsorbed on the GBs or if the distance between the grains is changed. Noll et al. fabricated an ammonia sensor using NCG-FET with detection limits of 100 ppb<sup>[100]</sup>. Zhao et al. fabricated a tunable strain sensor based on NCG and explain the dependence of tunneling distance on the sensitivity of strain sensing<sup>[24]</sup>. Yasei et al. demonstrated a 300 times higher sensitivity to the gases for NCG compared to mono-crystalline graphene<sup>[133]</sup>.
- **Photodetectors:** NCG on Si/SiO<sub>2</sub> has shown promising application as Schottky junction photodetectors<sup>[134][25]</sup> owing to high transparency, ~80-90% in the visible and IR region<sup>[99]</sup>.
- Gas separation: Fishlock et al. utilized PECVD grown NCG films for the separation
  of gases based on molecular sizes<sup>[135]</sup>. The cut-off barrier of separation was ~0.34

nm, which is equivalent to the interlayer distance. This suggests that the flow of the gases for separation is interlayer and not through the defects in the layer.

# 2.2 Properties of carbon nano-systems

## 2.2.1 Raman spectroscopy

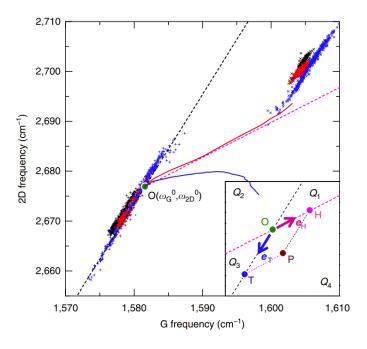


**Figure 2.16:** a) Raman spectra of graphene (blue) and graphite(red) by a 532 nm excitation laser b) Phonon scattering processes for different Raman peaks in graphene. D and D' peaks only occur in graphene films with defects (adapted from ref<sup>[136]</sup>).

Raman spectroscopy is a widely used technique to study carbon nano systems such as graphene, carbon nanotubes, and nanocrystalline graphene. Raman spectra of a graphite crystal have a G band located at 1580 cm<sup>-1</sup> and a 2D band at 2680 cm<sup>-1</sup> (figure 2.16a). It is a non-destructive tool to study graphene in many different aspects, for example, number of layers, level of doping, and strain, etc. The G peak at 1580 cm<sup>-1</sup> comes from the doubly degenerate zone-center E<sub>2g</sub> phonon mode (figure 2.16b). Graphene is a monolayer of graphite and has additional peaks depending on defects. Some of the defects like GBs could break the symmetry and a defect band called the D band appears at 1350 cm<sup>-1</sup>. The 2D mode at 2700 cm<sup>-1</sup> is caused by the double resonant

Raman scattering with two phonon emissions (figure 2.16b)<sup>[137]</sup>. The 2D band intensity is insensitive to defects but is sensitive to the number of layers of graphene. The G and 2D mode peak positions or the intensity ratio, both are sensitive to the doping level and charge carrier density due to electron-phonon coupling<sup>[138]</sup>. Temperature and the levels of strain can also be determined by the shifts in graphene's Raman G and 2D peaks<sup>[139][140]</sup>. Tunistra and Köenig used Raman to calculated the crystallite size of a graphitic material<sup>[141]</sup>. Although, many pieces of information can be extracted from Raman spectroscopy of graphene, in this chapter the discussion is limited to the level of doping and crystallite size.

## 2.2.1.1 Doping and Strain

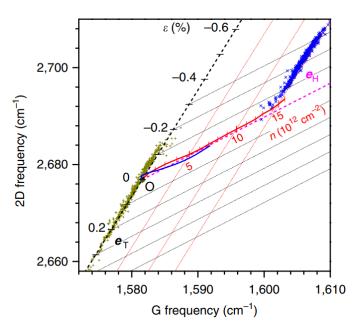


**Figure 2.17:** Three graphene samples were studied (red, blue, and black). The green dot is a reference point unaffected by any strain and doping. Magenta color dashed line depicts strain-free graphene with varying density of holes. The black dashed line depicts undoped graphene with varying strain. Red and blue solid lines represent holes and electron doping trajectories respectively. Inset: Decomposition of OP vector into  $e_T$  (strain axis) and  $e_H$  (doping axis) and the four quadrants  $Q_{1-4}$  (adapted from ref<sup>[145]</sup>).

Numerous reports have studied the strain and doping level based on Raman peak positions of graphene<sup>[142–144]</sup>. One of the reports on studying strain and hole doping is discussed which is used in chapter 5 to understand the results. Lee et al.<sup>[145]</sup> characterized graphene doping and strain levels based on shifts and position of graphene Raman G and 2D peaks. Two vectors based on tensile strain ( $e_T$ ) and hole ( $e_H$ ) doping were established in  $\omega_{G^-}\omega_{2D}$  space. Any vector OP (inset of figure 2.17) can be

deconvoluted into two components as  $ae_T+be_H$  and the level of doping and strain can be derived from the Raman peak frequencies.

The green dot is the G ( $\omega_{C^0}$ ) and 2D ( $\omega_{2D^0}$ ) frequencies of free-standing graphene which is undoped and unstrained. Red and blue solid lines represent varying holes and electron doping in the graphene film respectively. The space is divided into four parts by the vectors  $e_T$  and  $e_H$ . As the strain varies, the values move along the black dashed line ( $e_T$ ) from O and with varying doping levels along the magenta line ( $e_H$ ). The quadrants  $Q_2$  and  $Q_3$  are prohibited because the doping of electrons or hole should increase the G frequency.  $Q_4$  and  $Q_1$  is attributed to tensile and compressive strain respectively in combination with the doping.



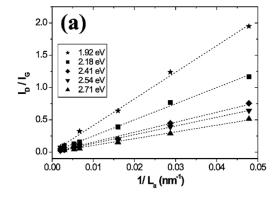
**Figure 2.18:** The parallel red dashed lines represent the different hole doping levels and the parallel black dashed lines represent the strain level. Step size of hole doping is  $5x10^{12}$ cm<sup>-2</sup> and of strain is 0.1% (adapted from ref [145]).

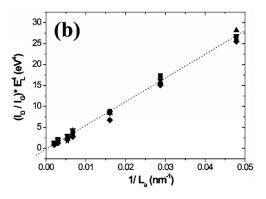
The assumption for the constant slopes of the line  $\frac{\Delta\omega_{2D}}{\Delta\omega_G}$  is valid for a wide frequency range. At lower charge density, ( $|n| \sim 10^{12} \text{ cm}^{-2}$ ) because of anomalous softening of G phonon, the variation of  $\omega_G$  with n becomes non-linear and the slope is non-linear. The red solid line in figure 2.18 (theoretical) and the blue solid line (experimental) agree with each other and are represented by  $e_H$  lines. The uniaxial strain sensitivity,  $\frac{\Delta\omega_G}{\Delta\varepsilon}$  = 23.5 cm<sup>-1</sup> %<sup>-1</sup> was calculated from Yoon et al. Each tick mark on  $e_T$  corresponds to 0.1% strain. The slope of the  $e_T$  lines  $\frac{\Delta\omega_{2D}}{\Delta\omega_G}$  = 2.2  $\pm$  0.2 was determined experimentally. For the effect

of hole doping, an average of two results were used which is the slope of the line  $e_H = \frac{\Delta \omega_{2D}}{\Delta \omega_G} = 0.7 \pm 0.05$ . Each tick mark on the  $e_H$  line corresponds to 5 x  $10^{12}$  cm<sup>-2</sup>. A point on the  $\omega_G$ - $\omega_{2D}$  space can disentangle the amount of strain and doping levels in the graphene film using Raman shifts.

## 2.2.1.2 Crystallite size

Next, the crystal size can be estimated based on the intensities of the D and G peaks. This method is more beneficial for graphene which has crystal sizes in nm scale similar to nanocrystalline graphene or polycrystalline graphene. Tuinstra and Koenig concluded that the intensity ratio of D and G peak  $\frac{I_D}{I_G}$  is inversely proportional to the crystallite size  $L_a$  of the measured sample<sup>[141]</sup>. Mernagh et al.<sup>[146]</sup> showed that the laser excitation wavelength also plays a crucial role in determining the crystallite size. Concado et al.<sup>[147]</sup> derived a general relation to calculate the crystallite size for any excitation wavelength in the visible region. The ratio of intensities was plotted against the crystallite sizes obtained by x-ray diffraction profiles for each excitation wavelength. The plot was fitted by different powers of excitation wavelength as shown in figure 2.19a to converge at one point of  $L_a$ . The best fit was obtained for fourth power of excitation energy.



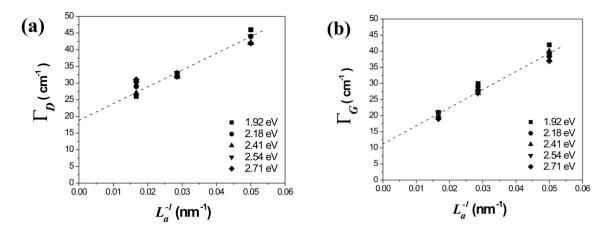


**Figure 2.19:** a) Ratio of integrated intensities vs inverse of crystallite sizes for different excitation lasers. b) Points fall in same straight line for E<sup>4</sup>dependence (adapted from ref <sup>[32]</sup>).

Next, the fourth power data is then fitted (figure 2.19b) to obtain a general relation for crystallite size calculation in terms of the wavelength of excitation wavelength in nm as follows

$$L_a(nm) = 2.4 \times 10^{-10} \times \lambda^4 \left(\frac{I_D}{I_G}\right)^{-1}$$
 (2.40)

In other work by Concado et al.<sup>[148]</sup>, the full width at half maximum (FWHM) of Raman D and G peaks were used to calculate the crystallite size in nanographites.



**Figure 2.20:** FWHM of a) D and b) G peak vs inverse of crystallite size for different excitation energies (adapted from ref [33]).

From the linear fit in figure 2.20a-b, it is clear that FWHM of D and G peaks is inversely proportional to crystallite size and independent of the excitation wavelength. This fit can be expressed as

$$FWHM = A + \frac{B}{L_a} \tag{2.41}$$

FWHM in Raman peaks is linked to the lifetime of the phonons and is inversely proportional to it. For crystallite sizes smaller than the phonon mean free path, the phonon lifetime is proportional to the crystallite size. Hence, FWHM is inversely proportional to  $L_a$ . The lifetime reduces due to phonon scattering at the defect sites<sup>[149]</sup>. Therefore, broader D, G, and 2D peaks are obtained for nanographites as they have smaller crystal size (in nanometer) compared to graphene which has sharper G and 2D peaks and larger crystal size (in micrometer). For different peaks, A and B values were calculated from the fit parameters and are tabulated in table 2.1.

Table 2.1: Linear fit parameters for D and G peaks obtained experimentally<sup>[148]</sup>

Using equation 2.41 and values of A and B from table 2.1 crystallite size can be

Peak	A (cm <sup>-1</sup> )	B (cm <sup>-1</sup> nm)
D	19	500
G	11	560

calculated.

# 2.3 Support layers and coatings

#### 2.3.1 Boron Nitride

Boron nitride, or white graphene, has been used as a ceramic for almost seven decades and integrated into graphene devices for almost a decade. Song et al. have reported an elastic modulus of 0.334 TPa and intrinsic strength of 26.3 GPa for h-BN<sup>[150]</sup>. It has a melting point of  $2973^{\circ}$ C and a thermal expansion coefficient higher than diamond. It is stable in the air till  $1000^{\circ}$ C confirming its chemical stability<sup>[151]</sup>. With a thermal conductivity of  $^{\sim}$  2000 W mK<sup>-1</sup>, it can conduct heat equivalent to graphene. Therefore, with such versatile properties, boron nitride is applied in different applications since its discovery<sup>[10]</sup>.

h-BN can boost charge carrier mobility in other 2D materials like graphene  $^{[10,152]}$  if used as the gate dielectric. Minimized Coulomb scattering, smooth surface with an absence of dangling bonds, and prevention from being contaminated are the key advantages allowing 2D materials to exhibit outstanding performance on h-BN $^{[151]}$ . Boron nitride has  $sp^2$  hybridized, equally spaced, boron and nitrogen atoms with a structure analogous to graphene honeycomb structure. It has lattice parameters, a=0.25 nm and c=0.67 nm and interlayer spacing of 0.33 nm. The in-plane bonds are covalent and highly polarised. Interlayer, van der Waal bonds play a major role. It has a band-gap of  $^{\sim}6$  eV owing to large asymmetry in sublattices. The close lattice parameters of graphene and h-BN with slight lattice mismatch of  $^{\sim}1.7\%^{[153]}$  allow excellent interaction and opens possibilities for various device applications.

2D h-BN is an insulator and transparent that can work as a gate dielectric as well as an encapsulant with excellent chemical degradation resistance. Despite remarkable properties from the applications point of view, the following challenges persist:

- Large-scale synthesis and transfer techniques
- Combination with other nanomaterials

Recently progresses have been made to address these challenges.

Exfoliation of h-BN crystal gives the best quality, contamination-free thin films. The adhesive force of tape is used to physically cleave out boron nitride films<sup>[154]</sup>. Another exfoliation pathway is using chemical reactions achieved by Han et al<sup>[155]</sup>. Scalability is an issue in both processes and limits its industrial applications. CVD process is promising as it allows large-area synthesis and can be controlled for monolayer or multi-layered

growth. Precursors for CVD growth that have been extensively used in past times for h-BN synthesis are borazine<sup>[150][11][156]</sup> and ammonia borane<sup>[157][158]</sup>. Lower temperature (<1500°C) synthesis requires a catalyst to activate the precursor, discussed later in chapter 4. The drawback of h-BN synthesis on a catalyst surface is the requirement for the transfer process after synthesis. This makes the process complicated and can potentially lead to many defects such as wrinkles, film breaking, and contamination. Therefore, for a clean and easy process, catalyst substrate should be avoided. Growing h-BN thin films directly on a dielectric substrate is a better alternative for transistor applications<sup>[156]</sup>.

Integrating h-BN with other nanomaterials is the next challenge that has been addressed by the research community. The pioneering study of graphene and h-BN heterostructure was a water-based transfer process<sup>[10]</sup>. Recently, the dry transfer method by using polypropylene carbonate (PPC) polymer on a polydimethylsiloxane (PDMS) elastomer stamp <sup>[159–161]</sup> was demonstrated. Okada et al. used CVD to grow Tungsten disulfide (WS<sub>2</sub>) directly onto h-BN/SiO<sub>2</sub><sup>[162]</sup>. h-BN in combination with CNTs has proved to be an interesting material to study photonics<sup>[163]</sup> as well. Baumgartner et al. reported carbon nanotubes quantum dots on flakes of h-BN<sup>[164]</sup>. Huang et al. reported a better current carrying capacity for CNT devices embedded in h-BN flakes<sup>[165]</sup>. Thus, it can be concluded that h-BN is a promising substrate for a variety of nanoscale electronics.

Although not implemented by the CNT community, integration of h-BN as a gate dielectric can eliminate one of the detrimental problems for CNT transistors that is hysteresis. This work captures insights into the synthesis process of h-BN using borazine and aims to address the long-standing issue of hysteresis in the CNT transistors.

## 2.3.2 Ultra-thin glass

Ultra-thin glass is a type of glass that has thicknesses in hundreds of microns. Many companies like Schott and Corning already developed technologies to fabricate glasses as thin as 30 microns. The technology used by Schott is called as down draw method. In this work, AF 32 eco thin glass was used which was acquired from Schott. Some of the important properties are mentioned in a tabular form below in table 2.2.

Table 2.2: Important properties of ultra-thin glass source: Schott.de

Properties	Values
Surface roughness	< 1 nm
Thickness	0.05 mm
Glass transition temperature	714°C
Luminous transmittance	91.9%

The low roughness and high transmittance make it an excellent choice for the fabrication of transparent strain sensors. Some of the advantages of this substrate are:

- The coefficient of thermal expansion is similar to silicon
- Temperature resistance up to 600°C

## 2.3.2.1 Stress in two-point bending of ultra-thin glass

There are various ways to impart stress to a thin film on a flexible substrate. Two-point, three-point, or four-point bending fixtures are the prominent ones. Gulati et al. in their work studied the stress distribution of two-point bending fixtures in ultra-thin glass<sup>[166]</sup>. Prominent advantages for the use of ultra-thin flexible glass are:

- Ease of building and operation of the setup
- Uniform stress across the width substrate
- Bending without contacting the surface of the substrate

The stress in the ultra-thin glass in a two-point bending setup is at maximum at the midlength and is given by [167]

$$\sigma_{max} = 1.198 \frac{Et}{(D-t)} \tag{2.42}$$

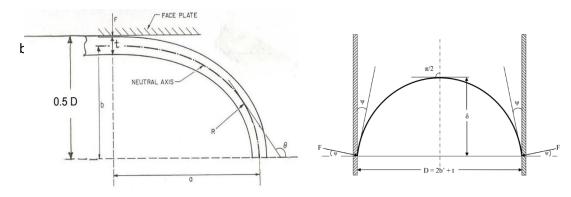
Here E is the young's modulus, t is the thickness and D is the distance between the two bending plates. The value decreases with moving to either side, based on the angle between the tangent and the normal at the center as shown in figure 2.21 and is given by the following equation 2.43

$$\sigma = \sigma_{max} \sqrt{\sin\theta} \tag{2.43}$$

For substrates (<5mm), the angle  $\psi$  between the substrate and the plates comes into account:

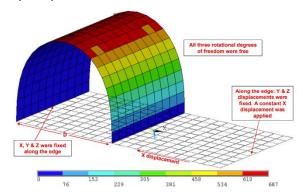
$$\sigma_{max} = 1.198 \frac{Et}{(D-t)} \sqrt{\cos\psi}$$
 (2.44)

$$\sigma = \sigma_{max} \sqrt{\sin\theta} \sqrt{\cos\psi} \tag{2.45}$$



**Figure 2.21:** a) Schematic of two-point bending test b) A short glass substrate with angle  $\psi$  at the contacts (adapted from ref <sup>[47]</sup>).

Finite element method (FEM) simulations show that the stress in ultra-thin glass in a



**Figure 2.22:** Stress profile in ultra-thin glass film under two point bending calculated by finite element analysis (adapted from  $ref^{[47]}$ ).

two-point bending fixture is maximum at the middle area of the substrate (figure 2.22). The stress then reduces to a minimum value at the ends<sup>[166]</sup>.

### 2.3.3 Teflon AF 2400

Teflon, the chemical name of PTFE, commercial name Teflon AF 2400, is a family of amorphous fluoroplastics. It is an insulator, chemical resistant, and withstands higher

temperatures. It has a high gas permeability, high creep resistance, and very low thermal conductivity. Among fluoroplastics, Teflon has the lowest dielectric constant and refractive index<sup>[168]</sup>. Some of its properties have been tabulated below in table 2.3.

**Table 2.3**: Properties of Teflon AF2400. Important barrier properties are marked blue.

Properties	Values
Dielectric constant	1.9 kV/0.1 mm
Optical transmission	>95%
Refractive index	1.29
Yield strength (MPa)	26.4 @ 25°C
	8.7 @ 220°C
Tensile modulus (GPa)	1.6
Contact angle with water (°)	105
Gas permeability (Barrer)	H <sub>2</sub> O = 4026
	N <sub>2</sub> = 490
	O <sub>2</sub> = 990
	CO <sub>2</sub> = 2800
Water absorption	<0.01 %
Glass transition temperature (Tg) (°C)	240

# 2.3.4 Metal-organic frameworks (MOFs)

MOFs is a group of crystalline materials consisting of metal nodes and organic ligands (linkers) connected by coordination bonds. Nano-porous structures with a controlled environment are the key advantages of using this class of material. These enable a wide range of practical applications in different physical or chemical environments<sup>[169]</sup>.

Virtually, an infinite number of MOFs are possible based on different combinations of the organic linker and metal nodes. A detailed review was published in 2017<sup>[170]</sup>. MOFs have up to 90% of free volume and a large surface area makes them an ideal candidate for gas storage for example hydrogen and methane, and aid for separation<sup>[171][172]</sup>. The pore sizes can be tuned along with the surface properties allowing to tether the requirements based on MOFs.

MOFs possess a range of interesting properties. For instance, they can be an insulator or a semiconductor  $^{[173]}$ . They can also be magnetic or find applications in photonics  $^{[174][175]}$ . Therefore, to understand the versatility of this material few reports on its properties are briefly discussed below.

- <u>Electrical properties</u>: Normally, MOFs are electrical insulators, but there have been recent reports on semiconductor MOFs. The first conclusive result on semiconductor property in MOFs was on Cu[Ni(PDT)<sub>2</sub>] with p-type majority charge carriers and optical band gap of 2eV<sup>[176]</sup>. Another example is poly(Ni 1,1,2,2-ethenetetrathiolate), a nonporous MOF with n-type semiconductor properties<sup>[170]</sup>. There are three charge transport mechanisms reported: bond-conduction, space conduction, and through guest molecules. Discussion about these mechanisms is beyond the scope of this thesis.
- <u>Conduction of ions:</u> The nano-porous structure allows the separation and transfer of ions easily. Most reports in MOFs are focused on proton conduction. The proton transfer occurs by "vehicle mechanism" wherein hydrogen bonds play an important role in aqueous media<sup>[177,178]</sup>. Few studies have focused on Li<sup>+1</sup> transfer as a prospective application in Li-ion batteries. MOFs are also advantageous for large-sized and multivalent metal ion transfer because of their permanent porous structure<sup>[179]</sup>.
- <u>Light emission and energy harvesting:</u> The organic-inorganic combinations of MOFs allow tunability of light emission and detection. The guest molecules in pores, organic linkers, and metal nodes can all act as a light-harvesting tool. They have been implemented in photovoltaics. The concept to use in photovoltaics is that the MOFs act as electron donor-acceptor and guest molecules in the pores for hole transfer<sup>[180]</sup>. The application to light-emitting devices has been realized as well, where the engineering of different components of MOF can be done based on required applications. For example, a luminescent guest molecule can be infiltrated into the pores according to the wavelength of interest<sup>[181]</sup>. Long et al. reported on capturing carbon dioxide for clean energy and the environment<sup>[182]</sup> and Chen et al. demonstrated the prospect of storing hydrogen and methane<sup>[183]</sup>.

# 3 Experimental Methods

This chapter is divided into two parts. The first part describes the different experimental methods used in the thesis. The second part explains few characterization tools. Making of in-house built piezoresistance setup is explained including the dimensions of building blocks.

## 3.1 Device fabrication

## 3.1.1 Synthesis of h-BN and NCG

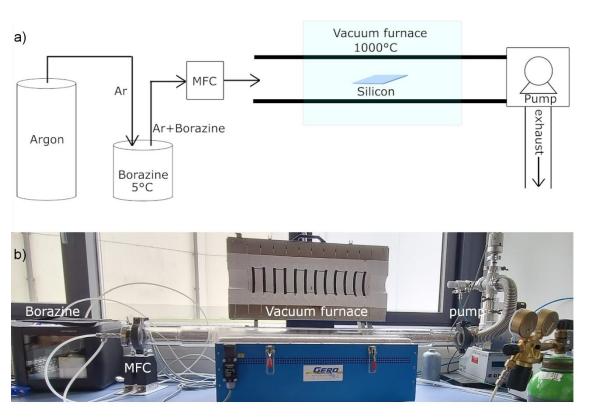


Figure 3.1: a) Schematic of the experimental setup, and b) corresponding image.

The experimental setup requires an inert atmosphere or vacuum for the synthesis of h-BN and NCG thin films. A cylindrical vacuum furnace that can be heated up to 1000°C was used. A stainless-steel bubbler (100 ml) filled with borazine (5 g) (Katchem, Czech

Republic) was connected to an Ar gas bottle (Linde, purity 5N), and to the high-vacuum oven (Gero, heating zone diameter: 7 cm, length: 65 cm) via a mass flow controller (MKS 1179B). The process parameters which can be controlled are pressure, temperature, and flow rate of borazine. Figure 3.1 shows the schematic and actual experimental setup used for h-BN synthesis.

## 3.1.1.1 Calculations on the mass flow rate of borazine for h-BN synthesis

A mass flow controller (MFC) was used to limit the total mass of a gas flowing through it at a certain temperature and pressure. It is calibrated for standard temperature and pressure (STP) conditions (0°C and 1 atm.). Mass flow is a dynamic quantity and is measured in g/min. At different working conditions than STP, the actual mass flow will be different for the same volumetric flow. Hence calculations for sccm measurements for an MFC must be done for mass flow rate rather than volumetric flow rate.

For 1 sccm flow rate of nitrogen ( $N_2$ ), volume flow rate  $\dot{V}$  is:

 $\dot{V} = 1 \text{ sccm} = 1 \text{ cm}^3 \text{ min}^{-1}$ 

Density ( $\rho$ ) of N<sub>2</sub> = 1.251 g l<sup>-1</sup>

Mass in 1 mole (m) of  $N_2 = 28 g$ 

Number of moles (n) = 1

Pressure (P) = 1 atm

Gas constant (R) =  $82.1 \text{ cm}^3 \text{ atm mole}^{-1} \text{ K}^{-1}$ 

Temperature (T) = 273.15 K

Mass flow of nitrogen =

$$\dot{m} = \dot{V} \, \frac{mp}{nRT} \tag{3.1}$$

 $\dot{m} = 1.2 \text{ x } 10^{-3} \text{ g min}^{-1}$ 

Borazine was carried from the bubbler to the reaction chamber using nitrogen as a carrier gas.

Assuming equilibria conditions, the vapor pressure at 273.25 K can be calculated:

log p(mm) = 6.39278 - (919.131) / (T-67.198)

```
p = 84.4 \text{ mm Hg} = 84.4 / 760 \text{ atm} = 0.11 \text{ atm}
```

Therefore, inside the bubbler nitrogen will be at 1 atm pressure and borazine at 0.11 atm pressure. Considering Dalton's law of partial pressure, for 1 sccm total flow, 0.11 sccm is the flow of borazine.

```
\dot{V} = 0.11 sccm 
Mass in 1 mole of borazine = 80.4 g 
Hence, using equation 3.1, mass flow rate = \dot{m} = 0.11 × (80.4 ×1) / (1 ×82.1×273.15)
```

## 3.1.2 Patterning with EBL

 $= 4 \times 10^{-4} \text{ g min}^{-1}$ 

Electron beam lithography (EBL) is a device fabrication technique to pattern structures in nanometer scale. A beam of the electron is focused on a material that is electron sensitive (resist) which changes the property of the resist which affects its solubility. One of the prominent e-beam resists is PMMA which was discovered by Hatzakis in 1969<sup>[184]</sup>. The SEM is modified to be used for EBL. The difference between an SEM and EBL is a pattern generator. In EBL, the pattern generator scans the beam onto the resist coated sample following the instructions fed into the pattern generator.

For the devices fabricated in this work, PMMA 950k A4.5 from Microresist was used. After spin-coating PMMA, a post-annealing step of 3 min at 150°C was done. The patterning step was performed at 30 kV acceleration voltage and 1.8 mm working distance.

## 3.1.3 Deposition of CNTs with dielectrophoresis

Force exerted on dielectric particles under a non-uniform electric field is known as dielectrophoresis (DEP). This process can be used to deposit aligned SWCNTs onto the electrodes. The imbalanced forces exerted on induced dipoles of CNTs can result in their movement under the applied electric field<sup>[185]</sup>. A recent study on DEP principles has been reported by Li et al.<sup>[186]</sup> The magnitude of the force experienced by the nanotubes is proportional to the difference between the dielectric constants of nanotubes and the dispersion medium<sup>[187]</sup>. The force also depends on the parameters like length and diameter of the tube<sup>[187]</sup>. With the high dielectric constant of metallic tubes compared to semiconducting nanotubes, they have a larger affinity to move towards the applied

electric field. Krupke et al. used this technique to separate metallic and semiconducting nanotubes<sup>[188]</sup>. Details about the parameters for SWCNT deposition in thesis is included in chapter 4.

## 3.2 Characterization tools

#### 3.2.1 Electrical measurement

#### 3.2.1.1 Transport measurement

All the transport measurements were conducted using an Agilent 4155C semiconductor parameter analyzer and a probe station with TRIAX probes and a current detection limit of 30 fA. The transport was measured by sweeping silicon back gate (Chapter 4 and 5), and ionic liquid top gate (Chapter 6). The gate windows for measuring CNT-FET transport were kept between -5 V to 5 V and for the ionic liquid top gate NCG-FET between -1 V to 1 V. For GFETs, the transport measurement gate window was kept -10 V to 100 V. Calculations of parameters from transport measurements, for example mobility, is explained in Chapter 2.

#### 3.2.1.2 Sheet resistance measurement

#### 3.2.1.2.1 Two-point probe

Current is applied by two probes and the same probes are used to read the potential drop across the device under test. This measurement can be used to determine the contact resistance between metal and the channel using the scaling law. The y-intercept of the plot of resistance vs channel length gives the value of contact resistance.

### 3.2.1.2.2 Four-point probe

Separate pairs of probes are used to apply current and sense voltage drops. This method is more accurate than the conventional two-terminal measurements because it eliminates the role of contact resistances.

A simple approach to measuring sheet resistance using four-point is fabricating a structure where the direction of flow of current is fixed. The following figure 3.2 demonstrates it.

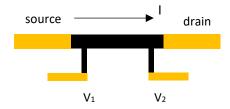


Figure 3.2: Device structure for four-point sheet resistance measurement.

The current flows between the outer two electrodes (main lines) and voltage drops are measured between the inner two electrodes (sense lines). Using  $R = \frac{V}{I}$ , resistance and the resistivity of the thin film are calculated. Sheet resistance is then given by the formula  $R_s = \rho/t$ , where t is the thickness of the film.

#### 3.2.1.2.3 Van der Pauw method:

This technique can be used to measure the sheet resistance of thin films independent of their shape. Montgomery method is an extension of the Van der Pauw method for measuring the sheet resistance of anisotropic materials<sup>[185]</sup>.

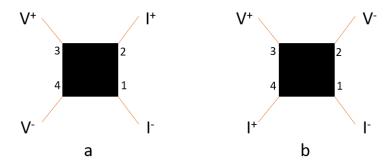


Figure 3.3: Measurement configurations a and b in the Van-der Pauw method.

Sample must have a homogeneous surface; contacts should have a small area of contact. The resistivity of the film is then calculated using the equation

$$\rho = R_s \times t \tag{3.2}$$

As an example, a square structure demonstrating two configuration schemes is shown in figure 3.3. For configuration a, a current is flown through one edge of the device structure ( $I_{12}$ ) and the voltage drop is measured along the opposite edge ( $V_{34}$ ). The voltage drop value is utilized to calculate the resistance  $R_{1234}$ . Similar measurements are repeated in configuration b to calculate  $R_{4123}$ . Sheet resistance is then calculated by the Van-der Pauw equation

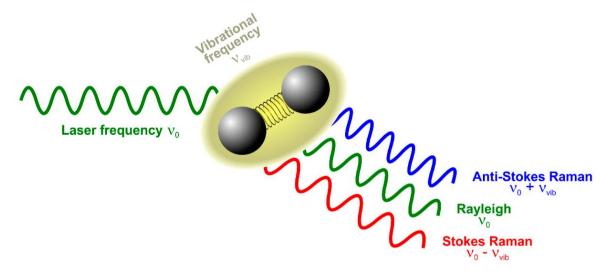
$$e^{-\frac{\pi R_{1234}}{R_S}} + e^{-\frac{\pi R_{4123}}{R_S}} = 1 \tag{3.3}$$

Iterations are needed to solve equation 3.3. Therefore, for ease of calculation, a symmetrical structure, where  $R_{horizontal} = R_{vertical} = R$ , can be used which reduces the Van der Pauw equation to

$$R_{\rm S} = \pi R / \ln 2 \tag{3.4}$$

## 3.2.2 Raman Microscopy

Raman spectroscopy was first observed by the physicist C.V. Raman in 1928<sup>[189]</sup>. The technique uses vibrational modes of the materials as its fingerprint. Inelastic scattering of photons is known as Raman scattering. It can be classified into a Stokes (longer wavelength) or an Anti-stokes (shorter wavelength) line. This increase or decrease in the wavelength of the scattered radiation is attributed to the vibrational energy state of the system under test (figure 3.4).



**Figure 3.4:** An excitation laser beam  $(v_0)$  interacts with a molecule with vibrational frequency  $v_{vib}$  generating a Rayleigh scattered light  $(v_0)$ , Stokes Raman scattering  $(v_0 - v_{vib})$ , and an Anti-Stokes Raman scattered light  $(v_0 + v_{vib})$  (adapted from ref [189]).

The Stokes and Anti-stokes spectra are spaced equally from the Rayleigh (elastic) scattering line because one quantum of energy is gained (Anti-stokes) or lost (Stokes). The intensity of the Anti-stokes scattering is weaker than Stokes because only the molecules in the excited vibrational energy states can give rise to Anti-stokes. Rayleigh scattering has the highest intensity.

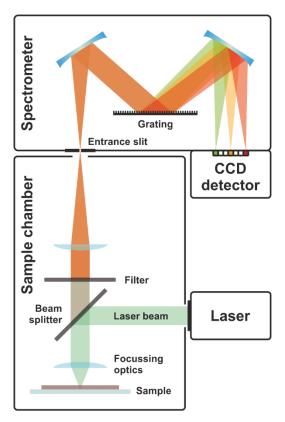


Figure 3.5: Schematic of a Raman microscope (adapted from ref [189]).

In figure 3.5, a schematic of a basic setup of Raman spectroscopy is shown. The laser light is passed through a beam splitter and focused onto the sample using the microscope objective which is also used to collect the scattered light from the sample. The filter allows only Raman scattered photons to reach the spectrometer and removes Rayleigh scattered photons. The spectrometer consists of a grating and CCD detector. The Raman scattered light falls on the grating which disperses each wavelength of light to a different angle. The dispersed light is then detected by a CCD detector. Higher grooves per mm are required for a higher resolution of detection. Gratings are "blazed" for a certain wavelength such that the efficiency of diffraction can reach up to 80%.

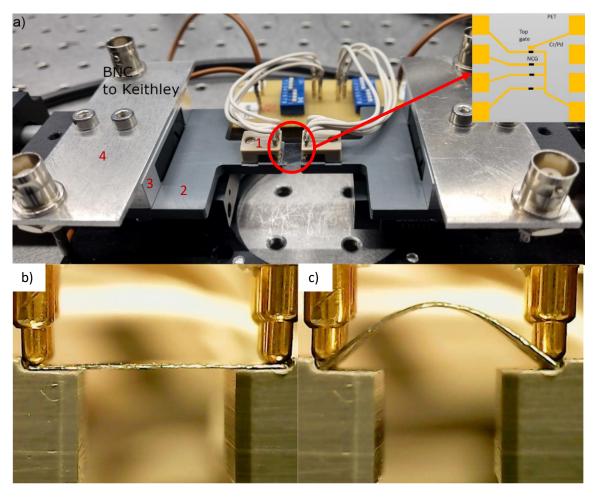
Raman spectroscopy gives results in intensity vs Raman shift. Raman shifts are usually reported in wavenumbers in the units of cm<sup>-1</sup>.

$$\Delta v = \frac{1}{\lambda_0} - \frac{1}{\lambda_1} \tag{3.5}$$

Here  $\lambda_0$  is the excitation wavelength and  $\lambda_1$  is the Raman scattered light wavelength. Raman spectroscopy conducted in the thesis was measured by Renishaw inVia Raman

microscope. The measurements conditions are included within the experimental chapters.

## 3.2.3 Piezoresistance setup



**Figure 3.6:** a) Piezoresistance setup with sample loaded (red circle) and the inset shows device structure b) Side view of the substrate and the spring contacts at zero stress and c) at a finite stress value.

**Table 3.1:** Few Parameters of Standa 8MT173-20 translation stage.

Travel range	20 mm
Lead Screw pitch	0.25 mm
Resolution in full step	1.25 um
Maximum speed	5 mm/s

This setup was built in-house using two commercially bought stepper motors and a source measure unit (SMU). The motorized translation stage 8MT173-20 was acquired

from Standa Inc for the x-axis movement control. A Keithley 2636A was used as the SMU. Some of the important configurations of the stand stepper motor are given in table 3.1.

The setup is shown in figure 3.6. A flexible substrate with structure is shown in the inset and marked in the red circle (figure 3.6a). The strain was imparted in steps by moving the stepper motor. Each step of the motor provides an x-movement of 1.25 um. The x-movement was then converted to a strain value (shown in the calculation in chapter 6). The parts built by the mechanical workshop are marked as 1, 2, 3, and 4. Diagrams of them are shown below. All dimensions marked are in mm.

#### Part number 1: Contact holder

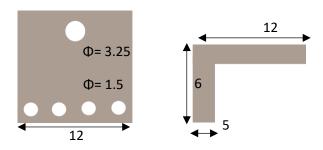


Figure 3.7: Schematic of the top (left) and side (right) view of the contact holder with dimensions in mm.

Part number 1 is the contact holder made from polyether ether ketone (PEEK). It has four holes in the front with a diameter equivalent to gold-coated spring contacts. Four spring contacts were inserted into these holes, where the frictional force inhibited the movement of spring contacts. The front end of the gold contacts touches the metal electrodes on the structure as shown in figure 3.6a. As the strain is applied on the substrate, the spring moves back keeping the contacts intact as shown in figures 3.6b and 3.6c.

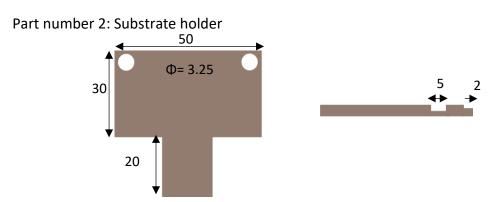
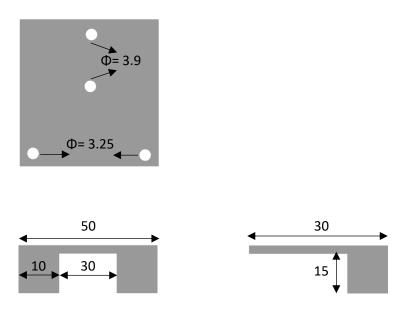


Figure 3.8: Schematic of the top (left) and side (right) view of the substrate holder with dimensions in mm.

The substrate holder has an undercut of 2mm at the front to place the sample. The 5mm undercut fixes the contact holder and inhibit its movement. The holder was made of polyvinyl chloride (PVC) to keep the structure lightweight and the undercut low in roughness. Low roughness keeps the stress on the edge of the substrate uniformly distributed and prevents premature fracture of the glass substrate.

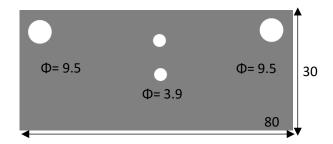
#### Part number 3: Motor connector



**Figure 3.9:** Schematic of the top (top left), front (bottom left), and the side (bottom right) view of the motor connector with dimensions in mm.

This part attaches the substrate holder to the stepper motor. The substrate holder was kept as low as possible by using the motor connector. This allows a microscope objective (FTIR and Raman) to fit at the top of the substrate. The setup with Raman measurement is shown in chapter 6. The material used was for its fabrication was aluminum.

#### Part number 4: BNC connector holder:



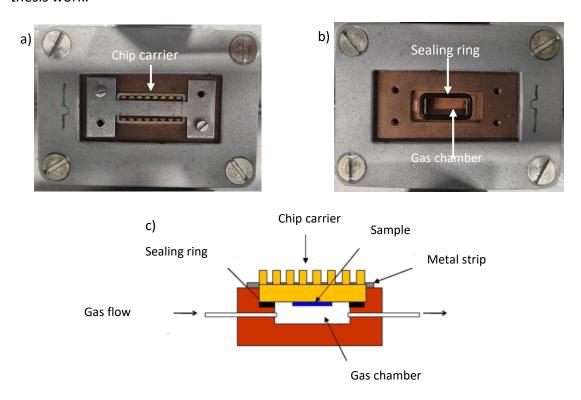
**Figure 3.10:** Schematic of the top view of BNC connector holder.

This part holds the BNC connectors on its top as shown in figure 3.6a. Metal electrodes on the substrate are connected to switchboards via copper wires for source, drain, and gate. The switchboards are soldered to BNC connectors placed on the holder. Connections to Keithley 2636A was made using these BNC connectors. Steel was used for the fabrication of this part.

The python code to control the Piezoresistance setup and Keithley is included in https://github.com/krupke-group.

## 3.2.4 Gas sensing setup

The setup was originally built by Marc Ganzhorn<sup>[3]</sup>. A modified version was used for the gas sensing experiments. The explanation and images are adapted from his diploma thesis work.



**Figure 3.11** a) Sample holder bond wired into the chip carrier and loaded in the gas chamber upside down b) Gas chamber and the sealing c) schematic of the gas chamber with the device.

In figure 3.11a, a sample loaded into the gas chamber is shown. The sample is bond wired onto a chip carrier and loaded upside down in the gas chamber. The sealing ring prevents any leakage of gas from the gas chamber (figure 3.11b). The schematic in figure 3.11c shows the gas flow direction with the sample loaded into the gas chamber.

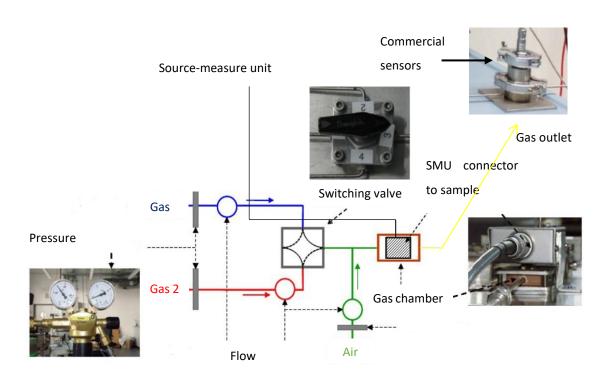


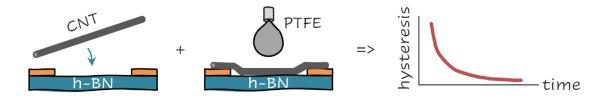
Figure 3.12: Schematic of the connection diagram of the gas sensing setup.

Figure 3.12 shows a schematic of the gas sensing setup. Gas 1 and Gas 2 flow through flowmeters which control the amount of gas flowing through the gas line. The pressure of the gas flow is controlled by a pressure controller. The electrical readout as sensing response is taken from the chip carrier on which the sample is wire bonded. The chip carrier is connected to a SMU. The gas outlet passes through another chamber which contains alcohol (MQ3) and moisture (BME280) sensors to read out as reference readings.

# 4 Vanishing Hysteresis in CNT Transistors

This chapter includes content from a research article that has been published<sup>[190]</sup>.

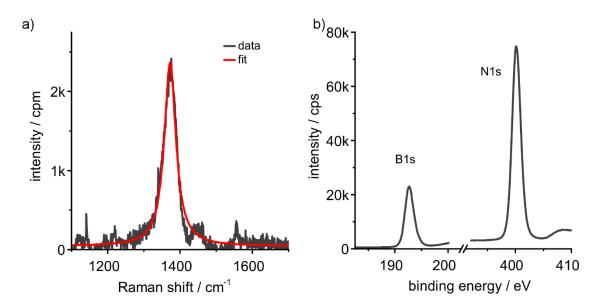
The chapter has been divided into two parts. The first part introduces the synthesis of h-BN on a dielectric substrate. The role of pressure and metal catalyst in the synthesis process was studied. The second part demonstrates a novel approach to incorporate the grown h-BN film into CNT transistors to achieve hysteresis-free operation. It is described how the bottom h-BN reduces hysteresis (charge traps) and that a top coating of the hydrophobic polymer PTFE film (or Teflon in this case) was needed to encapsulate CNTs as well as metal-CNT contacts to eliminate hysteresis. The schematic below captures the complete vision of the chapter.



# 4.1 Synthesis and characterization of Boron Nitride

The synthesis of large-area h-BN has been demonstrated in previous works by decomposition of ammonia borane or borazine on metal foils [11][150][156][191][192], dielectric surface [193][194], and by chemical conversion of graphene to h-BN [195]. In this work, to avoid any transfer process, h-BN was grown by allowing borazine to polymerize on SiO2 substrate. Borazine was chosen because of its 1:1 stoichiometric ratio and high vapor pressure at 5°C, and high chemical reactivity [11][156][196]. The process schematic is shown in figure 4.5a. The bubbler was cooled to 5°C to inhibit the premature decomposition of borazine. Boron doped silicon wafers (resistance < 0.005  $\Omega$  cm, Active Business Company) with 300 nm thermal oxide were cut into 10 x 10 mm² pieces, loaded into the center of a quartz tube (Aachener Glass, 2inch diameter, 50cm long), evacuated to 10<sup>-7</sup> mbar and ramped up to 1000°C at a rate of 10°C min<sup>-1</sup>. Argon (2 sccm) was flown through the borazine-filled bubbler into a heated quartz tube for 10-60 min, depending on targeted h-BN thickness. It has been assumed that Ar uptakes borazine at equilibrium partial pressure of 144 mbar (5°C) when streaming through the bubbler at 1 bar pressure. The calculation has been shown later in the chapter. The amount of borazine

was calculated to approximately 1 mg min<sup>-1</sup> at 2 sccm Ar flow, as shown in calculations later in the chapter. During growth, a pressure of 5 mbar was maintained. After terminating the gas flow, the sample was kept in a vacuum at  $1000^{\circ}$ C for 1 hr to improve crystallinity and to promote further dehydrogenation. Finally, the furnace was cooled to room temperature at a rate of <  $7^{\circ}$ C min<sup>-1</sup>.

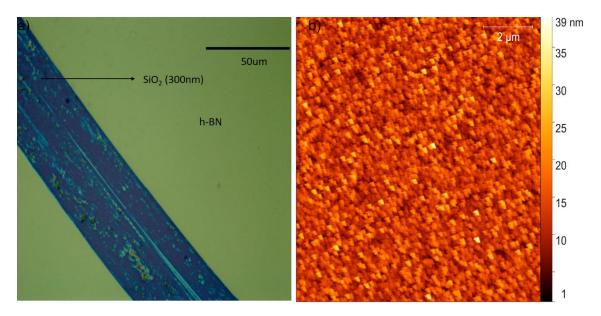


**Figure 4.1:** a) Raman data of high pressure grown h-BN on  $SiO_2/Si$ , background subtracted and fitted to a Lorentzian (peak 1372.5 cm<sup>-1</sup>, linewidth 38.7 cm<sup>-1</sup>). b) XPS of the h-BN film with the peak binding energies for B1s and N1s at 192 eV and 399.5 eV, respectively<sup>[190]</sup>.

To avoid premature polymerization of borazine to polyborazylene <sup>[197]</sup>, it was ensured to keep the gas temperature in gas pipes below 70°C. Borazine undergoes polymerization to form polyborazylene around 70°C. Between temperatures 125-300°C cross-linking of B-H and N-H occurs resulting in loss of hydrogen. The second stage of dehydrogenation occurs between 700°C - 1000°C to ultimately form boron nitride<sup>[197][198]</sup>.

Raman spectroscopy was used to confirm the presence of h-BN film (Renishaw inVia Raman spectroscopy) shown in figure 4.1a. 532nm excitation wavelength at 100x magnification with 3mW laser power was used as parameters with an integration time of 60 s for all Raman measurements on h-BN thin films. X-ray photoelectron spectroscopy (XPS) measurements were done using a Thermo Fisher's K-alpha machine. The samples were excited with a monochromatized Al K-alpha source (1486.6 eV) with an X-ray spot size of 400  $\mu$ m. The ejected photoelectrons were collected using a hemispherical 180° double focus analyzer with a 128-channel detector. Measurements were done without

the use of a charge compensation mechanism (figure 4.1b). Figures 4.2a and 4.2b show optical and atomic force micrographs of the grown film respectively. Figure 4.3 shows corresponding scanning electron micrographs.

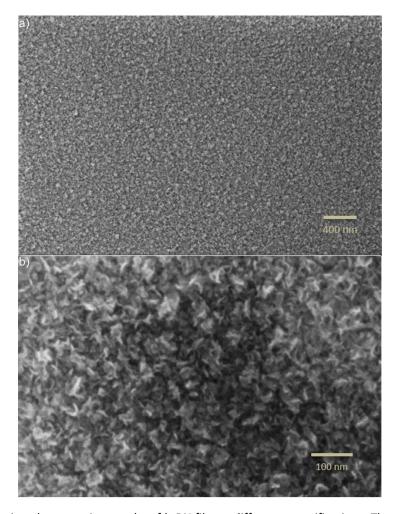


**Figure 4.2:** a) Optical image (100x) showing a scratch on h-BN film. b) Atomic force micrograph of the grown h-BN film. [190]

$$BH_3NH_3 \underset{-nH_2}{\Longleftrightarrow} (BH_2NH_2)_n \underset{-mH_2}{\Longleftrightarrow} B_xN_yH_z \underset{-kH_2}{\Longleftrightarrow} BN \tag{4.1}$$

Koepke et al. [199] have discussed the role of pressure in growing h-BN. They concluded that beyond 2 Torr (1.33 mbar) pressure the growth of h-BN on Cu is increasingly amorphous and the growth rate is uncontrolled. Behura et al. have used the same pressure range (5-10 Torr) to grow h-BN on dielectric substrates<sup>[200]</sup>. The growth temperature must be above 1170°C for a crystalline growth and can be lowered down to 1000°C in presence of transition metal catalysts<sup>[201]</sup>. Borazine undergoes dehydrogenation process twice to form the final product as h-BN. The electron pair is delocalized on nitrogen and it can act as a Lewis base in presence of a Lewis acid. The first stage of dehydrogenation (polymerization) is the rate-determining step (RDS) in the complete process of h-BN formation<sup>[202]</sup> (equation 4.1). Transition metals are a good Lewis acid as they have vacant d-orbitals to accept electron pairs and form complexes which therefore reduces the activation energy for polymerization. The reaction in the absence of a metal catalyst needs higher activation energy and proceeds slowly. Hence, at low pressure (10-6 mbar), no growth was detected on SiO<sub>2</sub>/Si substrates (shown as the yellow line in figure 4.4). It can be inferred that the residence time of the precursor

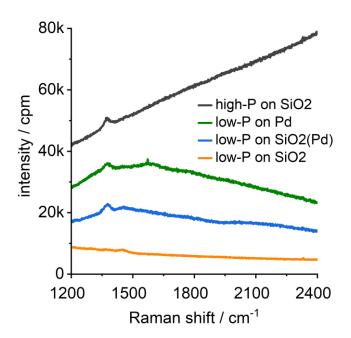
is small enough to not allow the reaction to pass the RDS, and therefore no Raman signature of growth is measured. Increasing pressure (5-10 mbar), and repeating the experiment gives a clear Raman signature of h-BN film at ~1370 cm<sup>-1</sup> (shown as the black line in figure 4.4). The increasing background is due to photoluminescence for h-BN on 300nm SiO<sub>2</sub>.



**Figure 4.3:** Scanning electron micrographs of h-BN film at different magnifications. The grown h-BN film is grainier in texture<sup>[190]</sup>.

As already known from the literature, at  $1000^{\circ}$ C, a larger crystal size is only possible in the presence of the catalyst<sup>[156]</sup>. This is also confirmed by the line width of Raman E<sub>2g</sub> which is broader (figure 4.1a, linewidth 38.7 cm<sup>-1</sup>) compared to Raman peaks of monocrystalline h-BN (15.5 cm<sup>-1</sup> for bulk)<sup>[150]</sup>. In this work, no catalyst was used and therefore can be concluded that in absence of catalyst nanometer domain size film is synthesized. At higher pressures, the growth rate becomes uncontrolled, and the film is

increasingly amorphous, disordered, and  $sp^3$ -hybridised<sup>[199]</sup> which leads to the roughness of the film confirmed by AFM (figure 4.2b) and SEM images (figure 4.3).

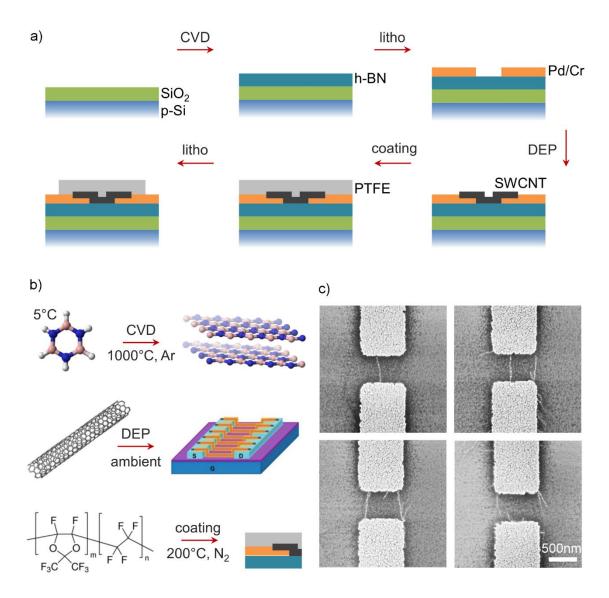


**Figure 4.4:** Raman spectra recorded after CVD. The  $E_{2g}$  Raman peak of h-BN at ~1370 cm<sup>-1</sup> is visible on bare  $SiO_2/Si$  substrates when synthesized at high pressure (5-10 mbar, black line). Synthesis at low pressure ( $10^{-6}$  mbar) yields h-BN only in the presence of Pd electrodes: on top of Pd (green line) and nearby Pd (blue line), not on bare substrates (yellow)<sup>[190]</sup>.

After establishing that the h-BN film on dielectrics can be formed at high pressures (5-10 mbar), the role of the metal as a catalyst for growth on dielectric at lower pressure was studied. Therefore, the Si/SiO<sub>2</sub> substrate with square patches of palladium metal was fabricated. With similar conditions of fabrication, but at low pressure (10<sup>-6</sup> mbar), the experiment was repeated. In this case, a Raman signal was observed on the complete dielectric surface and the metal surface (shown as blue and green curves in figure 4.4). Here, the RDS was faster because of the presence of palladium metal catalyst leading to the successful synthesis of h-BN at low pressure.

## 4.2 Device fabrication

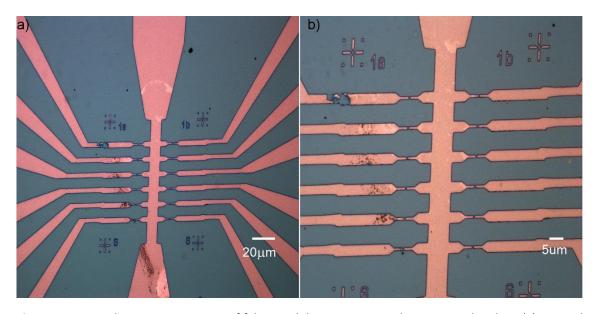
The parts included in device fabrication, and results and discussion sections have been published in a research article<sup>[190]</sup>.



**Figure 4.5:** a) Process flow comprising CVD synthesis of h-BN, deposition of SWCNTs by dielectrophoresis (DEP), spin coating of Teflon AF2400 (PTFE), and lithographic patterning of metal contacts and Teflon layer. b) Schematic on the deposition processes and conditions. c) Scanning electron micrographs of devices after deposition of (9.8) -SWCNTs and before Teflon coating. The SiO<sub>2</sub>/Si substrate was overgrown with 80 nm h-BN.<sup>[190]</sup>

The graphene community has realized that packaging graphene into flakes of h-BN eliminates doping and hysteresis, and boosts mobility, provided the h-BN layer is tens of nanometers thick<sup>[10]</sup>. The Bockrath group has similarly encapsulated SWCNT devices in h-BN flakes and reported superior current carrying capability<sup>[165]</sup>. However, the integrated nanotubes were metallic, and transistor behavior was not observed. Similarly, not for SWCNTs grown on h-BN flakes<sup>[164]</sup>. Here in this work, the target was to achieve a wafer-scalable fabrication of hysteresis-free SWCNTs transistors via encapsulation into the h-BN bottom layer and a PTFE top layer.

80nm h-BN was first overgrown on 300nm SiO<sub>2</sub>/Si substrates. 45 nm thick Pd/Cr sourcedrain electrodes with 600 nm gap size were fabricated on h-BN coated substrates by standard electron-beam lithography and metallization. (9,8) semiconducting SWCNTs were produced by selective catalyst CVD<sup>[203][204]</sup> dispersed in toluene by wrapping in poly(9,9-di-n-dodecylfluorenyl- 2,7-diyl) (PFD) and purified and length sorted by gel filtration<sup>[205]</sup>. The SWCNTs were deposited simultaneously onto 6-7 devices per chip by electric field-assisted deposition (DEP). For details regarding synthesis, purification, optical characterization, and deposition of (9,8) SWCNTs please refer to Gaulke et al<sup>[5]</sup>. Devices were then annealed at 400°C and 10<sup>-7</sup> mbar vacuum for 4 h. Electrical characterization was conducted first without PTFE top layer under ambient conditions, with an Agilent 4155C semiconductor parameter analyzer and a probe station with TRIAX probes with a current detection limit of 30 fA. Transfer characteristics were measured with back gate voltage sweeps from -5 to +5 V (step size 100 mV, scan rate 0.5 V s<sup>-1</sup>) using source-drain voltages of 0.5 V. For coating PTFE, the devices were loaded into a nitrogen glove box with a spin-coater and hotplate and annealed at 150°C for 1 h. Α solution of poly[4,5-difluoro-2,2-bis(trifluoromethyl)-1,3-dioxole-cotetrafluoroethylene] (1 g) (Teflon AF-2400, Sigma-Aldrich) dissolved in Fluorinert FC-40 (100 ml) (Sigma-Aldrich) was prepared by stirring the solution for 2 weeks at 60°C. Residual nanometer-sized PTFE particles were removed by ultracentrifugation for 1 h at 40000 rpm. The devices were loaded to the spin-coater and the purified 1 % PTFE solution (10 µl) was drop-casted onto the sample surface. For 30 and 45 nm thick PTFE layers, samples were rotated for 60 s at 3000 and 1000 rpm, respectively. A thickness of 80 nm was obtained by spin-casting two times, at 3000 and 1000 rpm for 60 s each. The samples were then heated, still in nitrogen atmosphere, at 100°C for 5 min, 150°C for 5min, and 200°C for 10h. Finally, the samples were cooled down to room temperature and unloaded from the glovebox. PTFE was removed from the metal contact pads by direct electron beam pattering using an exposure dose of 10 mC cm<sup>-2</sup> at 10 keV beam energy<sup>[206]</sup>. Subsequent electrical characterizations were done under conditions outlined before. At the end of the fabrication process the SWCNT devices were encapsulated between the h-BN bottom and PTFE top layer (Figure 4.5a). SWCNTs within completed devices cannot be imaged due to the PTFE coating, therefore shown in figure 4.5c are scanning electron micrographs of 4 representative SWCNT devices without PTFE coating. The images were recorded after nanotube dielectrophoresis. Typically, 1-5 SWCNTs are contacted and visible in the gap region. The underlying h-BN layer was characterized by Raman spectroscopy after CVD synthesis. Figures 4.6a-b show an overview of the fabricated device structure at 50x and 100x optical magnifications respectively.

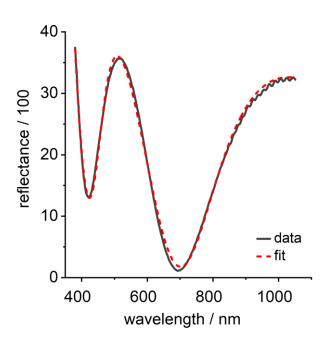


**Figure 4.6:** Optical microscopy images of fabricated device structure showing metal pads at (a) 50x and (b) 100x magnifications. The channel length is 600nm. Device 1 metal damaged while measurements.

### 4.3 Results and discussions

The complete stack of PTFE/h-BN/SiO<sub>2</sub>/Si was characterized by reflectance spectroscopy to analyze the thicknesses of individual layers via the transfer matrix method, as shown in figure 4.7. Refractive indices of PTFE AF-2400<sup>[207]</sup> and h-BN<sup>[208]</sup>, and respective nominal thicknesses of the layers were used as starting values in the fitting procedure. The h-BN layers are homogenous over large areas, as shown in the optical image in figure 4.2a. However, the layers are not single crystalline but have a grainy structure, which gives rise to a broader linewidth of the Raman  $E_{2g}$  peak (38.7 cm<sup>-1</sup>) as compared to monocrystalline h-BN (15.5 cm<sup>-1</sup> for bulk) and is visible in the scanning electron micrographs (figure 4.3) and atomic force micrographs (figure 4.2b). The gate-voltage hysteresis observed in the transconductance curves of h-BN/SiO<sub>2</sub>/Si supported devices

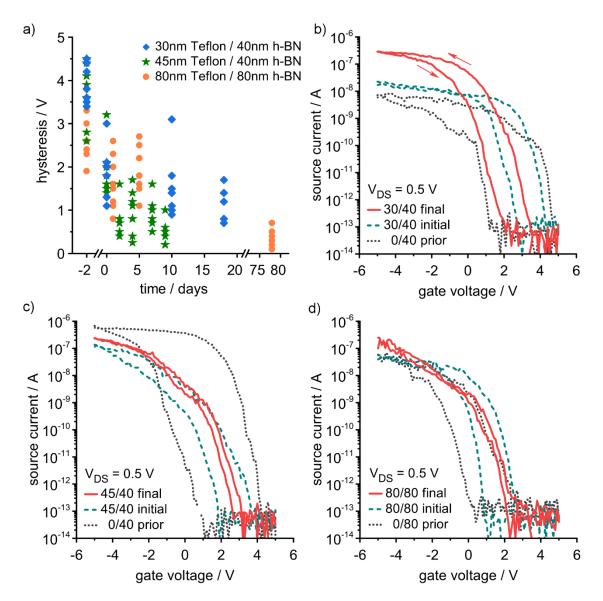
before and after Teflon coating is now discussed. (9,8) SWCNTs were embedded in three combinations of layer thicknesses: 30nm Teflon / 40nm h-BN, 45nm Teflon / 40 nm h-BN, and 80 nm Teflon / 80nm h-BN.



**Figure 4.7:** Reflectance spectra of a complete stack of 30nm Teflon / 40nm h-BN / 300nm SiO<sub>2</sub> / Si determined by fitting with the transfer matrix method<sup>[190]</sup>.

Figure 4.8a summarizes the temporal evolution of hysteresis in a total of 21 devices. The largest hysteresis values among all layer combinations were observed prior to Teflon (on h-BN) deposition at t =-2 days ranging between 2-4.5 V. This is significantly smaller than 4.5-6 V range for SWCNT devices fabricated on bare Si/SiO2 (figure 4.11) and shows that h-BN without Teflon coating yields devices with a reduced hysteresis. This conclusion is supported by a quantitative comparison of the number of charge traps at the end of the chapter. An additional reduction of hysteresis to 1-3 V was then observed after Teflon AF coating at t = 0, as was expected due to the water-repelling nature of Teflon. However, an unexpected further significant reduction was observed over a timescale of days to weeks, in particular for 45 nm and 80 nm thick Teflon layers. Depending on the waiting time and layer combination, hysteresis becomes eventually smaller than 1 V. A direct comparison of the transconductance curves for representative devices of three layers combinations is given in Figure 4.8b-d. The results resemble a similarity to Ha et al.[8] work, where a vanishing hysteresis was obtained after Teflon AF coating of SWCNTs on an amino-acid functionalized substrate. Teflon layers required a thickness larger than 10 nm to be effective, however, the reduction in hysteresis was instantaneous and did

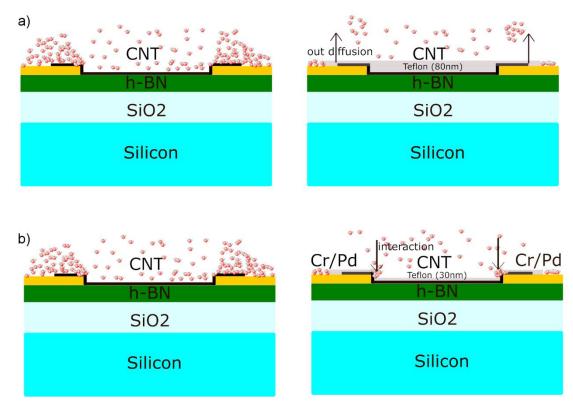
not show a time dependence, likewise for a complete encapsulation in Teflon AF as shown by Lefebvre et al<sup>[73]</sup>.



**Figure 4.8**: a) Temporal evolution of hysteresis in transconductance curves for 21 devices. The SWCNT devices were encapsulated in three combinations of Teflon and h-BN layer thicknesses, as indicated. Also shown are results taken 2 days before the Teflon deposition at t=0. (b-c) Corresponding transconductance curves of representative devices, taken before Teflon deposition (prior), directly after Teflon deposition (initial), and after a week of storage under ambient conditions (final). Current detection limit is 30 fA<sup>[190]</sup>.

Here in this work, a minimum Teflon thickness of 45 nm is required to coat not only the SWCNTs segment lying flat on the h-BN surface but also the nanotube segment on the top of metal electrodes. Concerning the origin of the temporal evolution of hysteresis,

it is the long-timescale that points to a diffusion process – likely of water molecules. Interestingly, transconductance traces show that the threshold voltage is stable during the hysteresis reduction; however, the on-state hole current systematically increases with time after Teflon coating. This shows that the Schottky barrier reduction for holes is correlated with the reduction of hysteresis, possibly via surface dipoles trapped in the contact area between Pd electrodes and SWCNTs. Indeed, an obvious difference to previous works is that in this work SWCNTs were deposited on top of the Pd electrodes and hence the contact interface was inevitably exposed to humidity. Since water molecules are known to adsorb on Pd surfaces<sup>[70]</sup> and desorption of water from Pd surfaces is slow below 450°C<sup>[209]</sup>, it can be speculated that slow repelling of water molecules from the contact area is causing the time dependence.



**Figure 4.9:** a), b) Left image shows the initial condition of the fabricated device in terms of water adsorption. The water molecules are heavily adsorbed onto the metal and CNT contacts as well onto the CNTs. a) Right image shows an encapsulated CNTs by 80 nm Teflon and bottom h-BN. As the thickness of Teflon (80 nm) is larger than electrodes (45 nm), interaction is completely cut-off and the trapped water molecules diffuse out through Teflon. b) Right image shows a thickness of Teflon (30 nm) less than the electrodes, which leaves some area on the contacts for water interaction to the contacts and in turn contributes to hysteresis.

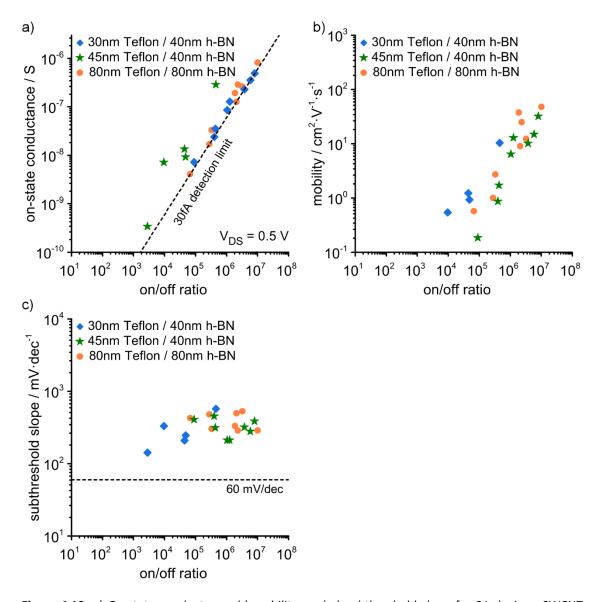
To gain further understanding, the mechanism schematic is shown in figure 4.9. Initially, without the Teflon coating, water molecules are adsorbed on nanotubes as well as on the metal electrodes (left figures in figure 4.9a and b), but not on the h-BN surface because of the absence of dangling bonds and charge traps. After a Teflon coating of 80 nm, the interaction of water with the nanotubes is completely cut off, however, adsorbed water onto the metal electrode, and nanotubes get trapped. This contributes towards hysteresis shown in transconductance curves at t = 0 (in figure 4.8b-d). Teflon is a good permeable membrane that allows water trapped to diffuse out, and due to its extremely good hydrophobicity doesn't allow re-adsorption of water molecules on CNTs and metal-CNT contacts, known as the lotus effect in Teflon<sup>[210]</sup>. The out-diffusion of water is a slow process and a waiting time is required to achieve a hysteresis-free device (shown in figure 4.8a). Thicknesses (30 nm) less than metal electrodes (45 nm) are not beneficial to achieve this kind of environment, because it does not completely cover the metal-CNT contacts (figure 4.9b). The hysteresis value for 30 nm Teflon coating (figure 4.8b) of course shows a smaller value compared to values without Teflon coating. But, values of the order of 0.1 V were not achieved. The temporal evolution (blue diamonds in figure 4.8a) of 30 nm shows a similar trend of decreasing hysteresis with time.

The direction of the hysteresis loop in figure 4.8b-d is counter-clockwise and further evidence that the source of hysteresis at low gate sweeping ranges is due to water molecules and associated charges, which redistribute in response to gate voltage<sup>[211]</sup>. At larger gate voltage sweeps hysteresis may also depend on charge traps in the dielectric, however, in light of the observed reduction of hysteresis with time it implies, that hysteresis in the devices must be caused by water-related charge traps that are mobile at room temperature and traps in h-BN are absent.

The low hysteresis of 0.1 V, observed for devices embedded in 40 nm h-BN / 45 nm Teflon and 80 nm h-BN / 80 nm Teflon on  $SiO_2$  substrates is comparable to devices with CNTs deposited on SAM passivated oxides<sup>[71][74]</sup>, devices with CNTs overgrown by a top gate oxide<sup>[75]</sup>, and devices passivation with fluoropolymers<sup>[81][212][8]</sup>. Table 4.1 compares the hysteresis values of this work to the available literature.

**Table 4.1:** Comparison of the hysteresis values and the conditions used for achieving lower hysteresis values. The blue-colored row gives the values in this work<sup>[190]</sup>.

Gate sweep	Hysteresis	Dielectric	Effective Dielectric	CNT Condition
window (E)	(V)	thickness	constant	
-3 to 3 V	2.2	SiO <sub>2</sub> -	SiO <sub>2</sub> +air = 2.13	Suspended (CNT on
(3.14 MV/m)		500nm		top of electrodes) <sup>[69]</sup>
		Air-		
		200nm		
-5 to 5 V	<0.1	SiO <sub>2</sub> –	SiO <sub>2</sub> =3.9	PTFE encapsulation
(2 MV/m)		50nm		Dielectric Surface
				passivation
				(Electrodes on top of
				CNT film) <sup>[8]</sup>
-10 to 10 V	5	SiO <sub>2</sub> -	SiO <sub>2</sub> +	Hydrophobic film on
(16.7 MV/m)		200nm	Parylene (3.15) =	dielectric (CNT on top
		Parylene-	3.61	of electrodes) <sup>[62]</sup>
		100nm		
-1 to 1 V	0.1	SiO <sub>2</sub> -4nm	3.3	SAM on
(16.7 MV/m)		SAM-2nm		SiO <sub>2</sub> (Electrodes on
				top of CNTs) <sup>[71]</sup>
-10 to 10 V	8	SiO <sub>2</sub> -	SiO <sub>2</sub> =3.9	PMMA passivation
(80 MV/m)		100nm		on top of
				CNT (Electrodes on
				top of CNT) <sup>[213]</sup>
-5 to 5 V	0.5	ZrO <sub>2</sub> -	ZrO <sub>2</sub> =10	PVDF-
(5.55 MV/m)		90nm		TrFE passivation on
				top of CNT (CNT film
				on top of
				electrodes) <sup>[212]</sup>
-1.5 to 3 V	0.3	TiO <sub>2</sub> -	TiO <sub>2</sub> =22	TiO₂ top gate
(10 MV/m)		30nm		(Electrodes on top of
				CNT film) <sup>[75]</sup>
-5 to 5 V	0.1	h-BN-	h-BN = 3.29	PTFE + h-BN
(0.263 MV/m)		80nm	h-BN+SiO <sub>2</sub> = 3.75	(CNT on top of
		SiO <sub>2</sub> -		electrodes) <sup>[190]</sup>
		300nm		



**Figure 4.10:** a) On-state conductance, b) mobility, and c) subthreshold slope for 21 devices. SWCNTs devices were encapsulated in three combinations of h-BN and Teflon layer thicknesses, as indicated. The data has been acquired from the transconductance curves measured after a week of storage under ambient conditions. In a) the dashed line represents 30-fA detection limit of the setup, and in c) the theoretical subthreshold slope limit at 300  $K^{[190]}$ .

In summary, devices with electrodes on top of CNTs placed on SAM passivated  $SiO_2$  require additional hydrophobic packaging to remove hysteresis, [8] whereas devices with electrodes on top of CNTs based on SAM passivated  $Al_2O_3$  show long-term hysteresis-free behavior without additional packaging. [214] On the other hand for devices with CNTs on top of electrodes, hydrophobic packaging seems to be mandatory to eliminate hysteresis as observed in this work as well. Transconductance curves measured after a week under ambient condition storage (final) concerning on-state conductance, on/off

ratio, mobility and subthreshold slope have also been evaluated. Figure 4.10a shows the on-state conductance values up to  $10^{-6}$  S and on/off ratio up to  $10^{7}$ , with no correlation to the h-BN and Teflon layer thicknesses. Taking into account the small number of 1-5 SWCNTs per device and 30 fA current detection limit it can be concluded that the performance is comparable to previous devices on DEP-deposited polymer-wrapped SWCNTs. [205] Also, field-effect mobilities [82] up to  $100 \, \text{cm}^2 \text{V}^{-1} \text{s}^{-1}$  and subthreshold slopes [82] down to 140 mV dec-1, shown in figure 4.10b-c, are evidence for the good performance of liquid-phase sorted, polymer-wrapped SWCNTs embedded in h-BN and Teflon. Calculations of subthreshold slopes and field-effect mobility are explained in chapter 2.

Next, the charge traps are calculated based on hysteresis values. The theory related to it has been discussed in chapter 2. The difference in threshold voltages in forward and reverse bias is plotted for different devices in figure 4.11. Taking value for one of the devices  $|V_t| = 6.5 \text{ V}$ ,

$$Q_{SiO2} = c_{g,SiO2} |V_t| = 0.302 \text{ pF cm}^{-1} \text{ x 6.5 V} = 1.2 |e| \text{ nm}^{-1} (|e| \text{ is the elemental charge})$$

For devices with h-BN+SiO<sub>2</sub>, number of charge traps,

$$Q_{h-BN} = c_{g,h-BN} |V_t| = 0.29 \text{ pF cm}^{-1} \text{ x } 3.4 \text{ V} = 0.61 |e| \text{ nm}^{-1}$$

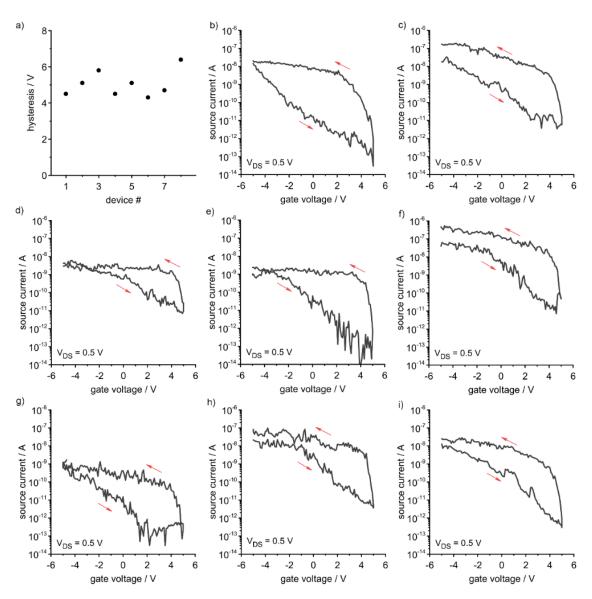
With  $Q_{SiO2} > Q_{h-BN}$  it can be concluded that h-BN film already reduces the number of charge traps. Since there are no contributions from the substrate, these charges should come from water adsorbed onto the metal electrodes and carbon nanotubes.

Charges after Teflon coating of 80nm (t = 0):

$$Q_{Teflon} = c_{g,h-BN} |V_t| = 0.29 \text{ pF cm}^{-1} \text{ x } 1.5 \text{ V} = 0.27 \text{ |e| nm}^{-1}$$

This implies  $(Q_{h-BN}-Q_{Teflon})=0.34$  |e| nm<sup>-1</sup>charges were removed by coating 80nm of Teflon and 0.27 |e|  $nm^{-1}$  charges then contribute towards hysteresis after encapsulating the CNTs in Teflon and h-BN. These charges diffuse out through Teflon after a certain waiting time as discussed earlier.

The devices characterized in this work had subthreshold slope down to 140 mV dec<sup>-1</sup> and field-effect mobility up to 100 cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup> indicates the good performance of devices on h-BN. Figure 4.10 a-c summarizes device performances at room temperature for all 21 devices.



**Figure 4.11:** a) Hysteresis in transconductance curves for SWNCT devices on Si/SiO₂ without h-BN and PTFE encapsulation. (b-i) The corresponding transconductance curves were measured after vacuum annealing and are hence comparable to the h-BN supported devices prior to Teflon coating<sup>[190]</sup>.

## 4.4 Summary

To summarize, for h-BN synthesis, the two important parameters are the metal catalyst and process pressure. The presence of metal catalysts supports synthesis at lower pressures, but in its absence, synthesis only occurs at higher pressures on dielectric substrates.

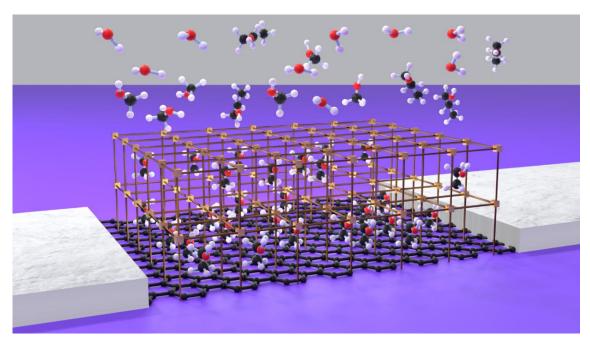
Furthermore, nearly hysteresis-free transconductance curves for (9,8) single-walled carbon nanotubes embedded in h-BN / PTFE heterolayers were observed. The reduction of hysteresis after PTFE coating over a timescale of days has been explained by the slow repelling of residual water molecules that strongly adhere to the metallic electrodes onto which CNTs have been deposited under ambient conditions. Longer waiting times would eventually eliminate hysteresis. However, Teflon coating without breaking the annealing vacuum appears to be a more promising route to be explored.

# 5 Graphene-SURMOF Molecule Sensor

This chapter includes major parts of a research article that has been published<sup>[215]</sup>.

2D materials in the form of monolayers constitute atomically thin and extended planar structures with inherently entangled "bulk" and "surface" properties. As such, 2D materials are highly susceptible to electric fields induced by gate voltages and local charge distributions, [10,26,216,217] to physisorption, chemisorption, and interfacial doping, [218-221] to light, sound, and strain. [222-225] Consequently, 2D materials are of great interest for a range of sensing applications, as has been demonstrated for the detection of gases, [226-228] metal ions, [229,230] strain, [222], and pressure. [231] A challenge for the field is the limited availability of large-area 2D materials. Currently, graphene is the only commercially available wafer-scale material with well-defined properties. Graphene has been demonstrated to be highly sensitive to interfacial doping, electric fields, and vicinal dielectrics, and for a reliable and easily accessible readout, the position of the Dirac point in the gate-voltage dependence of a GFET can be exploited. This sensing principle has already been demonstrated for pH measurements, [232] where a dielectric oxide provides sensitivity towards hydronium ions. However, for general applicability, an interfacial layer is required that can be tailored to be highly selective to specific molecules, offering a larger versatility as compared to surface functionalization. Metalorganic frameworks are such a class of material, and a selective absorptivity for specific molecules can be tailored by specific combinations of metal ions or clusters and organic ligands. There are countless MOFs that have been synthesized and exploited for many applications related to energy harvesting<sup>[233]</sup> and gas storage.<sup>[234-236]</sup> Fewer examples of MOF based sensors exist for gas sensing, [237] chemical sensing, [238] small molecule detection by inducing stress in MOF crystal structure, [239] and metal ions detection via a luminescent MOF. [240] Recently a MOF with high affinity to SO<sub>2</sub> was grown between interdigitated electrodes, and the change in the capacitance of the structure under gas uptake was used for SO<sub>2</sub> detection. [241] In this work, SURMOFs were directly grown onto GFET, to benefit from the combination of the high sensitivity, easy read-out capability of a GFET, and the high selectivity of a SURMOF. In the chapter, SURMOF/GFETs sensors based on Cu<sub>2</sub>(BDC)<sub>2</sub>-SURMOF-2 are presented, which yield a selective sensitivity to ethanol. It is described in detail the fabrication of the SURMOF/GFET sensor via multiple processing steps for the GFET formation. The liquid-phase synthesis of the MOF on the surface of the GFET, prepared by our collaborators at IFG, KIT, is also discussed briefly. It is shown that the SURMOF substantially changes the response of a GFET against gas

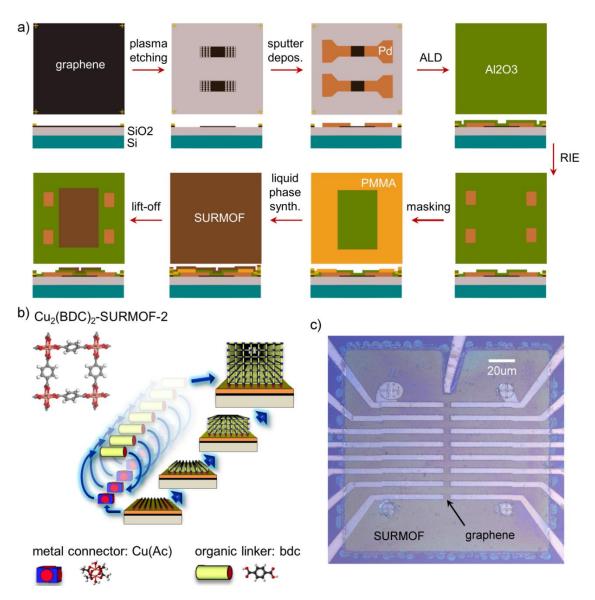
molecules in the air and common alcohols by measuring in-situ the corresponding gate-voltage dependence of the conductance. To study the performance of the ethanol sensor, sensitivity and response time are determined, and the activation and resetting schemes are described.



**Figure 5.1:** 3-D model mimicking ethanol selectivity to SURMOF. Metal linkers (brown squares) are connected to organic linkers (brown rods) grown on a hexagonal honeycomb structure of graphene.

To support these findings, the electrical transport characteristics with Raman spectroscopy are correlated. In addition, energy-dispersive X-ray, X-ray diffraction, scanning electron microscopy, adsorption, and desorption data are provided, and a model is proposed to approximate the response of the sensor. A 3-D model mimicking the selectivity of SURMOF/GFET for ethanol molecules is shown in figure 5.1.

### 5.1 GFET Device Fabrication



**Figure 5.2:** Fabrication of SURMOF/GFET devices. (a) Process flow involving multiple electron beam lithography patterning, etching, and deposition steps. (b) Liquid-phase synthesis of the SURMOF on the GFET by repeated exposure to metal connectors and organic linkers. (c) Optical micrograph of the final SURMOF/GFET devices. The sensor areas are located between the metal source-drain electrodes where graphene can be recognized due to the optical transparency of the SURMOF<sup>[215]</sup>.

Figure 5.2a shows all the steps involved in GFET fabrication. For all the steps, PMMA 950k resist (Allresist) diluted in anisole was used and prebaked at 150°C on a hot plate for 3 minutes. The e-beam exposed areas were developed in a solution of MIBK/IPA (Methyl isobutyl ketone/isopropanol) for 30 s, rinsed with IPA, and dried in a nitrogen

stream giving the required patterned structure. In the first step, markers were defined. PMMA A4.5 (4.5 % PMMA in anisole) was coated at 5000 rpm for 60 s, e-beam patterned and developed. 50 nm tungsten was deposited by sputtering (Bestec 300W DC, 60 s) and subsequently lifted off in acetone. For the second step, graphene strips of dimensions 5 um x 100 um were defined and perforated with holes (230 nm diameter, 640 nm lattice spacing, in total 2x(75x5) by spin coating PMMA A4.5 at 4000 rpm for 60 s, e-beam patterning, cold development at 0°C. By etching of the graphene around the PMMA protected strips and in the holes via oxygen plasma in a reactive ion etcher (RIE Oxford Plasmalab 80 plus, 15 sccm O<sub>2</sub>, 60 mTorr, 30 W for 75 s) holes in the channel are made, leaving an unperforated area in the middle of 5 um x 5 um. In the third step, the source-drain electrodes and the back gate contacts were defined. A scratch through the SiO<sub>2</sub> was made to contact the Si gate. After spin-coating PMMA A4.5 at 6000 rpm for 60 s, e-beam exposure, and development, 3 nm Cr and 42 nm Pd were deposited by sputtering (100 W, RF, 30 s, and 70 W, DC, 45 s, respectively). On this fabricated GFET device, 5 nm of aluminum oxide was grown using trimethylaluminum (TMA) and ozone at 150°C by thermal atomic layer deposition (TMA pulse time = 0.1 s at 150 sccm flow rate, T= 25°C, purge time = 6 s, Oxygen pulse time = 0.1 s at 100 sccm flow rate, purge time = 6 s, power= 70%, 75 cycles). The Al<sub>2</sub>O<sub>3</sub> layer was locally removed for electrical contacting by first spin-coating PMMA A8 (8% PMMA in anisole) at 6000 rpm, e-beam patterning, and development. Then, the aluminum oxide was etched in the RIE (40 sccm Ar, 10 sccm CHF<sub>3</sub>, 200 W, 15 mTorr, 70 s). In the fifth and final step, the area for the SURMOF growth on top of the devices was defined by spin coating PMMA A4.5 at 5000 rpm, e-beam patterning, and development. The GFET was then used to grow SURMOF on top by our collaborators at IFG, KIT, Germany.

# **5.2 SURMOF synthesis**

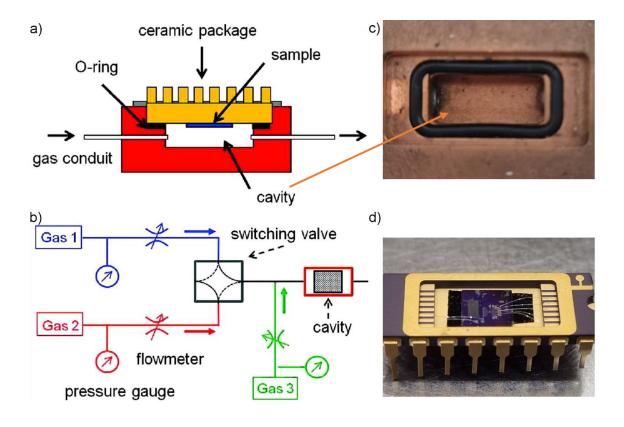
Figure 5.2b shows a schematic for the synthesis of SURMOF. The SURMOF layer was grown by the layer-by-layer (lbl) synthesis as described in Liu et al. [242] by our collaborators from IFG, KIT. To summarize the process, first, the surface of the GFET device was activated in a UV/Ozone cleaner (Ossila, Sheffield, UK) for one minute to maximize the number of functional OH groups at the  $Al_2O_3$  surface. Afterward, the devices were put immediately into a 1 mM of copper (2) acetate  $(Cu_2(OAc)_4(H_2O)_2)$  ethanol solution before the synthesis. Then, the devices were placed on the sample holder and subsequently sprayed with 1 mM Cu  $(OAc)_2$  ethanolic solution for 15 s and with an 0.2 mM 1,4-benzene dicarboxylic acid (BDC) ethanol solution for 25 s at room temperature. Between both steps, the sample was thoroughly rinsed with pure ethanol

to remove under-coordinated metal nodes or organic linker molecules. This procedure was repeated in total 35 times to grow a nominally 100 nm thick layer.

## **5.3 Characterization parameters**

- **1.** XRD: X-ray diffraction (XRD) measurements were carried out in an out-of-plane geometry using a Bruker D8-Advance diffractometer equipped with a position-sensitive LynxEye detector in  $\theta$ -2 $\theta$  geometry. A Cu-anode with a wavelength of  $\lambda$ =0.154 nm was used. The samples were investigated with an angle increment of 0.02° and a scan speed of 4 s per step.
- 2. <u>SEM / EDX</u>: Scanning electron micrographs were taken with a Zeiss Ultra plus SEM at 10 keV beam energy, at 45° tilt angle, and inlens detection. Energy-dispersive X-ray spectra were recorded with a Zeiss LEO 1530 SEM and an Oxford instruments X-maxN detector at 4 keV beam energy and analyzed with AZtec software from Oxford instruments.
- **3.** Raman: Spectra of CVD-graphene/300nm-SiO<sub>2</sub>/Si sample was measured before and after Al<sub>2</sub>O<sub>3</sub> and SURMOF growth, under ambient conditions with a Renishaw inVia Raman microscope at 532 nm excitation wavelength, 3 mW power, 60 s integration time, and 20x magnification. SURMOF/5nm-Al<sub>2</sub>O<sub>3</sub>/graphene/300-nmSiO<sub>2</sub>/Si reference sample was measured under ambient conditions after exposure to ethanol, isopropanol, and methanol vapor. Before alcohol vapor exposure, the samples were vacuum annealed at 150°C. The data were acquired at 532 nm excitation wavelength, 0.6 mW (after alcohol exposure) and 3 mW (after vacuum annealing), 60 s integration time, 20x magnification.
- 4. Electrical transport and sensing measurements: The SURMOF/GFETs were mounted to a ceramic chip carrier, wire bonded (figure 5.3d), and with the package mounted to a cavity of volume ~1cm³ (figure 5.3b). The cavity has an inlet and outlet as part of a gas line system as described in Ganzhorn et al.<sup>[3]</sup> A four-way valve enables instantaneous switching between gases, and the conditions were controlled by pressure gauges and flow meters (dosing valves) (figure 5.3a, b). All measurements were carried out at flow rates of 0.5 L min<sup>-1</sup> and a static gas pressure of 0.25 bar above atmospheric pressure. The dynamic pressure (<10<sup>-4</sup> bar) is negligible. The gas in the cavity is exchanged within 0.1 s, setting the time resolution of the measurements. In the downstream, a humidity sensor (Bosch BMP280) and an alcohol sensor (NCD MQ-3) were mounted in an additional cavity and monitored with a Raspberry Pi. For exposure to alcohols and water, the liquids were filled into a steel bubbler and dry nitrogen was

passed through it at a flow rate that yields the carrier gas saturated with the corresponding liquid, as monitored by the downstream sensors. If not stated otherwise the initial starting condition was flowing dry nitrogen, and measurements were taken ~15 min after changes of conditions. Electrical transport measurements were carried out with an Agilent 4155C semiconductor parameter analyzer with TRIAX cabling, and back gate sweeps were conducted at source-drain voltages of 0.01 V, 0.02 V, and 0.03 V. Time-dependent measurements were done at zero back gate voltage and 1 s time intervals. All experiments were carried out at room temperature.



**Figure 5.3:** a), b) Schematic of the gas sensing setup adapted from Ganzhorn et al. <sup>[3]</sup>c) Cavity on which the chip carrier is loaded in inverted position d) Sample bond wired onto the chip carrier.

**5.** <u>Simulation</u>: A qualitative proof-of-principle simulation of the graphene carrier concentration and Dirac voltage as a function of back-gate voltage and surface charge density was conducted using a finite element partial differential equation solver (FlexPDE 6). The coupled Poisson-Drift-Diffusion equations were solved for the electrostatic potential and the quasi-Fermi potentials<sup>[243]</sup> by solving

iteratively the Laplace equation and the Drift-Diffusion equations<sup>[244][245]</sup>. The surface charge density was imposed by a natural boundary condition.

### 5.4 Results and discussions

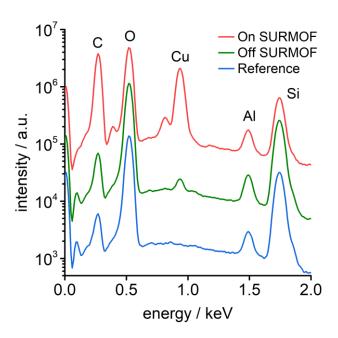


Figure 5.4: Energy-dispersive X-ray (EDX) spectra for evaluating the integrity of the ALD-grown  $Al_2O_3$  layer. The reference is a 5nm- $Al_2O_3$ /graphene/300nm-SiO<sub>2</sub>/Si sample, on which a nominally 100 nm thick SURMOF layer was grown. The primary beam energy was 4 keV, sufficient to probe the  $Al_2O_3$  layer underneath the top layer (On SURMOF). The upper two curves are shifted for clarity. Residues of copper from the SURMOF are detectable in lifted-off regions (Off SURMOF)<sup>[215]</sup>. See also figure 5.6.

The SURMOF/GFET sensors were fabricated by employing several steps of electron-beam lithography, metallization, etching, coating, atomic layer deposition, and liquid-phase synthesis, as shown in the schematic process flow in figure 5.2a. Large-area monolayer graphene on p\*\*-Si/300 nm-SiO<sub>2</sub> was etched by plasma oxidation into multiple 5  $\mu$ m wide and 100  $\mu$ m long strips, each electrically contacted separately with Pd/Cr source-drain electrodes. The uncovered graphene channels have a length of 5  $\mu$ m with an active area of 25  $\mu$ m<sup>2</sup>. To minimize the contact resistance, the graphene underneath the electrodes was perforated with holes<sup>[246]</sup>. 5 nm of Al<sub>2</sub>O<sub>3</sub> was grown by thermal atomic layer deposition (ALD) by subsequent pulses of trimethylaluminum and ozone<sup>[247]</sup> over the entire structure to provide surface hydroxyl groups required for the SURMOF growth and to electrically isolate the graphene and the electrodes. The contact

pads were opened by local etching and the area for the SURMOF growth was defined by a resist mask. A  $Cu_2(BDC)_2$ -SURMOF-2 with a nominal thickness of 100 nm was grown by liquid-phase synthesis (figure 5.2b), [248][242] using a spray synthesis similar to Hurrle et al. [249] Afterward, the SURMOF was lifted off from outer areas with a resist mask. An optical micrograph of the complete SURMOF/GFET devices is shown in figure 5.2c. It is

observed that the  $Al_2O_3$  layer is required for the SURMOF to grow on top of graphene. With energy-dispersive X-ray spectroscopy (EDX), it is verified that the ALD-grown  $Al_2O_3$  layer is still intact after the SURMOF synthesis (figure 5.4). Out-of-plane XRD measurements on a large-area reference sample show that the SURMOF grows on 5nm- $Al_2O_3$ /graphene/300nm-SiO<sub>2</sub>/Si with the (001) orientation parallel to the substrate surface and with a lattice constant of 1.12 nm (figure 5.5), which is comparable to the synthesis of the SURMOF on other –COOH terminated surfaces<sup>[242][250]</sup>. Despite the flat appearance of the SURMOF in the optical image and the (001) reflex in the XRD data, the SURMOF layer is rough on a microscopic scale. SEM images recorded under different angles reveal that the SURMOF at the interface with the  $Al_2O_3$  layer is continuous, whereas the upper surface is uneven, with small SURMOF crystals pointing in various directions (figure 5.6), likely a consequence of the atomically uneven  $Al_2O_3$  surface.

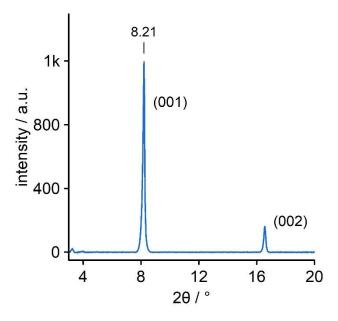
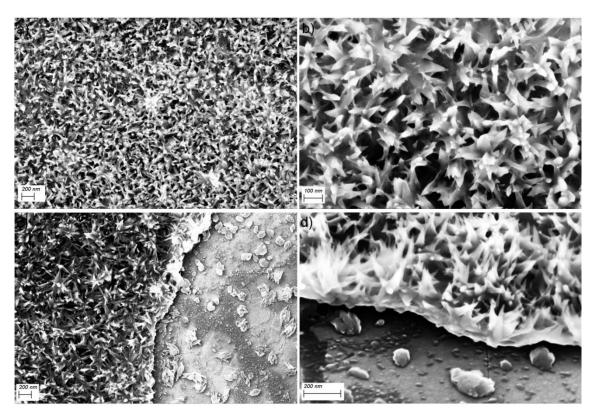


Figure 5.5: XRD measurements of SURMOF showing (001) and (002) peaks<sup>[215]</sup>.

After fabrication, the devices were wired to a ceramic package, mounted to a cavity of volume ~1cm³, and connected to a gas line system, described in Ganzhorn et al.<sup>[3]</sup> (figure 5.3). A four-way valve enables instantaneous switching between gases, and the conditions were controlled by pressure gauges and flow meters (dosing valves). Devices

were exposed to air,  $N_2$ ,  $O_2$ , and  $CO_2$ , and to molecules from liquids by purging  $N_2$  through  $H_2O$ , methanol ( $C_3OH$ ), ethanol ( $C_2H_5OH$ ), and isopropanol ( $C_3H_7OH$ ). The relative humidity and alcohol concentrations were monitored downstream with commercial sensors. The gate-voltage dependencies of the device conductance were measured with a semiconductor parameter analyzer at 300 K, and the doped silicon was used as a back-gate for all devices. The details have been already discussed in the chapter 3.



**Figure 5.6:** Scanning electron microscopy images of a SURMOF/5nm-Al<sub>2</sub>O<sub>3</sub>/graphene/300-nmSiO<sub>2</sub>/Si sample, recorded at 10 keV and with in-lens detection. (a-c) measured at normal incidence, (d) under 45° angle from the front. (c-d) show the edge of the SURMOF after partial lift-off<sup>[215]</sup>.

First, the gate voltage dependence of the conductance of GFETs without SURMOF coating is discussed. As a measure for the doping, the Dirac voltage has been used, which is defined as the gate voltage that yields the minimum conductance. If the applied gate voltage is equal to the Dirac voltage, the Fermi level coincides with the charge neutrality point of graphene at the K point of the graphene band structure. The devices show significant hole doping in air, but also while purging with dry N<sub>2</sub>, as shown by the blue traces in figure 5.7 and figure 5.8a, respectively. The conductance decreases monotonically with increasing gate voltage and the Dirac voltage  $V_{\rm Dirac}$  is beyond the

maximum applied gate voltage of +100 V. The corresponding doping level can be calculated from the carrier concentration n in the graphene layer, which depends on gate voltage  $V_{\rm gate}$ , the Dirac voltage  $V_{\rm Dirac}$ , the geometrical capacitance C formed between graphene and SiO<sub>2</sub>/Si, and the graphene quantum capacitance, using equation 5.1. The derivations of the graphene quantum capacitance have been discussed in the chapter 2.

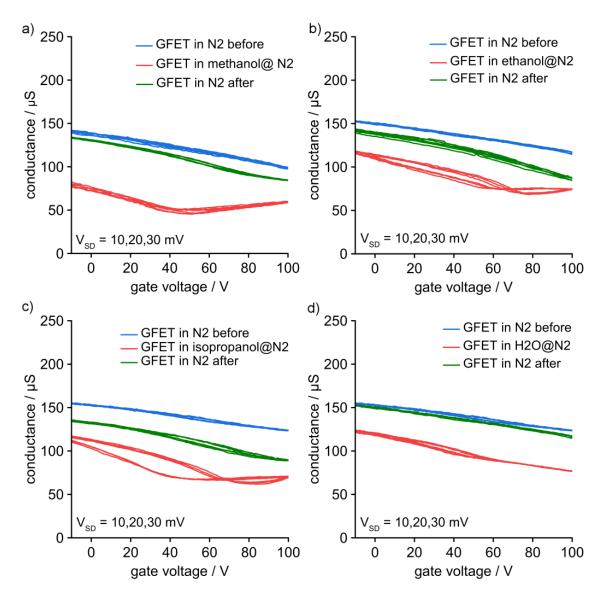


Figure 5.7: Response of the GFET without SURMOF to methanol- (a), ethanol- (b), isopropanol- (c), and water molecules in the  $N_2$  stream. The conductance versus gate-voltage is shown for the GFET in  $N_2$  before and after the exposure. Source-drain voltages are indicated.<sup>[215]</sup>

$$n(V_{gate}, V_{Dirac}) = -(V_{gate} - V_{Dirac}) \left( \sqrt{\frac{|V_{gate} - V_{Dirac}|}{a + c^2}} - c \right)^2$$
 (5.1)

$$c = \frac{b}{2a} \tag{5.2}$$

$$a = \frac{e}{C} \tag{5.3}$$

$$b = \frac{h \, v_F \pi^{0.5}}{e} \, , v_F = 10^6 \, ms^{-1}$$
 (5.4)

$$C = 11.5 \, nF \, cm^{-2}$$

The carrier concentration at zero gate voltage due to doping is then given as  $n_{\text{doping}} =$  $n(V_{\rm gate}=0, V_{\rm Dirac})$ , and the value obtained is  $n_{\rm doping} > +7 \cdot 10^{12} \, \rm cm^{-2}$  for  $V_{\rm Dirac} > +100 \, \rm V$ ;  $V_{\rm Dirac} > +100 \, \rm V$ = +150 V yields  $n_{\rm doping}$  =  $10^{13}\,{\rm cm}^{-2}$ . The doping level of the graphene layer was also followed by Raman spectroscopy. The spectrum of a reference CVD-graphene/300 nm-SiO<sub>2</sub>/Si sample in figure 5.9a shows the graphene G-peak at 1596 cm<sup>-1</sup>, the 2D-peak at 2686 cm<sup>-1,</sup> and no visible D-peak. The peak positions were analyzed with the model of Lee et al., [251] by plotting our data onto the dashed grid lines, which shows how the Gpeak and 2D-peak positions depend on doping and strain. Data points can then be deconvoluted into components of strain and doping by projection onto the grid lines, as demonstrated in figure 5.9b. The data is consistent with a hole-doped graphene layer which is nearly strain- and defect-free. The Raman peak in figure 5.9a at 2461 cm<sup>-1</sup> is from a two-phonon combination mode in graphene, referred to as G\* or D+D".[252] The peak at 2331 cm<sup>-1</sup> originates from ambient molecular nitrogen above the sample surface<sup>[253][254]</sup>. After the growth of a nominally 5 nm thick Al<sub>2</sub>O<sub>3</sub> layer, a small D-peak appears in figure 5.9a at 1351 cm<sup>-1</sup>, and the G- and 2D-peaks are shifted down to 1589 cm<sup>-1</sup> and 2682 cm<sup>-1</sup>, respectively. It indicates that the growth of Al<sub>2</sub>O<sub>3</sub> induces a moderate defect concentration in the graphene layer and it reduces slightly the hole doping as shown in figure 5.9b. The graphene layer though remains free of strain. Note that the Raman measurement conditions (ambient, laser irradiation) are not strictly identical with the GFET characterization conditions (gas flow system, dark), but the data is in agreement with the transport data and shows that the graphene layer underneath the Al<sub>2</sub>O<sub>3</sub> layer is hole-doped.

The sensitivity of the GFET without SURMOF against the presence of alcohol and water vapors has been explored first. Figure 5.7a-c shows the response of the GFET to methanol-, ethanol-, and isopropanol molecules in the N<sub>2</sub> stream. It is observed that the

overall conductance is reduced in the presence of all three alcohols and the corresponding Dirac voltages could be determined. The amount of hole doping reduction is similar but most pronounced during exposure to methanol ( $n_{\text{doping}} = 3.3 \cdot 3.5 \cdot 10^{12} \, \text{cm}^{-2}$ ), followed by ethanol ( $n_{\text{doping}} = 4.6 \cdot 5.7 \cdot 10^{12} \, \text{cm}^{-2}$ ), and isopropanol ( $n_{\text{doping}} = 4.3 \cdot 6.0 \cdot 10^{12} \, \text{cm}^{-2}$ ). Also, the exposure to water molecules reduces the hole doping when

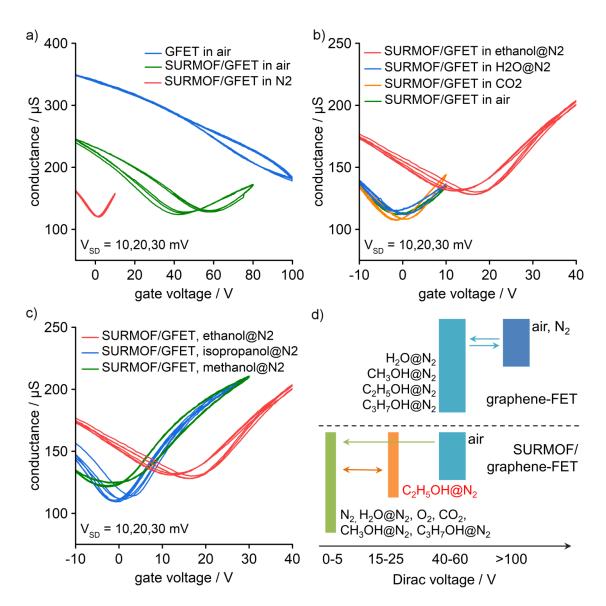
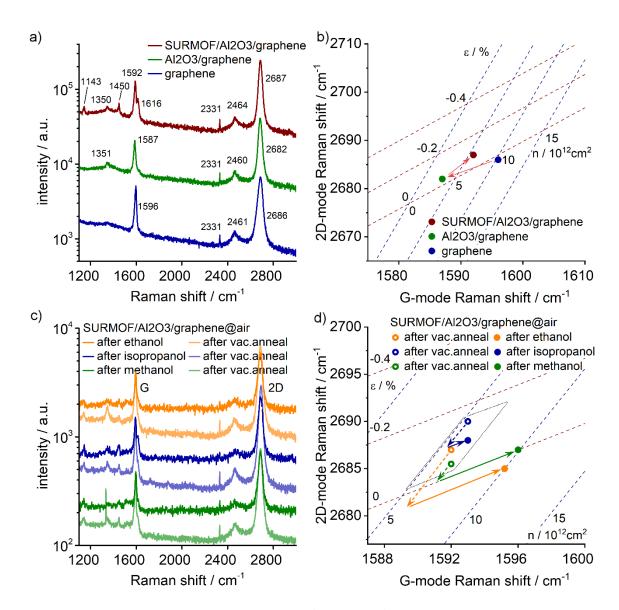


Figure 5.8: Gate-voltage dependence of SURMOF/GFET device conductance. (a) Response to air before (GFET in the air) and after SURMOF coating (SURMOF/GFET in the air), and to  $N_2$  after SURMOF activation (SURMOF/GFET in  $N_2$ ). (b) Response of SURMOF/GFETs to air and  $CO_2$ , and  $N_2$  purged with  $H_2O$  and ethanol. (c) Response SURMOF/GFETs to  $N_2$  purged with ethanol, methanol, and isopropanol. (d) Schematic overview on the responses of the SURMOF/GFET and GFET devices and the Dirac voltages measured under the indicated conditions<sup>[215]</sup>.



**Figure 5.9:** (a) Raman spectra of a CVD-graphene/300nm-SiO<sub>2</sub>/Si sample before and after Al<sub>2</sub>O<sub>3</sub> and SURMOF growth measured in air. The peak at 2331 cm<sup>-1</sup> is from N<sub>2</sub>(g). The upper two curves are shifted for clarity. b) Correlation between the frequencies of the G and 2D Raman modes shown in (a). The dashed lines show the effects of hole doping  $(\Delta \omega_{2D}/\Delta \omega_G = 0.7)$  and strain  $(\Delta \omega_{2D}/\Delta \omega_G = 2.2)$  on the mode frequencies of graphene using the model of Lee et al.<sup>[145]</sup> (c) SURMOF/5nm-Al<sub>2</sub>O<sub>3</sub>/graphene/300-nmSiO<sub>2</sub>/Si reference sample measured in the air after exposure to ethanol, isopropanol, and methanol vapor. Before alcohol vapor exposure, the samples were vacuum annealed at 150°C. All curves except the lowest are shifted for clarity. (d) Correlation between the frequencies of the G and 2D Raman modes is shown in (c). The dashed and full arrows show the corresponding changes in strain and doping. The dotted box encompasses the data of the vacuum annealed samples and defines the error for determining the relative changes in strain and doping<sup>[215]</sup>.

the gas flow is changed from dry  $N_2$  to humid  $N_2$  (~80% relative humidity, figure 5.7d). All changes are reversible and strong hole doping is reestablished when purging the

devices with dry N<sub>2</sub> (figure 5.7). It is important to note that GFETs without SURMOF coating always remain in the strong hole-doping regime, with the Dirac voltage above 40 V, independent of the type of alcohol exposure. This is summarized schematically in the upper part of figure 5.8d. The growth of the SURMOF changes this behavior completely. The Dirac voltage of the SURMOF/GFET in the air is at 40-60 V, whereas without SURMOF it was beyond 100 V, as shown in figure 5.8a. Also, the Raman spectrum has changed in figure 5.9a. After the growth of a nominally 100 nm thick SURMOF layer, the G and 2D peaks shifted upwards to 1592 cm<sup>-1</sup> and 2687 cm<sup>-1</sup>, respectively. The shifts in figure 5.9b indicate that the SURMOF growth introduces about 0.1% compressive strain in the graphene layer and a slight change in doping. After the SURMOF growth three additional Raman peaks are observed at 1143 cm<sup>-1</sup>, 1450 cm<sup>-1</sup>, and 1616 cm<sup>-1</sup>, assigned to the ring stretch of benzene-dicarboxylate, asymmetric C-O stretch, and C= stretch in the SURMOF layer, respectively. [256] Again, absolute strain and doping values should be taken with care.

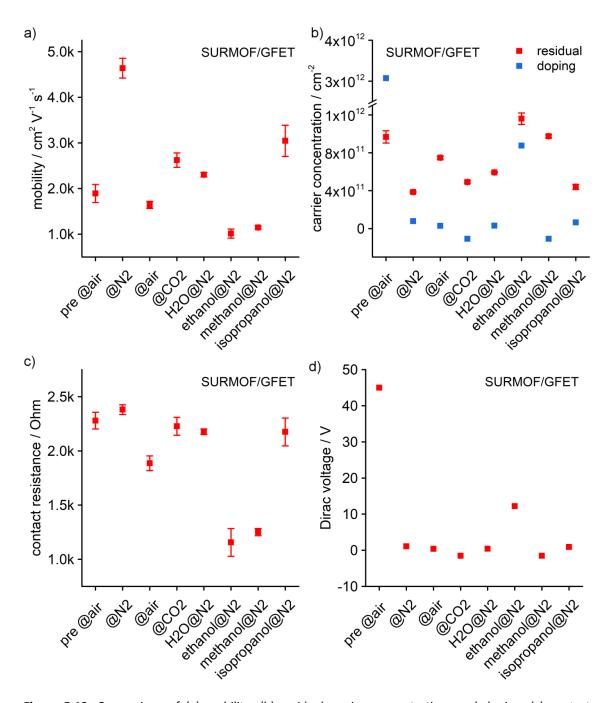
Most interestingly, the hole doping in the air is not only significantly reduced for GFETs after the SURMOF growth, but after purging with  $N_2$ , the device becomes undoped, and the Dirac voltage approaches zero, as shown in figure 5.8a. Also, the mobility has increased to  $4640 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ , which is remarkably high for CVD-graphene on  $\text{SiO}_2/\text{Si}$  without vacuum or current annealing.

The purging of SURMOF with  $N_2$  is a common activation process step, [257] promoting the desorption of solvent and guest molecules from MOF pores. The details of the desorption process can be rather complex. For instance, the desorption of water is inhibited in the presence of ethanol due to complex formation at the outer surface of a  $MOF^{[256]}$ . The activation process is important also in this work and essentially constitutes the last step in the SURMOF synthesis. The outcome of this activation process implies that ethanol captured in the SURMOF from the synthesis process diffuses out while purging with  $N_2$ , revealing an undoped SURMOF/GFET. It is expected that ethanol can diffuse back into the structure as well.

Further tests were performed and the SURMOF/GFET devices were exposed to a flow of  $N_2$  saturated with ethanol (ethanol@ $N_2$ ). Indeed, figure 5.8b shows that the Dirac voltage shifts from 0 V to 15 V when switching from  $N_2$  to ethanol@ $N_2$ . The data also shows that the SURMOF/GFET is insensitive to  $H_2O$  molecules ( $H_2O@N_2$ ),  $CO_2$ , and air. Furthermore, figure 5.8c shows that the Dirac voltage of the SURMOF/GFET remains at zero and is not reacting to  $N_2$  saturated with methanol (methanol@ $N_2$ ) or isopropanol (isopropanol@ $N_2$ ). These results are summarized schematically in the lower part of figure 5.8d.

The selectivity of the Dirac voltage to ethanol must have been imposed by the SURMOF/Al<sub>2</sub>O<sub>3</sub> interface since the adsorption and desorption properties of the SURMOF itself are very similar for the three alcohols (figure 5.12). More insights were gained by analyzing the SURMOF/GFETs transconductance curves with the model of Kim et al. [255]. For all conditions, the mobility, residual carrier concentration, contact resistance, and Dirac voltage were determined. Figure 5.10a shows that the highest mobility has been measured in pure nitrogen (~4640 cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup>, @N2) after the activation procedure. The lowest mobility is obtained for ethanol@N2 (1020 cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup>) and methanol@N2 (1150 cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup>). The mobility for isopropanol@N2 is 3050 cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup>, and thereby significantly higher than for the other two alcohols. The mobility for all other conditions is between 1.6-2.6 kcm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup>. Also, the residual carrier concentration in figure 5.10b follows a similar pattern. The lowest value is observed in pure nitrogen (3.8·10<sup>11</sup> cm<sup>-2</sup>), the highest values for ethanol@N2 (1.2·10<sup>12</sup> cm<sup>-2</sup>) and methanol@N2 (9.7·10<sup>11</sup> cm<sup>-2</sup>), and comparable to air@N2 (9.7·10<sup>11</sup> cm<sup>-2</sup>) before the activation procedure. The value for isopropanol@N2 is 4.4·10<sup>11</sup> cm<sup>-2</sup>, and thereby significantly lower than for the other two alcohols. The other values are in between (4.9-7.5·10<sup>11</sup> cm<sup>-2</sup>). Furthermore, the contact resistance in figure 5.10c reproduces the different behavior of the alcohols. The lowest contact resistance is observed for ethanol@N2 (1156 Ohm) and methanol@N2 (1251 Ohm). For isopropanol@N2 the value is 2175 Ohm, and thereby again significantly different from the other two alcohols. The contact resistance for all other conditions is between 1.9-2.3 kOhm. On the other hand, the Dirac voltage in figure 5.10d and the corresponding doping level in figure 5.10b show a different behavior among the exposure to the alcohol molecules. For ethanol@N2 the Dirac voltage is at 12V and the doping at 8.8·10<sup>11</sup> cm<sup>-2</sup>, whereas both values are significantly smaller for methanol@N2  $(-1.5 \text{ V}, 1.07 \cdot 10^{11} \text{ cm}^{-2})$  and isopropanol@N2 (+0.9 V, 6.4·10<sup>10</sup> cm<sup>-2</sup>).

The data thus leads to the conclusion that ethanol and methanol molecules reach the SURMOF/Al<sub>2</sub>O<sub>3</sub> interface but isopropanol does not, and only ethanol molecules induce a shift in the Dirac voltage. Therefore, it can be inferred that MOF/GFET sensing of ethanol is due to shift in Dirac voltage and not due to the changes in carrier mobility. This is shown in figures 5.10a and d where the mobility data shows similar values for both the alcohols but different Dirac voltages. This result may not be surprising given that the graphene layer is  $^{\sim}$ 3-5nm below the Al<sub>2</sub>O<sub>3</sub> surface making the charge carriers in graphene less susceptible to scattering by molecules. Also, Schedin et al. [18] reported no change in mobility despite exposing graphene directly to various gases.



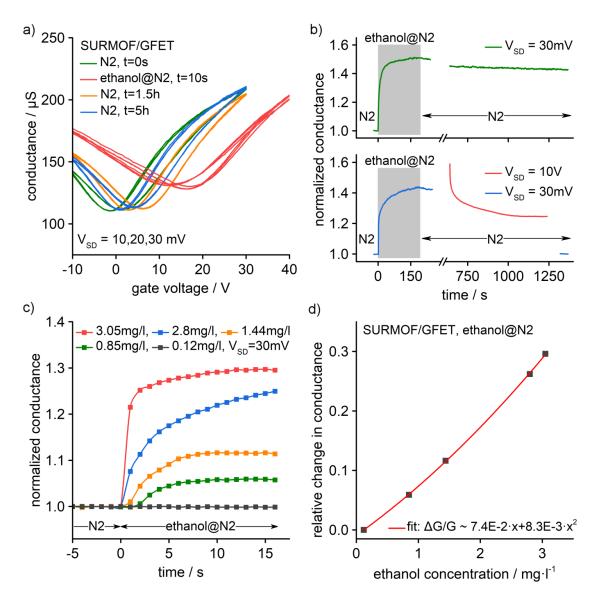
**Figure 5.10:** Comparison of (a) mobility, (b) residual carrier concentration, and doping, (c) contact resistance, and (d) Dirac voltage of SURMOF/GFET devices under various conditions. The values have been determined by fitting the transport measurements to the model of Kim et al.<sup>[255]</sup> The carrier concentration due to doping in (b) has been derived from the Dirac voltage. All data were acquired after SURMOF activation, except for the measurement in the air before activation (pre @air)<sup>[215]</sup>.

Further insight into the alcohol-induced doping of graphene was gained by Raman spectroscopy on a reference SURMOF/5nm-Al<sub>2</sub>O<sub>3</sub>/graphene/300nm-SiO<sub>2</sub>/Si sample. Figure 5.9c shows changes to the Raman spectra when exposed to ethanol, isopropanol,

and methanol vapors. Additional measurements were taken immediately after vacuum annealing at 150°C, before each alcohol exposure. Once again, the correlation between G and 2D peak positions to determine the doping and strain in the graphene layer has been used<sup>[251]</sup>. No effect is observed after exposure to isopropanol. The changes in strain and doping are within uncertainty, as shown in figure 5.9d. This result supports the transport data, which indicates that isopropanol does not reach the SURMOF/Al<sub>2</sub>O<sub>3</sub> interface. Also in agreement with the transport data is the effect due to exposure to ethanol molecules. The Raman data shows that ethanol does reach the interface and induces significant hole doping in the graphene layer. Regarding the exposure to methanol, both, Raman and transport data show that methanol reaches the SURMOF/Al<sub>2</sub>O<sub>3</sub> interface. However, the transport data shows no change in doping, whereas the Raman data does. It is important to note that Raman probes the local doping and that a shift of the G peak to larger wavenumbers occurs for electron doping and hole doping[258][259]. Transport measurements instead probe the average net charging by a shift of the Dirac voltage. The formation of electron-hole puddles with zero net charges, as reported in absence of charged impurities, [260] could explain the observation with methanol, and their presence would reduce the carrier mobility and increase the residual carrier concentration without shifting the Dirac voltage. At the same time, a shift of the Raman G-peak frequency should be observable because of local doping. The different interactions of ethanol and methanol with the SURMOF/Al<sub>2</sub>O<sub>3</sub> interface though must have a microscopic origin and figure 5.9d indicates a small difference in the change of strain in the graphene layer.

To further explore the sensor performance, the response time and sensitivity of the SURMOF/GFET devices were explored. Figure 5.11a shows that the Dirac voltage changes from 0 V to 15 V within seconds when switching from N<sub>2</sub> to ethanol@N<sub>2</sub>. However, after switching back to pure N<sub>2</sub>, it takes several hours until the Dirac voltage reapproaches 0 V. In the upper part of figure 5.11b, adsorption and desorption of ethanol are continuously monitored by measuring the conductance at a fixed gatevoltage of 0 V and a source-drain voltage of 30 mV. After 20 min of purging with N<sub>2</sub>, the conductance is still 40% above the value before ethanol@N<sub>2</sub> exposure. Such slow desorption is a serious obstacle for sensing applications. Fortunately, there is a simple way to overcome the problem by thermally activating the desorption process, for example, by heating the device via electrical power dissipation. The lower part of figure 5.11b shows that when increasing the source-drain voltage temporarily from 30 mV to 10 V, the desorption becomes as fast as the adsorption. The corresponding increase of the power dissipation from 140 nW to 15 mW resets the sensor within 10-20 seconds. The current-driven self-heating of the graphene is estimated to increase the

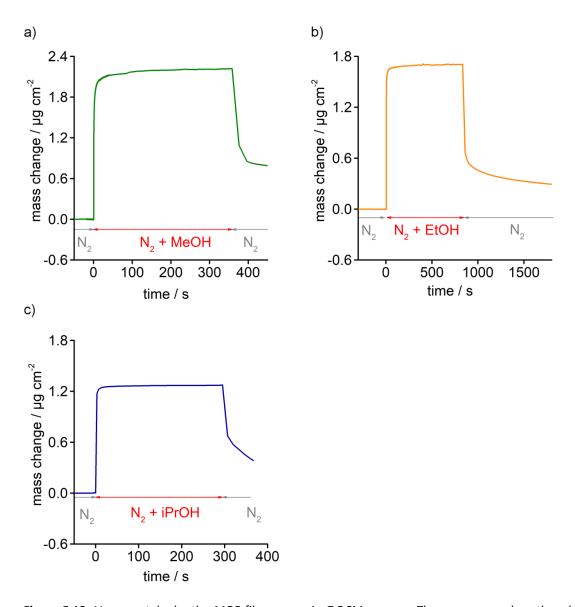
temperature  $\Delta T$  by ~150 K, determined based on  $\Delta T = p/r_0$ , where p is the dissipated power per area and  $r_0 = 0.4$  kWcm<sup>-2</sup>K<sup>-1[261]</sup>.



**Figure 5.11:** SURMOF/GFET time response and sensitivity to exposure of ethanol. (a) Time evolution of conductance versus gate-voltage measured before, after, and during ethanol exposure. Time steps and source-drain voltages  $V_{SD}$  are given. (b) In the upper panel, the corresponding change in conductance was measured at zero gate voltage and  $V_{SD} = 30$  mV (green trace). The lower panel shows the reset procedure with a temporary increase of  $V_{SD}$  from 30 mV (blue trace) to 10 V (red trace). After the reset, the conductance reached the initial value. (c) SURMOF/GFET response versus ethanol concentration. The concentrations were measure with a commercial ethanol sensor. (d) Change in conductance after 15 s of ethanol exposure versus ethanol concentration. The data is fitted with a second-degree polynomial function<sup>[215]</sup>.

Next in characterizing the sensors, ethanol sensitivity of the SURMOF/GFET was measured, and gauged the ethanol concentration in N<sub>2</sub> with a commercial alcohol sensor. The sensor-as all the commercial sensors- is based on heated metal oxide nanostructures and reacts unspecific to ethanol, methanol, and isopropanol, and many other organic molecules<sup>[262]</sup>. The measurement results are summarized in figure 5.11c-d. The SURMOF/GFET sensor can detect ethanol concentrations larger than 0.2 mgl<sup>-1</sup> (100 ppm), and a concentration of 3 mgl<sup>-1</sup> leads to a 30 % change in conductance. This level of sensitivity is comparable to other GFET sensors responding to alcohol. However, it should be emphasized that these GFET sensors are not selective to ethanol and do not discriminate against methanol and isopropanol<sup>[41]</sup>. In marked contrast, SURMOF/GFET in this work has high selectivity to ethanol, low detection limit, and is insensitive to humidity, and operates at room temperature. More sophisticated electrical readout schemes would enhance the sensitivity, yet with the simple tracking of the conductance applied here, the SURMOF/GFET sensor is sensitive enough to detect ethanol concentrations that are relevant for breath alcohol determination<sup>[263]</sup>.

To give a microscopic explanation for the observations made, results based on GFET without SURMOF need to be addressed first. As transport and Raman data show, the graphene layer underneath the Al<sub>2</sub>O<sub>3</sub> layer in air and N<sub>2</sub> is strongly hole-doped. This points to negative charges located in or on the oxide layers. Regarding the Al<sub>2</sub>O<sub>3</sub> layer, it has been reported that the growth by ALD can give rise to negative charge densities on the order of 10<sup>12</sup> cm<sup>-2</sup>, which can be two orders of magnitude higher than what is typically observed for thermally grown SiO<sub>2</sub><sup>[264]</sup>. Whereas it is unclear how many charges are located within the Al<sub>2</sub>O<sub>3</sub> and SiO<sub>2</sub> layers, the alumina surface is certainly terminated with –OH groups after the completion of the ALD process<sup>[265]</sup>. Exposure after growth to ambient air leads to the adsorption of water molecules on the hydrated alumina. Unlike on unhydrated alumina, molecular water is strongly bound to hydrated alumina and can only be removed when heated to 170°C[266]. When measuring the GFET without SURMOF, therefore it is reasonable to assume that the Al<sub>2</sub>O<sub>3</sub> surface is terminated with -OH groups and covered with water molecules, despite purging with N2. How those groups get negatively charged is not clear, but probably through an electrochemical electron trapping process by the -OH groups occurring during electrical characterization. This mechanism has been verified by FTIR spectroscopy for devices on -OH terminated SiO<sub>2</sub> surfaces<sup>[267]</sup>. It is reasonable to assume that hydroxyl groups on Al<sub>2</sub>O<sub>3</sub> are as effective in trapping charges and that the Al<sub>2</sub>O<sub>3</sub> layer is thin enough to allow the tiny tunneling current that is required to fill the traps. In any case, not all –OH groups of fully hydrated alumina will be charged as this would lead to an excessive surface charge density on the order of  $>10^{15}$  cm<sup>-1[268]</sup>.



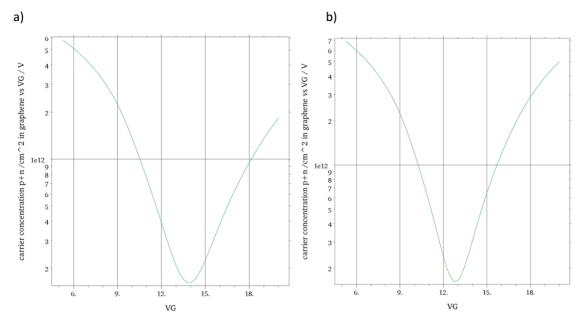
**Figure 5.12:** Vapor uptake by the MOF film grown Au@QCM-sensors. The vapors are a) methanol, b) ethanol and c) isopropanol. Initially, the samples are activated in an atmosphere of pure nitrogen, then the gas flow is instantaneously enriched with the vapor of the guest molecules with a vapor pressure close to saturation. After reaching equilibrium, the gas atmosphere is switched back to pure nitrogen. The gas flow rate is 100 ml per min. The temperature is 25°C<sup>[215]</sup>.

Next, an attempt is made to understand why exposure to alcohol and water reduces the hole-doping of the GFET. There are two reasons plausible:

- Screening of charges
- Charge removal

Simulations show that monohydric alcohol interacts non-dissociative with hydrated alumina by an antiparallel alignment of the alcohol polar group to the alumina hydroxyl

group<sup>[269]</sup>. Water molecules on hydrated alumina orient in a similar way<sup>[268]</sup>. Both lead to a reduction of surface dipole moments. Moreover, when surface charges are covered by a polarizable medium, the electric field underneath the surface – where graphene is located – will be reduced approximately by  $2\varepsilon_1/(\varepsilon_1+\varepsilon_2)$ , where  $\varepsilon_1$  and  $\varepsilon_2$  are the effective dielectric media below and above the interface, respectively<sup>[270]</sup>. For the devices studied above, the factor could be on the order of 0.5, which would explain the reduction of hole-doping. In this picture, the screening will disappear and the strong hole-doping will reappear as soon as a device is purged with N<sub>2</sub>, which experimentally occurs within minutes. The mechanism is kind of similar to the screening induced reduction of hole-doping observed for graphene devices covered with ionic liquid, <sup>[271]</sup> or metal top gate<sup>[272]</sup>. The physics of diffusive graphene devices can be described by coupled Poisson-Drift-Diffusion equations<sup>[244][245]</sup> and a finite element partial differential equation solver to approximate the device behavior within computational limitations.



**Figure 5.13:** Simulated charge carrier concentration p+n in graphene vs back-gate voltage  $V_G$  at 10 mV source-drain bias. Graphene was modeled as a 50 nm x 50 nm x 1 nm layer with a 30 nm back-gate dielectric (eps\_SiO<sub>2</sub>), a 3 nm top dielectric (eps\_Al<sub>2</sub>O<sub>3</sub>), and a surface charge density of -10<sup>13</sup> cm<sup>-2</sup>. (a) Simulation with a 30 nm layer (eps=1) on top, and (b) with a 7 nm (eps=10<sup>3</sup>) and a 13 nm (eps=1) layer on top. The data was taken from the center of the graphene layer. The high-k dielectric layer on top of the negative surface charge reduces the Dirac voltage and narrows the Dirac cone<sup>[215]</sup>.

Qualitatively, the reduction of the gate voltage when adding a polarizable dielectric on the top was reproduced. However, the effect showed to be rather small (figure 5.13). Therefore, charge removal as a possible mechanism should also be considered. A simple

hopping of negative charges from the surface is rather unlikely considering the electron affinities of alcohol (-2.1 eV), water (-1.3 eV), and hydroxyl radicals (+1.8 eV). However, a chemical reaction might take place to withdraw charges. From desorption experiments of ethanol on hydrated alumina, it is known that diethyl ether and ethene form and evaporate from the surface when heated above 180°C and 230°C, [273] with  $H_2$  and  $H_2$ O as byproducts. Whether charges promote reactions and lead to partial charge removal at room temperature remains unclear, but the reduction of hole-doping could be an indication for it. The re-charging in  $N_2$  back to strong hole-doping would then have to follow the initial charging mechanism.

After the SURMOF growth, fewer hydroxyl groups are left because of the chemical reaction with the copper acetate that anchors the SURMOF to the alumina. If only a few charged -OH groups remain it would explain the close to zero doping and the high mobility that is observed after the SURMOF growth procedure. However, zero doping could also be a result of compensation of the remaining negatively charged -OH groups by positive charges. Unfortunately, the initial stages of the SURMOF growth remain elusive. In the ideal structure, there are 1.5 Cu-paddle-wheel-metal-nodes per nm<sup>2</sup>, nominally bound to one OH-group per one metal node. This would affect a smaller fraction of the original –OH group density. On the other hand, the appearance of positive charges during synthesis is not known either, although the existence of charged defects in related SURMOFs structures has been reported recently<sup>[274][275]</sup>. Screening and/or removal of positive charges could then lead to hole doping as observed for ethanol. At this point, a complete microscopic model cannot be provided that explains all observations, including the different behavior of the alcohols. It can be anticipated that in-situ/in-operando FTIR spectroscopy, targeted towards molecular species and processes at the interface, shall provide the necessary information to fill the voids. However, this has unfortunately not been possible during this work and remains to be explored in a future study.

## 5.5 Summary

In summary, a novel sensing principle by interfacing a MOF film with a graphene transistor is demonstrated. Given the countless variations of MOF films and the possibilities to chemically engineer the interface between MOF and GFET, the emergence of a whole new class of MOF/GFET sensors with tailored selectivity and sensitivity can be envisioned.

# **6 NCG for Sensing Applications**

Graphene has been extensively studied after its discovery in 2004 but suffered due to a lack of techniques for large-area synthesis and integration with current technology. On the other hand, NCG, another form of graphene is easy to synthesize on a large area independent of substrates and with low roughness (root mean square (RMS) below 1 nm)<sup>[99]</sup>. With nanometer crystallite size, good thermal and electrical properties, and high density of defects in form of GBs<sup>[2][276]</sup>, it has been utilized in different applications (chapter 2). NCG can be classified based on the number of layers as nanocrystalline graphene (1 layer), nanocrystalline multilayer graphene (2-10 layers), and nanocrystalline graphite (more than 10 layers)<sup>[277]</sup>.

This chapter starts with the synthesis techniques of NCG explored at low temperatures (600°C). Next, the NCG thin-film transfer method used in this work is explained. Further in the chapter, NCG film is characterized based on temperature and strain. Characterization is further done by Raman spectroscopy measured under strain. Finally, an application in the form of a moisture sensor utilizing an NCG-FET is exhibited.

## 6.1 Low-temperature NCG synthesis

There have been several reports to synthesize NCG and described in chapter 2. Techniques such as molecular beam epitaxy, radio frequency magnetron sputtering, pulsed laser deposition, CVD, and high temperature (1000°C) pyrolysis of organic photoresist have been used<sup>[98,111,134]</sup>. Raman spectroscopy is widely used for the characterization of such films to recognize fingerprints of C-C bond and crystallite size. G peak at 1600 cm<sup>-1</sup>, D peak at 1350 cm<sup>-1</sup>, and a broad 2D peak <sup>[278]</sup> are the fingerprints of an NCG film.

In general, the synthesis of NCG is done at higher temperatures (800-1000°C). The synthesis and the characterization of NCG have been reported in detail by Riaz et al. Summarizing the method by Riaz et al., a carbon source (photoresist S1805 from Microresist) was diluted in propylene glycol methyl ether acetate (PGMEA) based on the required thickness. Next, the solution was spin-coated onto the substrate of choice which can withstand high temperatures. The spin-coated film was then loaded in a vacuum furnace and the temperature was ramped up to 1000°C at 10°C min<sup>-1</sup> heating rate. The substrate was annealed for 10 hrs and cooled down to room temperature to

obtain synthesized NCG film. However, flexible substrates are not suitable for high-temperature synthesis. Therefore, a low-temperature synthesis process is needed for synthesis on flexible substrates.

In this work, flexible glass was used as a flexible substrate. It was acquired from Schott, Germany. The technical data sheet reports glass transition temperature of 714°C and a safe operation till 600°C. Hence, a 600°C synthesis process was followed up. Higher temperature synthesis allows increased  $sp^2$  hybridization of C-C bonds and a better electrical conductivity<sup>[279]</sup>. One of the techniques to measure hybridization is using Raman spectroscopy. The comparison of the intensity ratio of the D peak and G peak of graphene films can give qualitative insight into the levels of hybridization in the film. Different low-temperature routes were studied to achieve a good quality NCG film that can be applied for synthesis on glass substrates.

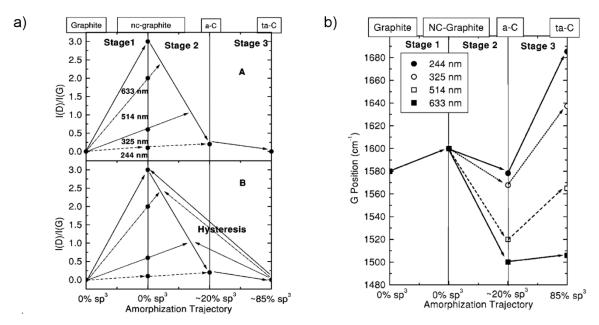
PMMA depolymerizes, and long chains are broken which eventually evaporate when heated at 420°C in a vacuum<sup>[280]</sup>. However, providing a metal capping layer allows the graphitization to occur. The advantage of using a metal as a capping layer is two-folds. First, the metals with melting points greater than graphitization temperature can remain in their pristine condition making it easier to remove. Secondly, the role of catalyst metals like copper has been studied for graphene synthesis at low temperatures<sup>[281]</sup>.

PMMA and S1805 were used as carbon sources for NCG synthesis. 5 substrates of 300nm SiO $_2$ /Si were cut of dimensions 10 mm X 10 mm. Two of them were spin-coated with PMMA (5% in Anisole) at 5000 RPM and the three with S1805 (diluted to 1:21 in PGMEA) at 8000 RPM. PMMA coated samples were post-annealed at 150°C and the S1805 coated samples at 110°C. One of each substrate from the two groups was covered by a capping layer of copper (30 nm) and the one from each group with aluminum (30 nm) using MBE. One of the substrates with S1805 film was used as a reference sample. The samples with metal capping layers were loaded in the vacuum chamber at  $10^{-6}$  mbar pressure and heated to  $600^{\circ}$ C at a  $10^{\circ}$ C min<sup>-1</sup> heating rate. The samples were allowed to heat at  $600^{\circ}$ C for 10 hours and then cooled down to room temperature. The sample without the metal capping layer (reference sample) was heated at  $1000^{\circ}$ C for 10 hours.

Raman spectra were recorded for all these samples and are plotted in figure 6.2a. The ratio of peak intensities of D and G mode is plotted in figure 6.2c.  $I_D/I_G$  was calculated by using integrated intensities of the D and G peaks. The values of all the ratios are in the range of 0.7-1.6. S1805 graphitization at 1000°C, which is the reference sample, has a ratio of 1.59. For 532 nm excitation wavelength, the ratio values for complete  $sp^2$  hybridization should be in the range of 2-3 (figure 6.1a). Hence, it can be concluded that there is approximately 10% of  $sp^3$  hybridization for the NCG synthesis at 1000°C.

Comparable amorphization exists for the condition Al\_PMMA (ratio = 1.46). For all the other conditions the  $sp^3$  hybridized carbon content was higher.

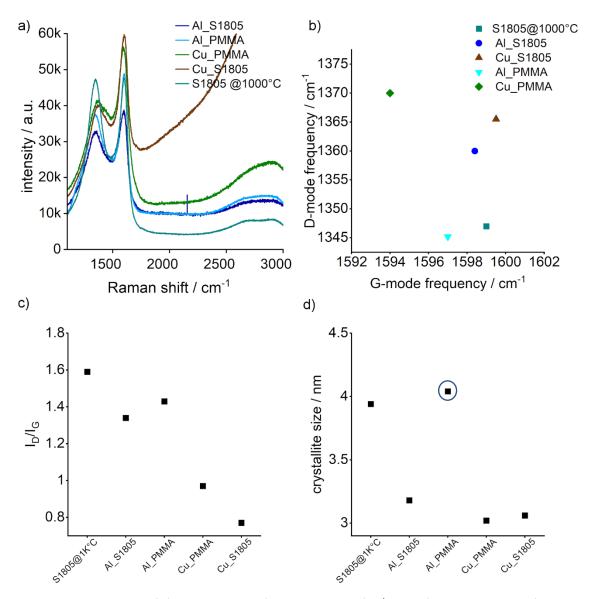
FWHM of D peak was used to calculate the crystallite sizes for different conditions by using the relation by Concado et al. as discussed in chapter  $2^{[148]}$ . The crystallite sizes for S1805@1K°C (3.94 nm) and Al\_PMMA (4.02 nm) are comparable (figure 6.2d). All the other synthesized films have smaller crystallite sizes. Lower  $I_D/I_G$  ratios and smaller crystallite sizes imply highly disordered graphene formed as in the case of Cu S1805.



**Figure 6.1:** a) Amorphization trajectory based on the intensity of ratio D and G peak and b) G peak position for different excitation wavelengths. Adopted from <sup>[282]</sup>.

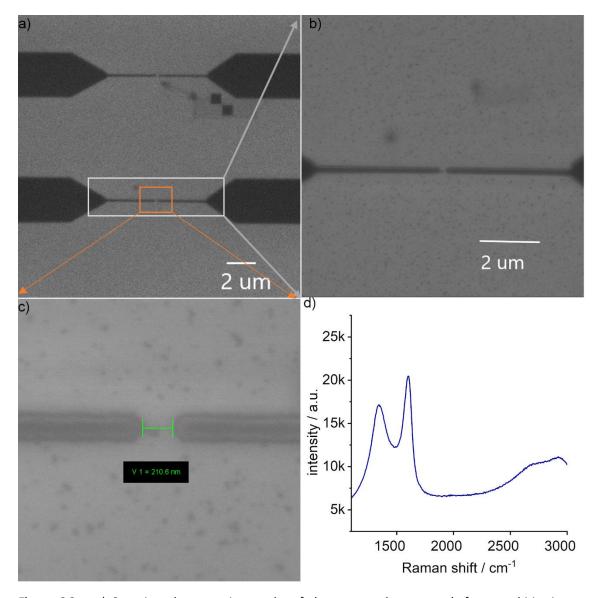
G and D peak positions have been plotted for all the conditions in figure 6.2b. The peak positions can also be linked to the percentage of  $sp^3$  amorphization present in the film. For NCG, the G peak is at 1600 cm<sup>-1</sup> independent of excitation wavelength as described by Ferrari et al. in figure 6.1b. Similar to  $I_D/I_G$  ratio and crystallite size, the G-peak position also confirms that Cu\_S1805 has the most  $sp^3$  amorphization compared to other synthesis conditions. S1805@1k°C has a peak position at 1599 cm<sup>-1</sup> and sample Al\_PMMA has a peak position at 1597 cm<sup>-1</sup> and therefore the least  $sp^3$  amorphization compared to other conditions. In summary, NCG synthesized using S1805@1k°C route had the least amorphization and crystal size of 3.94 nm. Nevertheless, synthesis route in preparation of sample Al\_PMMA demonstrates a promising synthesis technique at a lower temperature (600°C) with a comparable crystallite size (4.02 nm). This technique could be beneficial to grow NCG on substrates that cannot withstand higher

temperatures. However, the GBs density still remains high. Nevertheless, such a high defect density can be useful in some contexts like gas sensing and piezoresistivity<sup>[23][24]</sup>.



**Figure 6.2:** Comparison of a) Raman spectra b) D and G modes c)  $I_D/I_G$  and d) crystallite size  $L_a$  of the NCG samples fabricated under different conditions. The crystallite sizes have been calculated using Concado's relation utilizing the FWHM of D peak<sup>[148]</sup>. All the measurements were done using a 532nm laser with 100x objective (NA= 0.85) at 0.3 mW laser power.

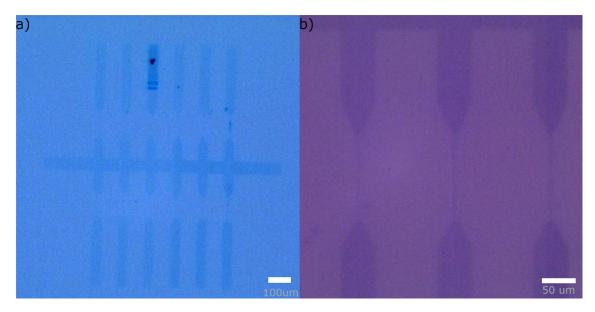
# 6.2 Patterning and graphitization of PMMA



**Figure 6.3:** a-c) Scanning electron micrographs of the patterned structure before graphitization at different magnifications. Structures as small as few hundred nanometers were achieved based on the required application d) Raman spectra of the graphitized film using 532 nm laser at 0.06 mW power with an integration time of 10 s.

PMMA is an electron-beam resist that is widely used for patterning and fabrication of nanoscale structures. It is chemical resistant and UV stable. Therefore, it can find its use in many applications like a protective coating, shatterproof windows, etc. In this work, an attempt has been made to first pattern and then synthesize NCG using PMMA as a carbon source. Duan et al. demonstrated the process to convert PMMA nanofibers into

graphene nanoribbons by in-situ e-beam electron radiation<sup>[283]</sup>. A similar technique has been used here to fabricate an NCG pattern with a potential application of electrodes for carbon nanotube transistors.

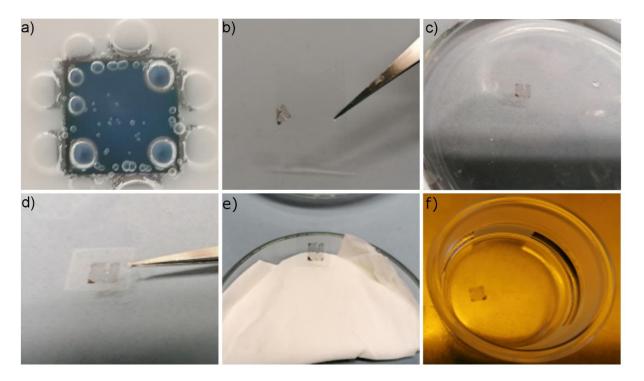


**Figure 6.4:** Optical micrograph of NCG film synthesized by cross-linking PMMA at a) 20x and b) at 50x magnifications.

PMMA A4.5 was spin-coated at 5000 RPM and post-annealed at 150°C on 300 nm SiO $_2$ /Si substrates. Next, the structure was patterned and exposed with a high dose of 8260  $\mu$ C cm $^{-2}$ . Due to the high dose, PMMA was cross-linked, and contrary to its behavior, does not dissolve in acetone or MIBK/IPA solution as shown in SEM images in figure 6.3a-c. The remaining PMMA was dissolved in acetone and the patterned structure was heated at 800°C for 2 hrs in  $10^{-6}$  mbar pressure to allow graphitization. Figure 6.3d shows the Raman spectra of heated film and confirms the synthesis of NCG.

Figure 6.4a, b shows the optical micrographs of the graphitized pattern on 300 nm  $SiO_2/Si$  substrate. Structures as small as 200 nm were successfully patterned which can be used to replace metal electrodes in CNT devices. This technique of device patterning and NCG synthesis can reduce several steps of electron beam writing and etching steps and make overall device fabrication easy. The quality of the synthesized film was not studied electrically and would be the vital next step to realize this technique. From Raman measurements, the crystallite size was calculated in the range of 3-5 nm which is similar to the synthesized NCG with the technique mentioned in the previous section. One of the demerits of this process is overexposure due to the proximity effect in e-beam lithography<sup>[284]</sup>, which leads to comparatively larger dimensions. Therefore, proximity correction of the large doses is needed to achieve precise results.

# **6.3** Transfer process



**Figure 6.5:** a) PMMA coated NCG/SiO<sub>2</sub> sample in 5M solution of NaOH/H<sub>2</sub>O b) PMMA/NCG film picked up using a transfer glass slide c) PMMA/NCG film in distilled water d) PMMA/NCG transferred on desired substrate e) drying in air f) PMMA lift-off in acetone.

Transferring 2D films onto the desired substrate without losing its properties is essential. Hence, an adequate process with immense control is significant. For graphene synthesis, metal substrates like copper is chosen by the research community<sup>[285][286]</sup>. However, application-dependent substrates are desired and a transfer process is inevitable to further investigate the synthesized film<sup>[287]</sup>. Few of the commonly observed defects appearing during transfer processes are cracks, wrinkles, and chemical or polymer residues used during the transfer process. Few ways these defects can originate are<sup>[288][289]</sup>

- Sacrificial substrate
- Etchant to dissolve the substrate
- Polymer transfer layer (for example PMMA)

The impurities generated are detrimental as they can dope graphene and create charge scattering sites<sup>[290]</sup>. As the film thickness is few nm, cracks can easily form and propagate due to applied mechanical stress during transfer and cleaning processes. To counter

these transfer issues, numerous good processes have been developed and reported in recent years. They can be categorized in following categories:

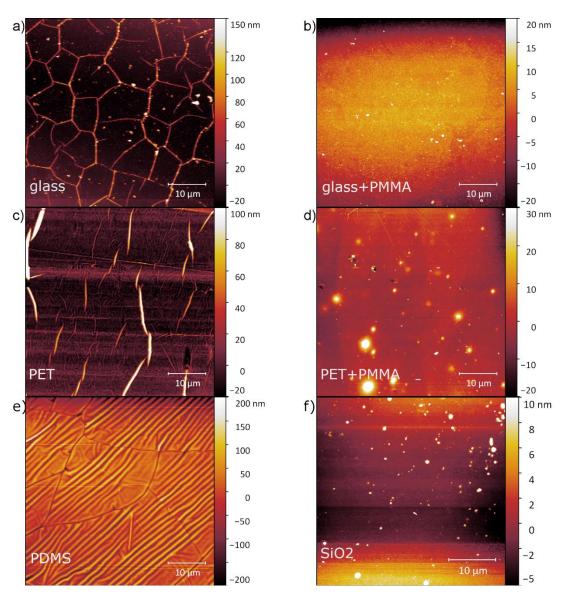
- a) Wet transfer: In the wet transfer process, the film remains in a liquid until the final transfer step. The grown film on a substrate is coated with a transfer layer. The growth substrate is then dissolved in an etching solution and the film (with transfer layer) is cleaned in an aqueous solution. The final step of transfer is performed in the cleaning solution. PMMA supported transfer of graphene grown on copper is the most extensively used transfer technique due to its straightforward approach and ease of chemical availability. But to prevent pdoping from PMMA, different transfer layers like naphthalene<sup>[291]</sup> and camphor<sup>[292]</sup> have been used because of their easy removal by sublimation with minimal residues. Gold as a transfer layer has also been used to prevent contamination from the organic polymers and demonstrated better quality graphene film compared to PMMA assisted transferred film[293]. Paraffin as a support layer for transfer provides two-fold benefits. First, there are no covalent interactions with graphene. Second, paraffin expands at very low temperatures (~40°C). This helps in avoiding wrinkles if the transfer is made in water which is at a slightly higher temperature. As paraffin expands, graphene is stretched, and wrinkles are flattened before transferring onto the substrate<sup>[290]</sup>. Removal of paraffin is easy because there are no chemical interactions with graphene.
- b) <u>Dry transfer:</u> This technique has an immense advantage over the wet transfer technique in terms of the level of contamination and defects induced in graphene. A lifting layer with low binding energy is used so it can be washed away using water. Molybdenum oxide is one of the examples of such a lifting layer and gives high-quality graphene film transfer<sup>[294]</sup>. Delamination of graphene from the copper substrate can also be achieved by a thermal release tape (TRT). Graphene on copper is allowed to sit in the water near the boiling point (90-95°C). This step forms a copper oxide between copper and graphene and allows easy delamination using TRT and with no effect on graphene<sup>[295]</sup>.
- c) <u>Support free transfer:</u> The first two methods are conventional methods to transfer graphene onto the target substrate and require support. However, the defects are inevitable in both of them. Thus, a process where the support layer can be avoided would be ideal. It reduces the number of steps involved and the cost incurred. One of the examples is by using an electrostatic generator, where the target substrate is charged and then used to pick copper/graphene. Copper is then etched away<sup>[296]</sup>. In another attempt, graphene is hot-pressed onto the target substrate to achieve a high-quality film<sup>[297]</sup>.

In this work, PMMA assisted wet transfer process was used to transfer NCG thin films onto different substrates. AFM images were then recorded to examine the quality of transferred NCG film on the target substrate.

NCG was grown on  $10 \times 10 \text{ mm}^2$ ,  $300 \text{nm SiO}_2/\text{Si}$  substrate (as discussed previously) using 1:4 diluted solution. The NCG-grown substrate was then spin-coated using A4.5 PMMA at 5000 RPM and post-baked at  $150^{\circ}\text{C}$  for 3 minutes. Simultaneously, a 5M solution of NaOH in water was prepared and kept on a hot plate at  $80^{\circ}\text{C}$ . The PMMA coated substrate was then put in the NaOH solution for  $\text{SiO}_2$  etching as shown in figure 6.5a. After the  $\text{SiO}_2$  etching, PMMA/NCG film floats on the top of the liquid. A clean transfer glass was used to pick up the floating film (figure 6.5b) and put it into distilled water (figure 6.5c) for 1 hour to dissolve the NaOH impurities. Water trapped between the transfer glass and the PMMA/NCG film prevents any damage to the NCG film. This process is repeated 3 times to remove any NaOH residues on the bottom part of NCG. Finally, the target substrate is used to fish the floating film (figure 6.5d) and allowed to dry (figure 6.5e) for 3 hours. On the dried PMMA/NCG film sitting on the target substrate,  $10 \, \mu$ l PMMA is gently drop cast and left in air for drying. This step drastically reduces the number of wrinkles and gives comparatively better films<sup>[298]</sup>. Finally, the substrate is put into acetone to remove PMMA (figure 6.5f).

PMMA is not a good polymer for its use in transfer processes as discussed earlier. If used, a strong solvent like acetic acid should be utilized to completely remove the residues of PMMA<sup>[299]</sup>. Therefore, to avoid PMMA, a paraffin-assisted transfer was explored as well. But, due to the requirement of higher temperature for SiO<sub>2</sub> etching and low melting point of paraffin, the process could not be completed till the final transfer step. One important observation to be mentioned is that 300 nm SiO<sub>2</sub>/Si substrates with NCG films synthesized at 1000°C, when etched, did not give continuous NCG films. However, NCG films synthesized at 800°C had a good quality and continuous film after etching. Note that it is just an observation based on numerous etching and transfer attempts made in our laboratory. The reason for this is not clear. Therefore, for transferring films, 800°C synthesis temperature was used.

AFM images were recorded for the films transferred on different substrates. An area of 50 um x 50 um was measured to analyze the quality of films transferred over a larger area. Figure 6.6a shows AFM of an NCG film transferred on glass using conventional PMMA supported transfer. The film consisted large density of wrinkles. Drop casting 10 ul of PMMA and allowing it to dry flattens the wrinkles as shown in figure 6.6b. Similar observations were observed for film transfer done on a PET substrate. Figure 6.6d shows a large number of particles due to the poor quality of PET substrate used. The substrate itself had many scratches and non-removable particles stuck on the surface. A good



**Figure 6.6:** AFM images of a) NCG film transferred on glass without drop-casting PMMA on a glass substrate b) with drop-casting PMMA drop on a glass substrate c) without drop-casting PMMA on a PET substrate d) with drop-casting PMMA on a PET substrate e) without drop-casting on a PDMS substrate f) with drop-casting on Si/SiO<sub>2</sub> substrate. All the images have been measured after the removal of the drop cast PMMA layer.

quality NCG film for transfer on glass (figure 6.6b) with few particles and wrinkles was confirmed by the AFM image. PDMS is not a good substrate for transferring a thin film onto it. The film contained a high density of wrinkles as shown in figure 6.6e. Figure 6.6f shows NCG film on Si/SiO<sub>2</sub> surface. PMMA residues are visible on the surface and can be removed with a heating step of 400°C. Wrinkles on Si/SiO<sub>2</sub> are also minimal. Therefore, it can be concluded that wrinkles are reliably eliminated using a top PMMA drop cast layer after the transfer step on the target substrate. However, PMMA residues are still

present when dissolved in acetone, and using different sacrificial polymer support or a strong solvent for PMMA could be beneficial to eradicate the issue. All the images reported in figure 6.6 have been recorded after removal of the drop cast PMMA layer.

## 6.4 Dependence of sheet resistance of NCG on temperature

Sheet resistance ( $R_s$ ) is a measure to express resistance for thin (sheet) films used out of convenience. It depends on the resistivity ( $\rho$ ) and thickness (t) of the material. It has the unit of  $\Omega$  sq<sup>-1</sup>. It can be altered by changing external environments like temperature<sup>[300]</sup> and strain<sup>[24]</sup>.

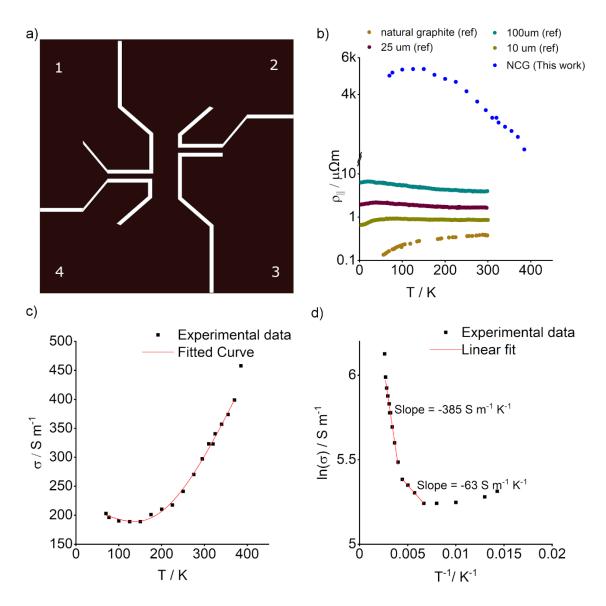
Mathematically, the sheet resistance is given by

$$R_s = \frac{\rho}{t} \tag{6.1}$$

There are several techniques to measure sheet resistance like the two-point, four-point, and Van der Pauw methods. These have been discussed in chapter 3. For the measurement of sheet resistance, in this case, the four-point or Kelvin method was used. The advantage of using the four-point method is, it directly measures the sheet resistance and is independent of contact resistances. Four contacts were made numbered 1-4 on the patterned structure as shown in figure 6.7a. The measurement scheme was made such that a constant current is applied across contacts 1 and 3 (main lines) and the voltage drop was measured across points 2 and 4 (sense lines). Resistance and hence, the resistivity of the device under test (DUT) of area 1 mm x 1 mm was calculated using the values of voltage drop and current applied using equation 6.1.

NCG for the device was synthesized by spin coating S1805 with a dilution of 1:4 in PGMEA on 300 nm SiO<sub>2</sub>/Si substrates. The sample was then heated to 1000°C for 10 hrs and then cooled down to room temperature. The thickness of the film was measured as 5 nm. Next, the structure (figure 6.7a) was patterned on the synthesized NCG. For patterning, NCG film was spin-coated with PMMA A4.5 at 5000 RPM and post-annealed at 150°C. The structure was written using e-beam and developed (MIBK: IPA in the ratio 1:3 for 30 s) and etched in RIE using oxygen plasma. The patterned substrate was wire bonded onto a substrate holder and loaded in the cryostat and pumped to 10<sup>-6</sup> mbar (refer Gaulke et al. for cryogenic setup details<sup>[99]</sup>). Liquid nitrogen was used to reach the temperature of 77 K and the temperature was controlled by the integrated heater. The starting temperature was 77 K. Measurements were done by sweeping current values from 100 nA to 200 nA with a step size of 1 nA. Lower values of current were used to

avoid any heating due to power dissipation in the channel which can heat the material and alter the actual temperature.



**Figure 6.7:** a) Schematic of structure used for the four-probe study of sheet resistance dependence on temperature. The white area indicates the etched area (no NCG) and the black area indicates NCG b) Dependence of in-plane resistivity on the temperature of an NCG film grown at 1000°C in comparison with data from ref<sup>[190]</sup>.c) Fitting of experimental data based on the model of variable range hopping (VRH) d) two different slopes for temperature indicating two activation energies of the processes involved.

The measurement shows the inverse dependence of sheet resistance of NCG on temperature. Similar results have been observed for thin pyrolytic graphite films, where the resistance first increases and then decreases with an increase in temperature.

Metallic behavior dominates at lower temperature and variable range hopping (VRH) at higher temperatures [301].

In figure 6.7b, in-plane resistivity of the measured sample with reference to data from a study done by Nakamura et al. [301] is plotted. Their study focuses on the dependence of resistivity of pyrolitic graphite sheets on temperature. Two resistivities, in-plane ( $\rho_{||}$ ) and out of plane ( $\rho_{\perp}$ ) were defined. Nakamura et al. inferred that with increasing thickness of graphite sheets, the in-plane resistivity increases. Also, for each thickness, the in-plane resistivity first increases with the temperature and reaches a peak. Further increase in temperature leads to a decrease in resistivity. Similar observations were recorded in the case of NCG in this work. The thickness of the film is 5 nm and the measured resistivity is in-plane resistivity. The peak lies at ~130 K. Two different regions in resistivity vs temperature plots indicate two different mechanisms. The first mechanism is similar to a metallic resistance which is dominated at lower temperatures. At higher temperatures VRH mechanism becomes dominant. The model is given for temperature below 150 K as [301]:

$$\sigma = \frac{1}{\rho_0 + AT} + \sigma_0^h exp\left(-\frac{T_0}{T}\right)^{\alpha} \tag{6.2}$$

In equation 6.2, the first term corresponds to the metallic mechanism and the second term corresponds to the VRH mechanism. This model agrees with the NCG data and fits as shown in figure 6.7c up to 400 K. For the fitting,  $\alpha = 1$  was used which corresponds to simple Arrhenius type conduction. All the other variables were kept as fitting parameters. To look closely at the activation energies in the VRH mechanism,  $ln(\sigma)$  was plotted against T<sup>-1</sup> as shown in figure 6.7d. Two linear regimes were found with different activation energies Ea, and therefore, two different VRH processes. Utilizing the slope of the linear fit lines, at lower temperature close to conductance dip, Ea was calculated as  $\sim$  5 meV. With the increase in temperature,  $E_a$  increases and was calculated as  $\sim$ 33 meV. Conductivity in NCG can be understood by the resistance model shown in figure 6.8. There are two VRH mechanisms (VRH<sub>1</sub> and VRH<sub>2</sub>) and one metallic process controlling the conductive pathway in NCG. All three mechanisms are in parallel connection. At a lower temperature (T < 130 K), VRH<sub>1</sub> and VRH<sub>2</sub> channels are highly resistive and therefore conductance in the material follows metallic behavior. With a further increase in temperature, VRH mechanisms start activating. However, the metallic channels become highly resistive and contribute negligibly to conductance at higher temperatures. For temperatures 225 K > T > 130 K, VRH₁ channels resistance decreases and is the main contributor towards conductance. Hence, more current flows through VRH<sub>1</sub> channels. Nevertheless, a second process (VRH<sub>2</sub>) starts contributing for T > 225 K.

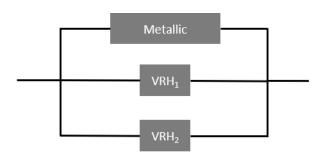


Figure 6.8: Conduction model in NCG

Table 6.1: Temperature-dependent behavior of conduction model in NCG

Temperature	Resistance of VRH <sub>1</sub> (E <sub>a</sub> =5 meV)	Resistance of VRH <sub>2</sub> (E <sub>a</sub> =33 meV)	Resistance of Metallic channel	Mode of conductance
T < 130 K	High	High	Low	Metallic
225 K >T> 130 K	Low	High	High	VRH <sub>1</sub>
T > 225 K	Low (Saturated)	Low	High	VRH <sub>1</sub> + VRH <sub>2</sub>
				$(VRH_1 < VRH_2)$

To see contributions from the VRH<sub>2</sub> process, two conditions should be fulfilled. First, VRH<sub>1</sub> remains low resistive, but the conductance does not increase further with increase in temperature and saturates (completely open). Second, more channels in the material follow the VRH<sub>2</sub> mechanism compared to VRH<sub>1</sub>, and are activated at this higher temperature. Therefore, a dominant contribution to conductance is observed for VRH<sub>2</sub>. With a further increase in temperature, VRH<sub>2</sub> becomes less resistive, and the overall conductance of the material increases. Table 6.1 summarizes the conduction model behavior at different temperature ranges.

# 6.5 Strain dependent measurements

Figure 6.9 shows the circuit diagram of the Piezoresistance measurement setup which was built in-house. The two stepper motors are attached at the two ends of the flexible substrate.

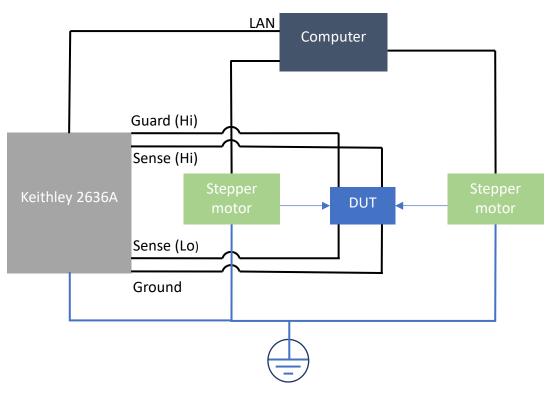
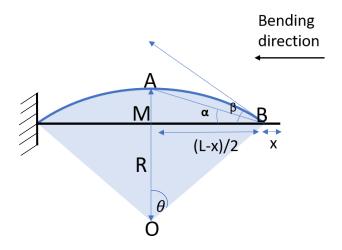


Figure 6.9: Circuit diagram of Piezoresistance measurement setup.

The connections are made from the DUT to Keithley SMUs using BNC cables. Keithley and the stepper motors are controlled by a python code using a computer. The building of bending setup is explained in detail in chapter 3.

In this section device design and fabrication, and the measured results are discussed. Schematics of different structures are explained later with the measurements. The applied strain on the film based on the movement of the stepper motor can be calculated as described in the next section.

#### 6.5.1 Strain calculation



**Figure 6.10:** Initial (black) and final (blue) curvature of the substrate when the stepper motor moves a distance x.

L and D are the length and thickness of the substrate respectively.

x corresponds to the distance moved by the stepper motor to impart strain to the film on top of the substrate. The value of x is the only known quantity, hence strain has to be calculated based on x. It is assumed that the thickness of the film t << D.

From figure 6.10, for a circular arc AB with center at O

$$\theta = L/2R \tag{6.3}$$

In  $\triangle$ MBO,

$$R\sin\theta = \frac{L-x}{2} \tag{6.4}$$

Binomially expanding  $sin\theta$ :

$$\sin\theta = \theta - \frac{\theta^3}{3!} + \frac{\theta^5}{5!} - \cdots \tag{6.5}$$

Combining equations 6.3, 6.4 and 6.5 gives,

$$2R\left[\frac{\left(\frac{L}{2R}\right)^3}{3!} - \frac{\left(\frac{L}{2R}\right)^5}{5!} + \frac{\left(\frac{L}{2R}\right)^7}{7!}\right] = x \tag{6.6}$$

The equation 6.6 is solved recursively using a MATLAB code which converges at a solution for R at each value of x.

Finally, strain is calculated using

$$\varepsilon = \frac{D}{2R} \tag{6.7}$$

#### 6.5.2 Piezoresistance in NCG

Piezoresistance is a property of materials wherein the electrical resistance of the material changes due to an externally applied mechanical stress. William Thomson (Lord Kelvin) was the first to report on change in resistance with strain in iron and copper in the year 1856. This material property has many applications like strain sensors, pressure sensors, etc. Graphene has already been used in such applications [302,303]. For flexible displays, graphene has an advantage due to its transparency and minimal piezoresistance effect [304]. However, the piezoresistance effect in graphene is small. The reason for such a diminished piezoresistance effect in graphene is that the displacement of the Dirac cone occurs in continuous k-space and there are no changes in band structure due to strain-induced lattice distortions up to 20% strain [305]. On the contrary, the band gap in carbon nanotubes can be controlled by uniaxial strain due to displacement of Dirac point in quantized k-space [306].

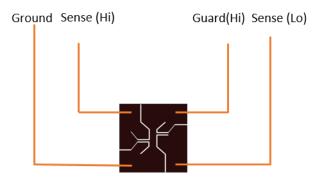
There are few reports to understand piezoresistance in graphene and it was concluded that grain boundaries could enhance this effect. The symmetric grain boundaries remain metallic whereas the asymmetric grain boundaries bandgap can be altered by providing an external strain<sup>[307]</sup>.

NCG intrinsically has a high density of grain boundaries and therefore, might have an enhanced piezoresistance effect. Zhao et al. have shown a tunable strain sensor based on nanographene by tailoring crystallite sizes<sup>[24]</sup>. Preliminary work done in our lab in this direction was reported by Riaz et al<sup>[99]</sup>. Higher GFs in comparison to graphene was demonstrated for NCG at lower strain values. However, the strain sensor was fabricated on rigid substrates, and therefore, higher strain values could not be achieved. In this work, piezoresistivity in NCG was studied on a flexible substrate to allow measurements at higher strain values. Therefore, Ultra-thin glass (AF 32 eco) from Schott as a flexible substrate was chosen due to certain merits. First, as reported in Schott datasheet, it can withstand higher temperatures for synthesis ( $T_g$ =740°C). Second, small thicknesses (~50 um) allow the glass to be highly flexible with a minimum radius of curvature up to 2 mm.

Third, the glass is transparent in the visible and IR region, suitable to study for transparent strain sensors.

The fabrication of the structure started with the growth of NCG by spin-coating the glass substrate by 1:4 diluted S1805 and heating the substrate. The heating temperature was kept at 600°C, below the glass transition temperature of the glass. NCG was successfully grown at 600°C on the glass, which was confirmed by Raman spectroscopy. But, the flexibility of the glass was completely impaired. The details have been discussed in the next section and a solution to this problem has been explored. Also, the wet transfer technique could not be used to transfer NCG film on ultra-thin glass substrate due to corrosion of glass substrate by water<sup>[308]</sup>.

Owing to the problems with the flexible glass, NCG film was then grown at  $800^{\circ}\text{C}$  on  $300 \text{ nm SiO}_2/\text{Si}$  and transferred on a flexible polymer substrate PET. Transfer steps are already discussed in the previous section. After the successful transfer of the film, the four-probe structure was patterned for sheet resistance measurements. The structure was patterned such that NCG itself provided a large area for contact (figure 6.11). Unfortunately, soldering was not possible as the solder does not stick on the NCG surface. Therefore, an alternative was required to make copper wire contacts on NCG. To solve this issue, a thermally curable silver epoxy (Elecolit 3653, Panacol) was used to make four contacts on the substrate. The epoxy has conductive silver particles and is liquid at room temperature. The epoxy solidifies at higher temperature (5 min at 150°C) and provides robust contacts for bending experiments. The other ends of copper wires were soldered to BNC connectors. BNC connectors were then connected to the Keithley 2636A SMUs as shown in the wire diagram (figure 6.11).



**Figure 6.11:** Wire diagram of the connections made from the substrate to Keithley 2636A for four-probe sheet resistance measurements.

A Keithley 2636A has two channels (A and B) allowing different configurations for measurements. Here, four terminals of one channel were used for four-probe

measurement. Guard (Hi) and the ground were connected at the ends of the device (DUT) where a constant current is maintained while measuring sheet resistance. Sense (Hi and Lo) lines were used to measure the voltage drop across the other two connected terminals. Thin NCG lines were used to connect the voltage drop points to ensure that the potential measured is not of an extended area. Four-wire mode (or sense mode) was selected from the settings in Keithley to perform four-point measurements. Here, that was automated using a python code. The movement of stepper motors was also controlled simultaneously using the code. The measurement scheme was such that for each movement of the stepper motor, the potential drop across the DUT was measured, whilst maintaining a constant current through DUT. The value of current was chosen to few nanoamps to avoid any heating due to power loss and change in temperature which can correspondingly change resistance.

Figure 6.12a shows the normalized change in resistance vs strain plot for NCG film patterned on PET substrate. The strain is varied to 2% and swept forward and backward. Therefore, it was confirmed that NCG is piezoresistive. As already discussed previously, resistance in NCG films is contributed by two resistances: grain resistance, and GB resistance. For the films grown in this study with 2-5 nm domain size, the resistance from GBs is a dominant contributor. Zhao et al. attributed the piezoresistive effect in NCG to change in GB resistance<sup>[24]</sup>. They inferred that charge carriers have to tunnel through grain boundaries to display conductance in NCG. Therefore, as the strain is increased, the tunneling distance between the two domains increases. This is similar to grains (conductor) and grain boundaries (polymer) in NCG.

In NCG used in this work, it was observed that GF changes from 24 to 52 at around 0.35% strain (figure 6.12a). Till 0.35%, increase in GF indicates that the tunneling distance increased (see Raman measurements in next section). Consequently, the change of resistance of the film with strain increased. Around 0.7% strain, GF drops to 39 indicating that the rate of change in resistance decreased. GF then drops further till 1.5% strain and again an increase of GF to 40 and then to 230 is observed. The complete behavior can be explained by the tunneling and tunneling + destruction model. Till 0.35% strain, the tunneling model is applicable where resistance is dependent mainly on tunneling distance.

The derivation of the tunneling and tunneling + destruction model has been discussed in Chapter  $2^{[127]}$ . To understand the results, it is included here briefly. Resistance in the material can be expressed by Simmons function<sup>[109]</sup> based on tunneling distance s as s and s as s and s and s and s and s as s and s a

$$R = \frac{V}{a^2 J} = \frac{8\pi hs}{3a^2 \gamma e^2} \exp(\gamma s)$$
 (6.8)

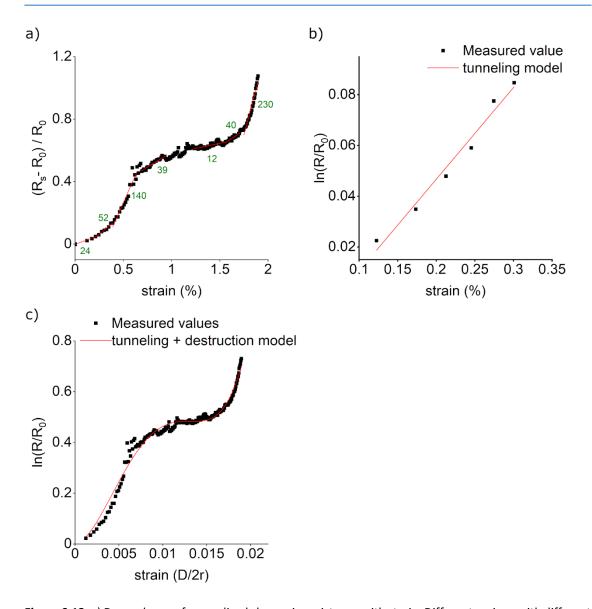


Figure 6.12: a) Dependence of normalized change in resistance with strain. Different regions with different GFs have been identified. b) In  $(R/R_0)$  vs strain curve fitted with tunneling model (red curve) c) tunneling + destruction model (red curve)

For smaller strain values < 0.35%, the tunneling model is applicable and follows the relationship between change in resistance and strain as

$$ln\frac{R}{R_0} = ln(1+\varepsilon) + \gamma s_0 \varepsilon \tag{6.9}$$

The equation was used to linearly fit the measured experimental data as shown in figure 6.12b and the value of  $As_0$ = 36.14 was extracted from the slope of the fitted line. Using empirical value of  $A = 10 \text{ nm}^{-1[131]}$ , initial tunneling distance  $s_0$ =3.6 nm was calculated.

Zhao et al. (3.4 nm) for nanographene films<sup>[126]</sup> reported comparable results of initial tunneling distance.

Further increase in strain (> 0.35%) causes the exponential decrease in the number of conductive paths, *N*, according to the following equation

$$N = N_0 exp[-(\alpha \varepsilon + \beta \varepsilon^2 + \gamma \varepsilon^3 + \lambda \varepsilon^4)]$$
 (6.10)

And the change in resistance as a function of strain can be rewritten as

$$ln\frac{R}{R_0} = ln(1+\varepsilon) + \alpha\varepsilon + \beta\varepsilon^2 + \gamma\varepsilon^3 + \lambda\varepsilon^4$$
 (6.11)

Using this equation, the complete measurement data up to  $\sim$ 2% strain was fitted as shown in figure 6.12c. The values of the fitting parameter are tabulated in table 6.2. The good fit with the model indicates that the mechanism of piezoresistance in NCG is indeed tunneling + destruction at higher values of strain.

**Table 6.2:** Fitting equation and parameters of tunneling + destruction model with errors used in figure 6.12.

Equation	$ln(1+\epsilon) + \alpha\epsilon + \beta\epsilon^2 + \Upsilon\epsilon^3 + \delta\epsilon^4$		
α	5		
β	1.55E4 ± 1.8E2		
Υ	-1.63E6 ± 2.5E4		
δ	4.6E7 ± 8.6E5		

In the reverse sweep of strain, the same trend is not observed as in the forward sweep. This could be due to the mechanical delay of the substrate in recovering from applied strain. Mechanical delay implies that even after removal of externally applied strain, the substrate takes an extended time (or delay) to recover. Additionally, the initial value of resistance (at zero strain in a forward sweep) was not achieved when returned to zero strain in the reverse sweep, indicating a plastic deformation of the substrate and/or fracture of the NCG at GBs which increases the resistance permanently.

FTIR measurements accompanied with strain will be the next step to get more insights into such dependence of GF on strain. In-situ study of the optical and electrical property of NCG based on external strain can help identify correlations between piezoresistivity and structural properties in NCG. Measuring the transmission spectrum in FTIR

microscope, Fermi level movement in NCG can be followed with respect to strain and subsequently, the piezoresistivity origin can be understood.

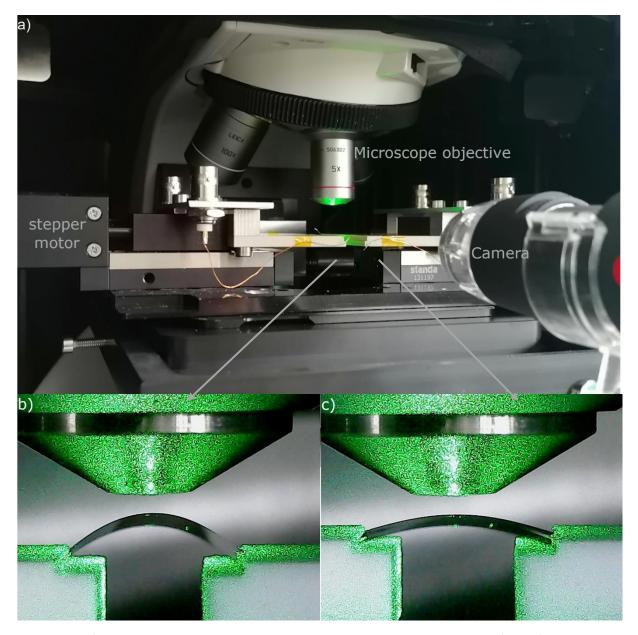
In addition, it was inferred from Raman measurements under the strain that at lower strain values (< 0.35%) there are no shifts in Raman D and G peaks. This supports the observation of piezoresistance measurement that the GBs move apart with strain and there is no stretching of C-C bonds within a domain (grains) in NCG. Therefore, an increase in the tunneling distance between two grains is the main contributor of piezoresistance in NCG for strain values less than 0.35%. Consequently, there is no distortion of the hexagonal lattice.

One point to note is that GF is dependent on initial tunneling distance  $s_0$  (equation 2.36). Therefore, to fabricate a highly sensitive piezoresistor,  $s_0$  should be maximized and not increase the initial resistance. At higher temperatures, there is a higher nucleation density of nanographene grains<sup>[24]</sup> in the PECVD process of NCG growth. This reduces the grain size but the tunneling distance increases. By utilizing the property of controlling initial tunneling distance, Zhao et al. fabricated a sensitive tunable GF piezoresistor<sup>[24]</sup>.

#### 6.5.3 Raman spectroscopy measured under strain

Raman spectroscopy is a non-destructive technique to gain useful information on carbon-nano systems. The peak position, intensity, and FWHM of G, D, and 2D bands are used to extract various information. It is used to determine the doping level in graphene, the stress in the film, the number of layers, disorders or defects in the film, and many more<sup>[309][140][310]</sup>. Details of Raman spectroscopy have been discussed in chapter 3. This section focuses on studying Raman spectroscopy of NCG film measured under applied strain.

Due to issues with ultra-thin glass, NCG films were transferred on polymer substrates. However, the transferred film on polymer substrates had a very large background and no Raman signal from the film was resolved. Water and high temperature (600°C) were detrimental to flexible glass; therefore, another approach was needed to grow NCG film and keeping the flexibility of the glass unimpaired.



**Figure 6.13:** a) Two-point bending setup compatible with Renishaw in-Via microscope. b) and c) Raman measurements at different strain values using 532nm laser at 0.3mW with 10s integration time.

#### 6.5.3.1 Process induced loss of flexibility of ultra-thin glass

Looking at the production process of flexible glasses, it is understood that the glass is under immense compressive stress. At high temperatures, as the atoms become more mobile, the stress in the glass can be easily released which reduces the flexibility. On the other hand, water corrodes glass and also affects its overall flexibility<sup>[308]</sup>. Therefore, the growth of NCG at 600°C or transferring NCG film in aqueous media was not possible. One possible solution could be to use an encapsulating material for glass which has a small thermal expansion coefficient and inhibits the expansion and release of stress at a

higher temperature. An important point to note here is that the side used for cutting the substrates using a diamond cutter should be always on the compressive side while bending. This is because cracks are generated while scratching and cutting the substrate and may propagate to premature failure of glass if kept on tensile side.

Graphite and graphene have negative thermal expansion coefficients<sup>[311,312]</sup> and are resistant to chemicals. The negative thermal coefficient of NCG allows keeping the inherited cracks in the glass intact. Therefore, encapsulating glass with NCG was deemed reasonable. Hence, the glass substrate was coated on both sides with S1805 and heated at 600°C for 10 hrs. This allows the NCG film to grow on both sides of the glass covering the edges of the 10 mm x 10 mm substrate. The benefit of this NCG film is three-fold. First, due to its negative thermal expansion coefficient, it prevents the release of stress present in the glass. Second, the film protects the glass from any corrosion from water if the transfer is required on glass in an aqueous media. Third, the NCG fills in the cracks present at the edges and reduces its propagation during the bending of the substrate.

To investigate the effect of the NCG layer on the flexibility of ultra-thin glass, glass substrates were single side coated, double side coated, and compared with uncoated glass samples. All of them were subjected to similar conditions of temperature at 600°C for 10 hrs. The nominal minimum radius of curvature that can be achieved is approximately 2 mm for the flexible glass from Schott (AF 32 eco). The minimum radius of curvatures achieved for different conditions was compared and has been tabulated in table 6.3.

Table 6.3: Comparison of the radius of curvatures for different processes of coating ultra-thin glass

Substrate	Uncoated	Single side NCG	Double side NCG
Radius of curvature (mm)	8.1	7.6	5.3

The results tabulated in table 6.3 show that the substrate with double side coated NCG was able to achieve the smallest radius of curvature compared to the other two samples for the same thickness of NCG on one side. The uncoated glass substrate flexibility was impaired completely at 600°C. Therefore, it confirms that the NCG film reinforces the glass substrate.

Next, to understand the role of the thickness of NCG, different thicknesses of NCG were grown with similar conditions, and the minimum radius of curvature achieved by them was compared.

Table 6.4: Comparison of the radius of curvatures for two thicknesses of S1805 resist

Dilution	1:10 at 8000 RPM	1:4 at 8000 RPM
Radius of curvature (mm)	5.3	4.1

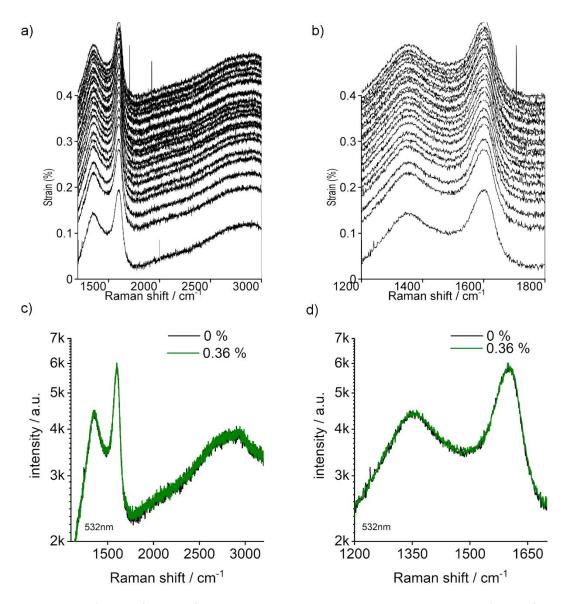
From table 6.4, it may be concluded that with a thicker film (1:4) of NCG encapsulating the glass, a smaller bending radius was achieved compared to a thinner film. This can be attributed to the fact that a thicker NCG film provides a stronger reinforcement. Further increase in thickness may allow reaching the limit of bending and could be beneficial for use in the synthesis of film at higher temperature (600°C) without losing the flexibility of the substrate.

For Raman measurements, the bending setup was modified to be used with the inVia Renishaw Raman microscope (figure 6.13a). The NCG films for the Raman study were synthesized by spin coating 1:4 resist at 8000 RPM for 30s on both sides and heated at 600°C for 10 hrs. The bending setup was attached to the Raman microscope and the sample was loaded onto it.

Figure 6.13b-c shows two instances of Raman measurements with laser light on and film under strain. The Raman measurements were recorded for each strain value and are plotted as a waterfall plot in figure 6.14a-b. No shifts in Raman G and D peaks are observed as the value of tensile strain is increased from 0% to 0.39%. Figure 6.14c-d is shown as zoom-in of the spectra in figure 6.14a-b, with focus on D and G peaks plotted versus y-scale in logarithmic scale. The 2D peak of NCG is broad and difficult to observe any shifts. It should be emphasized that there is no probable slip movement between the substrate and the grown film. The observation needs a microscopic picture of the NCG film to be addressed.

GBs and TJs are the most common defects present in nanocrystalline graphene. They are comprised of pentagon-heptagon or five-seven defects<sup>[307]</sup>. Stress is concentrated on these defects. Therefore, the fracture originates at these defects when an external strain is applied to the film and may propagate intergranular or intragranular. The number of such defect scales with parameter L²/D² where L is the length of the NCG strip and D is the grain size<sup>[2]</sup>. NCG in this work has a grain size between 2-5 nm and therefore possesses a huge number of GBs and TJs defects. This implies the density of such stress concentrators is huge and the strength of the film is diminished. Another parameter is strain rate that needs to be considered. The survival probability of the film reduces with decreasing strain rate. This is because, with a lower strain rate, thermal nucleation of fracture at more defects is possible, a similar concept to dislocation nucleation in plastic

materials<sup>[313][314]</sup>. Hence, based on the background, two reasons could lead to such type of behavior of NCG:



**Figure 6.14:** a) Water fall plot of complete Raman spectra with varying strain values b) Waterfall plot of Raman spectra till 1700 cm<sup>-1</sup> for clarity in showing D and G peaks c) and d) Comparing Raman spectra for strain values 0% (black) and 0.36% (green) on a logarithmic y-scale.

1) Due to the large density of defects present, the strength of the film is reduced drastically. Accordingly, when the tensile strain is applied to the film, rupture and crack propagation through GBs occur before phonon hardening or softening. This suggests that the strain is not imparted to the lattice and the energy is utilized in cracks generation at the defects. Since the Raman G peak measures the vibrational frequency of phonons and with no change in vibrational frequency

- no shift in peak is observed. In addition, from piezoresistance experiments, it was inferred that for lower values of strain (0.3 0.4%) the distance between the grains (at GBs) increases.
- 2) Strain rate plays a crucial role in determining nucleation of fracture at the defect sites and hence the overall fracture of the film. A lower strain rate can lead to a premature fracture of the NCG film. Fracture at GBs or TJ occurs as soon as the defects become unstable<sup>[314]</sup>. Therefore, in this case, a possible fracture could initiate due to a lower strain rate and lead to fracture while straining the film.

## 6.6 NCG field-effect transistors as a humidity sensor

NCG, as already discussed, is a highly defective graphene film. Nevertheless, it finds its way in many applications as sensors and FETs. There have been reports showing the increased potential of ions and gas sensing<sup>[23,315]</sup>. With good electrical properties and high transparency, NCG has shown a promising application in photodetectors<sup>[25]</sup>. In this section, an ionic liquid gated NCG field-effect transistor was fabricated, gated by the top ionic liquid, (IL) to demonstrate its potential application as a humidity sensor. IL can manifest high capacitances due to the formation of an electrical double layer (EDL) as shown in figure 6.15.

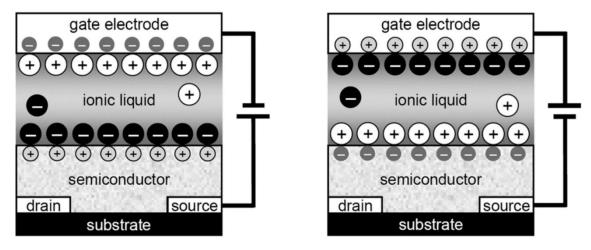


Figure 6.15: EDL formation while gating in ionic liquid, adapted from [316].

The EDL comprises holes in the channel and electrons in ionic liquid and vice versa depending on the gate configuration. Large capacitance values allow high-density charge carriers injection compared to a 300 nm  $SiO_2^{[317]}$ . These devices can achieve fast

operation of FETs. The ultra-thin (1 nm) EDL of the IL gate permits the low voltage operation of such devices.

For fabrication of NCG-FET, 1 nm thick NCG film was grown on 300 nm Si/SiO<sub>2</sub> using the technique discussed previously. Next, PMMA was coated and markers were patterned by e-beam lithography. Tungsten markers were then deposited using sputtering (45s). In the next step, NCG strips of dimensions 100 um x 1 um were patterned, and NCG was etched from the undesired surface using oxygen plasma. For the third and last step, electrodes were patterned and 45 nm thick tungsten was deposited by sputtering (45s). The active device area was 5 um x 1 um. 5 layers of Lithium per-chlorate (IL) were printed on top of the FET devices using an inkjet printer. The device structure with printed ionic liquid is shown in figure 6.16a. For gas sensing experiments, the devices were wire bonded onto the chip carrier as already discussed in previous chapters (Chapter 5).<sup>[3]</sup> Distilled water was filled into the bubbler and nitrogen was bubbled through water. The rate of flow of nitrogen in the bubbler was kept low to allow saturation with water molecules. Another line of flow of dry nitrogen was kept as the reference. Different levels of water saturation were achieved by mixing these two lines in different ratios.

A small gate voltage sweep window (-1 V to 1 V) was used to prevent the breakdown of the IL gate. In the transconductance curves (figure 6.16b, black curve), it was observed that there is no doping on the NCG channel and the conductance minima (Dirac point) is close to 0 V. At the Dirac voltage, the Fermi level coincides with charge neutrality point of graphene at the K point of the graphene band structure. If the Dirac voltage is zero, it implies the NCG channel is undoped. The devices were then exposed to water-saturated nitrogen mixed with dry nitrogen in ratio 1:1. The green curve in figure 6.16b shows the transconductance characteristics in presence of a 1:1 gas mixture. The Dirac voltage moves beyond 1 V, and due to limitations of ionic liquid, could not be measured. This indicates that the NCG channel was p-doped. In the humid nitrogen environment, water gets adsorbed onto the IL and p-dopes the channel. This in turn shifts the Dirac voltage to positive values and the conductivity of the channel at zero gate voltage increases. The devices were then exposed to different concentrations of water to investigate their sensitivity. Figure 6.16c shows the response of the FETs at different water concentrations at  $V_G = 0$  V and  $V_{SD} = 0.1$  V. At 1:3 mixture, devices are non-responsive and a flat line was observed. For the 1:1 mixture of dry and humid nitrogen, the device response showed a 40% change in conductance. Response for water-saturated nitrogen was maximum and a 400% change in conductance was observed.

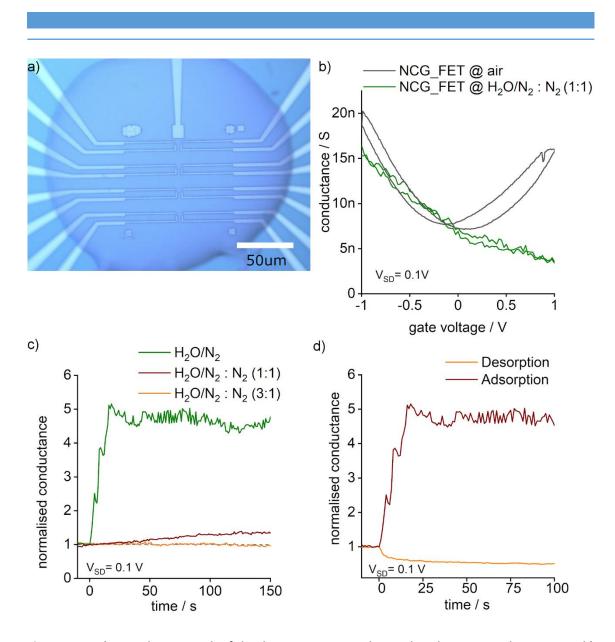


Figure 6.16: a) Optical micrograph of the device structure with ionic liquid gate printed as top gate b) Comparison of transconductance curves of the device in the air (black) and a mixture of  $N_2$  and  $H_2O/N_2$  (1:1) (green) c,) Device response vs water concentration at  $V_G = 0 \text{ V}$  d) Time scale showing different rates of adsorption and desorption of water molecules at  $V_G = 0 \text{ V}$ .

To further investigate the response, adsorption and desorption experiments were done. Figure 6.16d shows adsorption and desorption curves at room temperature and  $V_g = 0 \text{ V}$ . Adsorption was quick and takes few seconds to reach the maximum conductance for completely saturated nitrogen with water. For resetting the devices, the desorption rate should be faster. However, in this case, desorption in completely dry nitrogen was slower. Hence, a mild heating step should be required to bring devices back to their original state. It can be achieved by performing high bias current annealing, similar to the method discussed in chapter 5. However, current annealing was not studied for

these devices. Nevertheless, NCG gated with IL demonstrated a potential application as a moisture sensor. This device structure can also be exploited for fabricating different gas sensors, for example, a CO<sub>2</sub> or ammonia gas sensor.

## 6.7 Summary

In conclusion, in this chapter, different routes for NCG synthesis were discussed at low temperatures (600°C). PMMA as a carbon source was used for NCG synthesis via cross-linking at a high dose e-beam and subsequent heating. Next, the transfer technique was discussed and the problems associated with it and solutions were developed. In the characterization, temperature and strain-dependent studies were done on NCG. Raman as a probing technique with strain was applied to gain insights into piezoresistance characteristics of NCG. An ionic liquid gated NCG-FET was fabricated and its usage as a humidity sensor was explored.

# 7 Conclusion and Outlook

#### **Conclusion**

In summary, devices based on carbon nano-systems were studied. The prime objective of the thesis was to improve the device's performance. First, the long-term problem of hysteresis in CNTs was encountered and a possible solution was explored. Second, molecule-specific sensitivity in graphene FET was demonstrated by utilizing a porous structure of SURMOF. Third, NCG was then investigated as a material and its utilization in different device applications was explored.

In chapter 4, the mechanism of hysteresis in CNT-FETs was studied and a possible route was explored to solve the long-term prevalent issue. Therefore, a large area h-BN was chosen as the substrate. The direct growth synthesis route on the dielectric substrate was explored to grow h-BN using borazine as a precursor. CNT-FETs were fabricated on the h-BN and hysteresis in the transconductance curves were studied and compared with the FETs fabricated on SiO<sub>2</sub>/Si substrate. It was found that the main contributor is water molecules in the environment and therefore a hydrophobic film Teflon was required on the top of the channel. Utilizing this polymer film to encapsulate CNT and CNT-metal contacts completely eradicated hysteresis, after a waiting time of several days. The time scale of the reduction of hysteresis suggested a diffusion process of water molecules.

In chapter 5, a novel concept of sensing molecules was demonstrated using GFETs and SURMOF. The sensitivity of graphene to detect changes in its surrounding was the tool for sensing. The problem of selectivity in GFET was resolved by growing SURMOF on top of GFET. With the help of shifts in Dirac voltage in the transport data, it was shown that without SURMOF, GFET reacts with all the alcohols and as well as water, but after growing SURMOF over the GFET, it becomes selective to just ethanol. In addition, Raman spectroscopy was performed to understand and give insights into the doping and strain levels when exposed to different alcohols. Raman and transport data implied that both methanol and ethanol go at the interface of graphene and SURMOF, but only ethanol provides global doping detected by the shift in the Dirac voltage in the transport.

In the final experimental chapter, chapter 6, NCG was explored. The investigation starts with a low temperature (600°C) synthesis route compared to the conventional high temperature (1000°C) technique by using a metal capping layer. It was found that NCG

film with comparable crystallite size can be synthesized at lower temperatures by using a capping layer of metal. Also, it was possible to graphitize PMMA if encapsulated with metal, which is otherwise not possible. Next, the thin-film transfer technique was demonstrated by using PMMA as a support layer. Problems encountered and possible solutions were discussed. NCG was then characterized based on temperature and strain. NCG sheet resistivity showed decreasing behavior with an increase in the temperature. Variable range hopping mechanism as possible charge transfer technique was deduced. Next, piezoresistivity in the NCG thin films was studied. For this study, a piezoresistance setup was built in-house using two stepper motors and a Keithley. Piezoresistance in NCG was explained by a tunneling and destruction model similar to a matrix of conductive nanoparticles dispersed in an insulator polymer separated by a tunneling distance. Further investigation with Raman spectroscopy measurement based on strain did not show any peak shifts as a function of strain. This suggested that the vibrational frequency of C-C bonds does not change for NCG with strain and only the grains move apart at the grain boundaries. Further explaining the versatility of NCG, an application as a moisture sensor was exhibited.

#### **Outlook**

In chapter 4, synthesized h-BN was amorphous and had rough morphology due to the absence of catalyst and low (1000°C) synthesis temperature<sup>[318]</sup>. This limits its application as a gate dielectric for 2D materials. To improve the crystallinity of the film, h-BN synthesis should be done at temperatures higher than 1500°C or in presence of a metal catalyst<sup>[319]</sup>. Therefore, in relation to the h-BN growth process used in this work, further investigation needs to be done to achieve a high-quality 2D film of h-BN with a controlled growth rate.

Hysteresis in carbon nanotube transistors can be eliminated if the environment around the tubes is free from charge traps<sup>[190]</sup>. Hence, providing an encapsulant layer at the top and a charge trap-free layer at the bottom makes the environment charge-free, as already discussed in chapter 4 of the thesis. However, coating the polymer layer in an ambient environment leads to the trapping of water molecules which is not desirable. Hence, coating the nanotube devices in a vacuum or an inert atmosphere could prevent the trapping of water. Furthermore, encapsulating nanotubes from both the bottom and top by growing h-BN could be an alternative approach.

Findings in chapter 5 of the thesis explained a novel gas sensing technique which is exciting and opened up a whole new class of sensors. However, the sensor could not achieve lower detection levels (in ppb) due to the absence of off-state in graphene. Therefore, combining SURMOF with the material possessing a distinct off-state would

enhance the performance and lower the limits of detection. For example, CNTs could be the material to be tested by replacing graphene. Next, to understand the mechanism of sensing in more depth, FTIR measurements combined with molecules adsorption and desorption need to be done.

In chapter 6 NCG synthesis routes at low temperatures (600°C) was explored. Although, achieving a synthesis temperature to facilitate the growth of NCG directly on flexible substrates would be desirable. Strain-dependent sheet resistance measurements confirmed piezoresistive property in NCG. The results were explained using a tunneling and destruction model<sup>[24]</sup>. However, an in-depth study is required to understand the complete mechanism. In-situ FTIR measurements in the infrared region would give an insight into how the doping and defect levels change with strain. With a combination of the electrical and optical measurements, the correlation between the structure of NCG and piezoresistivity could be established.

# 8 List of abbreviations

2DEG: 2 dimension electron gas

AFM: Atomic force microscopy

ALD: Atomic layer deposition

**CNT**: Carbon nanotubes

CVD: Chemical vapor deposition

**DEP**: Dielectrophoresis

**DUT**: Device under test

EBL: Electron beam lithography

EDL: Electrical double layer

EDX: Energy-dispersive X-ray

FEM: Finite element method

FET: Field-effect transistor

FWHM: Full width at half maximum

GB: Grain boundary

GF: Gauge factor

GFET: Graphene field-effect transistor

IL: Ionic liquid

lbl: Layer-by-layer

MBE: Molecular beam epitaxy

MFC: Mass flow controller

MIBK: Methyl iso butyl ketone

MOF: Metal-organic frameworks

NCG: Nanocrystalline graphene

PECVD: Plasma-enhanced chemical vapor deposition

PEEK: Polyether ether ketone

PGMEA: Propylene glycol methyl ether acetate

 ${\bf PMMA: Polymethyl methac rylate}$ 

PTFE: Polytetrafluoroethylene

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PVC: Polyvinyl chloride

RDS: Rate determining step

RMS: Root mean square

SAM: Self-assembled monolayer

SAW: Surface acoustic wave

SEM: Scanning electron microscropy

SERS: Surface-enhanced Raman scattering

SMU: Source measure unit

STM: Scanning tunneling microscope

SURMOF: Surface-mounted metal-organic frameworks

SWCNT : Single-walled carbon nanotube

TJ: Triple junction

TRT: Thermal release tape

VC-SEM: Voltage-contrast scanning electron micrography

VRH: Variable range hopping

XPS: X-ray photoelectron spectroscopy

XRD : X-ray diffraction

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